



Cisco UCS C245 M8 Rack Servers



Overview

Q: What did Cisco® announce on February 6, 2024?

A: Cisco announced the Cisco UCS C245 M8 Rack Server based on AMD EPYC™ 9004 Series Processors.

Q: Are these servers quotable and orderable?

A: Customers may get quotes from their partners or Cisco sales team starting in early February and place orders in May 2024.

Q: Are you announcing the End-of-Life (EOL) of the C225 and C245 M6 servers?

A: There are no plans to EOL these servers. AMD recently extended the lifecycle of the AMD EPYC 3rd Gen Processors and plans to continue shipping these popular CPUs through CY2026.

Q: Will there be either a UCS C225 M8 Rack Server or a UCS X215c M8 Compute Node?

A: Yes, both those servers will be quotable mid-calendar 2024.

Cisco UCS C245 M8 Rack Server

Q: What is the C245 M8 Rack Server?

A: The C245 M8 is a 2RU, two-socket server supporting 4th Gen AMD EPYC CPUs and is designed to support the 5th Gen AMD EPYC CPUs.

Q: How many cores do the 4th Gen AMD EPYC processors support?

A: The 4th Gen AMD EPYC processors support from 16 to 128 cores depending upon the model.

Q: What are some of the benefits of UCS C-Series C245 M8 server with AMD EPYC™ 4th Gen CPUs?

A: Supporting double the number of cores per socket than AMD-based M6 servers and delivering up to 2.8X more performance, compute-intensive applications will see significant performance improvements and will reap other benefits like power and cost efficiencies.

Q: How much memory is supported?

A: Up to 6 TB of DDR5 memory with speeds up to 4800 MT/s is supported.

Q: How many drives are supported?

A: Up to 24 Small-Form-Factor (SFF) front-loading hot-pluggable drives—NVMe/SAS/SATA—and up to four additional rear drives.

Q: What network adapters are supported?

A: Cisco UCS 15000 Series Virtual Interface Cards (VICs) and a variety of 3rd-party adapters are supported.

Q: How is this server managed?

A: The server can be managed three ways: Cisco Intersight®, Cisco UCS® Manager, or with the Cisco Integrated Management Controller (IMC).