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/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.1, November 17th, 2003

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://www.ietf.org/rfc/rfc1950.txt> (zlib format), [rfc1951.txt](http://www.ietf.org/rfc/rfc1951.txt) (deflate format) and [rfc1952.txt](http://www.ietf.org/rfc/rfc1952.txt) (gzip format).

*/

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README for release 6b of 27-Mar-1998

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DOCUMENTATION

ROADMAP

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This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
RELATED SOFTWARE Other stuff you should get.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:

install.doc How to configure and install the IJG software.
usage.doc Usage instructions for cjpeg, djpeg, jpegtran,
 rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info as usage.doc).
wizard.doc Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.

Programmer and internal

documentation:

libjpeg.doc How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.doc Overview of the JPEG library's internal structure.
filelist.doc Road map of IJG files.
coderules.doc Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

OVERVIEW

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This package contains C software to implement JPEG image compression and decompression. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and gray-scale images.

JPEG is intended for compressing "real-world" scenes; line drawings, cartoons and other non-realistic images are not its strong suit. JPEG is lossy, meaning that the output image is not exactly identical to the input image. Hence you must not use JPEG if you have to have identical output bits. However, on typical photographic images, very good compression levels can be obtained with no visible change, and remarkably high compression levels are possible if you can tolerate a low-quality image. For more details, see the references, or just experiment with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. For legal reasons, we are not distributing code for the arithmetic-coding variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the library if not required for a particular application. We have also included "jpegtran", a utility for lossless transcoding between different JPEG processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the

REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

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The Unix configuration script "configure" was produced with GNU Autoconf. It is copyright by the Free Software Foundation but is freely distributable. The same holds for its supporting scripts (config.guess, config.sub, ltconfig, ltmain.sh). Another support script, install-sh, is copyright by M.I.T. but is also freely distributable.

It appears that the arithmetic coding option of the JPEG spec is covered by patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic coding cannot legally be used without obtaining one or more licenses. For this reason, support for arithmetic coding has been removed from the free JPEG software. (Since arithmetic coding provides only a marginal gain over the unpatented Huffman mode, it is unlikely that very many implementations will support it.) So far as we are aware, there are no patent restrictions on the remaining code.

The IJG distribution formerly included code to read and write GIF files. To avoid entanglement with the Unisys LZW patent, GIF reading support has been removed altogether, and the GIF writer has been simplified to produce "uncompressed GIFs". This technique does not use the LZW algorithm; the resulting GIF files are larger than usual, but are readable by all standard GIF decoders.

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REFERENCES

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We

highly recommend reading one or more of these references before trying to understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is Wallace, Gregory K. "The JPEG Still Picture Compression Standard", Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44. (Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PostScript file containing a revised version of Wallace's article is available at <ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz>. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics) omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...

The best full description of JPEG is the textbook "JPEG Still Image Data Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published by Van Nostrand Reinhold, 1993, ISBN 0-442-01272-1. Price US\$59.95, 638 pp. The book includes the complete text of the ISO JPEG standards (DIS 10918-1 and draft DIS 10918-2). This is by far the most complete exposition of JPEG in existence, and we highly recommend it.

The

JPEG standard itself is not available electronically; you must order a paper copy through ISO or ITU. (Unless you feel a need to own a certified official copy, we recommend buying the Pennebaker and Mitchell book instead; it's much cheaper and includes a great deal of useful explanatory material.) In the USA, copies of the standard may be ordered from ANSI Sales at (212) 642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI doesn't take credit card orders, but Global does.) It's not cheap: as of

1992, ANSI was charging \$95 for Part 1 and \$47 for Part 2, plus 7% shipping/handling. The standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

Some extensions to the original JPEG standard are defined in JPEG Part 3, a newer ISO standard numbered ISO/IEC IS 10918-3 and ITU-T T.84. IJG currently does not support any Part 3 extensions.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department
C-Cube Microsystems, Inc.
1778 McCarthy Blvd.
Milpitas, CA 95035
phone (408) 944-6300, fax (408) 944-6314

A PostScript version of this document is available by FTP at <ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz>. There is also a plain text version at <ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz>, but it is missing the figures.

The TIFF 6.0 file format specification can be obtained by FTP from <ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz>. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from <ftp.sgi.com> or from <ftp://ftp.uu.net/graphics/jpeg/>. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note. libtiff is available from <ftp://ftp.sgi.com/graphics/tiff/>.

ARCHIVE LOCATIONS

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The "official" archive site for this software is <ftp.uu.net> (Internet address 192.48.96.9). The most recent released version can always be found there in directory <graphics/jpeg>. This particular version will be archived as <ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz>. If you don't

have
direct Internet access, UUNET's archives are also available via UUCP; contact help@uunet.uu.net for information on retrieving files that way.

Numerous Internet sites maintain copies of the UUNET files. However, only ftp.uu.net is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the ftp.uu.net release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of general information about JPEG. It is updated constantly and therefore is not included in this distribution. The FAQ is posted every two weeks to Usenet newsgroups comp.graphics.misc, news.answers, and other groups. It is available on the World Wide Web at <http://www.faqs.org/faqs/jpeg-faq/> and other news.answers archive sites, including the official news.answers archive at [rtfm.mit.edu: ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/](ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/). If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu with body
send usenet/news.answers/jpeg-faq/part1
send usenet/news.answers/jpeg-faq/part2

RELATED SOFTWARE

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Numerous viewing and image manipulation programs now support JPEG. (Quite a few of them use this library to do so.) The JPEG FAQ described above lists some of the more popular free and shareware viewers, and tells where to obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free PBPLUS software, which provides many useful operations on PPM-format image files. In particular, it can convert PPM images to and from a wide range of other formats, thus making cjpeg/djpeg considerably more useful. The latest version is distributed by the NetPBM group, and is available from numerous sites,
notably <ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/>. Unfortunately PBPLUS/NETPBM is not nearly as portable as the IJG software is; you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford, is available from <ftp://havefun.stanford.edu/pub/jpeg/>. This program is designed for research and experimentation rather than production use;

it is slower, harder to use, and less portable than the IJG code, but it is easier to read and modify. Also, the PVRG code supports lossless JPEG, which we do not. (On the other hand, it doesn't do progressive JPEG.)

FILE FORMAT WARS

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Some JPEG programs produce files that are not compatible with our library. The root of the problem is that the ISO JPEG committee failed to specify a concrete file format. Some vendors "filled in the blanks" on their own, creating proprietary formats that no one else could read.

(For example, none of the early commercial JPEG implementations for the Macintosh were able to exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF. SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether

SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO

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The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary

smoothing, "poor man's variable quantization", and other means of improving quality-vs-file-size performance without sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.

Please send bug reports, offers of help, etc. to jpeg-info@uunet.uu.net.

1.5 apache-log4j 1.2.17

1.5.1 Available under license :

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1.7 libpcap 1.4.0

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libbz2.dll test program.
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1.9 slf4j 1.7.2

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1.10 open-ldap 2.4.40

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depcomp - compile a program generating dependencies as side-effects

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1.12 guava 18.0

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1.14 libxml2 2.9.2

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 jar/net/miginfocom/layout/LayoutUtil.java
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 jar/net/miginfocom/swing/SwingComponentWrapper.java
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 jar/net/miginfocom/swt/MigLayout.java
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jar/net/miginfocom/demo/SwingDemo.java
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*

* @version 1.0

* @author Mikael Grev, MiG InfoCom AB

* Date: 2006-sep-08

*/

/** Returns the absolute positioning of one or more of the edges. This will be applied last in the layout cycle and will not

- * affect the flow or grid positions. The positioning is relative to the parent and can not (as padding) be used
- * to adjust the edges relative to the old value. May be `null` and elements may be `null`.
- * `null` value(s) for the x2 and y2 will be interpreted as to keep the preferred size and thus the x1 and x2 will just absolutely position the component.

* `<p>`

* Note that { `@link #setBoundsInGrid(boolean)` } changes the interpretation of this property slightly.

* `<p>`

* For a more thorough explanation of what this constraint does see the white paper or cheat Sheet at www.migcomponents.com.

* @return The current value as a new array, free to modify.

*/

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* @version 1.0
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*   Date: 2006-sep-08
*/

/** Returns the gaps as pixel values.
* @param parent The parent. Used to get the pixel values.
* @param defGap The default gap to use if there is no gap set on this object (i.e. it is null).
* @param
refSize The reference size used to get the pixel sizes.
* @param before IF it is the gap before rather than the gap after to return.
* @return The [min,preferred,max] sizes for the specified gap. Uses { @link
net.miginfocom.layout.LayoutUtil#NOT_SET }
* for gap sizes that are <code>null</code>. Returns <code>null</code> if there was no gap specified. A new and
free to use array.
*/

```

Found in path(s):

* /opt/cola/permits/117692136_1695289078.9935892/0/miglayout-3-5-5-sources-jar/net/miginfocom/layout/DimConstraint.java

1.17 commons-collections 3.2.2

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```

1.27 tcsh 6.20.00

1.27.1 Available under license :

```
/*
 * config.h -- configure various defines for tcsh
 *
 * All source files should #include this FIRST.
 *
 * Edit this to match your system type.
 */

#ifndef _h_config
#define _h_config
/***** System dependant compilation flags *****/
/*
 * POSIX This system supports IEEE Std 1003.1-1988 (POSIX).
 */
#undef POSIX

/*
 * POSIXJOBS This system supports the optional IEEE Std 1003.1-1988 (POSIX)
 * job control facilities.
 */
#undef POSIXJOBS

/*
 * VFORK This machine has a vfork().
 * It used to be that for job control to work, this define
 * was mandatory. This is not the case any more.
 * If you think you still need it, but you don't have vfork,
 * define this anyway and then do #define vfork fork.
 * I do this anyway on a Sun because of yellow pages brain damage,
 * [should not be needed under 4.1]
 * and on the iris4d cause SGI's fork is sufficiently "virtual"
 * that vfork isn't necessary. (Besides, SGI's vfork is weird).
 * Note that some machines
 * eg. rs6000 have a vfork, but not
 * with the berkeley semantics, so we cannot use it there either.
 */
#define VFORK

/*
 * BSDJOBS You have BSD-style job control (both process groups and
 * a tty that deals correctly
 */
#define BSDJOBS
```

```

/*
* BSDTIMES You have BSD-style process time stuff (like rusage)
* This may or may not be true. For example, Apple Unix
* (OREO) has BSDJOBS but not BSDTIMES.
*/
#define BSDTIMES

/*
* BSDLIMIT You have BSD-style resource limit stuff (getrlimit/setrlimit)
*/
#define BSDLIMIT

/*
* TERMIO You have struct termio instead of struct sgtyb.
* This is usually the case for SYSV systems, where
* BSD uses sgtyb. POSIX systems should define this
* anyway, even though they use struct termios.
*/
#undef TERMIO

/*
* SYSVREL Your machine is SYSV based (HPUX, A/UX)
* NOTE: don't do this if you are on a Pyramid -- tcsh is
* built in a BSD universe.
* Set SYSVREL to 1, 2, 3, or 4, depending the version of System V
* you are running.
* Or set it to 0 if you are not SYSV based
*/
#define SYSVREL 0

/*
* YPBUGS Work around Sun YP bugs that cause expansion of ~username
* to send command output to /dev/null
*/
#undef YPBUGS

/***** local defines *****/

#endif /* _h_config */
/*_
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- */

1.28 Idns 1.7.0

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1.29 Izo 2.10

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it
under certain conditions; type `show c' for details.
```

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```
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```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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1.30 glib 2.46.2

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PCRE LICENCE

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End

```
/*  
*****
```

```
* Perl-Compatible Regular Expressions *
```

```
*****/  
*/
```

```
/* This is the public header file for the PCRE library, to be #included by  
applications that call the PCRE functions.
```

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*/

```
#ifndef _PCRE_H
#define _PCRE_H
```

```
/*
```

```
The current PCRE version information. */
```

```
#define PCRE_MAJOR      8
#define PCRE_MINOR      31
#define PCRE_PRERELEASE
#define PCRE_DATE        2012-07-06
```

```
/* When an application links to a PCRE DLL in Windows, the symbols that are imported have to be identified as such. When building PCRE, the appropriate export setting is defined in pcre_internal.h, which includes this file. So we don't change existing definitions of PCRE_EXP_DECL and PCRECPP_EXP_DECL. */
```

```
#if defined(_WIN32) && !defined(PCRE_STATIC)
#  ifndef PCRE_EXP_DECL
#    define PCRE_EXP_DECL extern __declspec(dllexport)
#  endif
#  ifdef __cplusplus
#    ifndef PCRECPP_EXP_DECL
#      define PCRECPP_EXP_DECL extern __declspec(dllexport)
#    endif
#    ifndef PCRECPP_EXP_DEFN
#      define PCRECPP_EXP_DEFN __declspec(dllexport)
#    endif
#  endif
#endif
```



```

# endif
#endif

/* By default, we use the standard "extern" declarations. */

#ifndef PCRE_EXP_DECL
# ifdef __cplusplus
#  define PCRE_EXP_DECL extern "C"
# else
#
#  define PCRE_EXP_DECL extern
# endif
#endif

#ifdef __cplusplus
# ifndef PCRECPP_EXP_DECL
#  define PCRECPP_EXP_DECL extern
# endif
# ifndef PCRECPP_EXP_DEFN
#  define PCRECPP_EXP_DEFN
# endif
#endif

/* Have to include stdlib.h in order to ensure that size_t is defined;
it is needed here for malloc. */

#include <stdlib.h>

/* Allow for C++ users */

#ifdef __cplusplus
extern "C" {
#endif

/* Options. Some are compile-time only, some are run-time only, and some are
both, so we keep them all distinct. However, almost all the bits in the options
word are now used. In the long run, we may have to re-use some of the
compile-time only bits for runtime options, or vice versa. In the comments
below, "compile", "exec", and "DFA exec" mean that the option is permitted to
be set for those functions; "used in" means that an option may be set only for
compile, but is subsequently referenced in exec and/or DFA exec. Any of the
compile-time options may be inspected during studying (and
therefore JIT
compiling). */

#define PCRE_CASELESS      0x00000001 /* Compile */
#define PCRE_MULTILINE     0x00000002 /* Compile */
#define PCRE_DOTALL       0x00000004 /* Compile */

```

```

#define PCRE_EXTENDED      0x00000008 /* Compile */
#define PCRE_ANCHORED     0x00000010 /* Compile, exec, DFA exec */
#define PCRE_DOLLAR_ENDONLY 0x00000020 /* Compile, used in exec, DFA exec */
#define PCRE_EXTRA        0x00000040 /* Compile */
#define PCRE_NOTBOL       0x00000080 /* Exec, DFA exec */
#define PCRE_NOTEOL       0x00000100 /* Exec, DFA exec */
#define PCRE_UNGREEDY     0x00000200 /* Compile */
#define PCRE_NOTEMPTY     0x00000400 /* Exec, DFA exec */
/* The next two are also used in exec and DFA exec */
#define PCRE_UTF8         0x00000800 /* Compile (same as PCRE_UTF16) */
#define PCRE_UTF16        0x00000800 /* Compile (same as PCRE_UTF8) */
#define PCRE_NO_AUTO_CAPTURE 0x00001000 /* Compile */
/*
The next two are also used in exec and DFA exec */
#define PCRE_NO_UTF8_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF16_CHECK) */
#define PCRE_NO_UTF16_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF8_CHECK) */
#define PCRE_AUTO_CALLOUT 0x00004000 /* Compile */
#define PCRE_PARTIAL_SOFT 0x00008000 /* Exec, DFA exec */
#define PCRE_PARTIAL      0x00008000 /* Backwards compatible synonym */
#define PCRE_DFA_SHORTEST 0x00010000 /* DFA exec */
#define PCRE_DFA_RESTART  0x00020000 /* DFA exec */
#define PCRE_FIRSTLINE    0x00040000 /* Compile, used in exec, DFA exec */
#define PCRE_DUPNAMES     0x00080000 /* Compile */
#define PCRE_NEWLINE_CR   0x00100000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_LF   0x00200000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_CRLF 0x00300000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_ANY  0x00400000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_ANYCRLF
0x00500000 /* Compile, exec, DFA exec */
#define PCRE_BSR_ANYCRLF  0x00800000 /* Compile, exec, DFA exec */
#define PCRE_BSR_UNICODE 0x01000000 /* Compile, exec, DFA exec */
#define PCRE_JAVASCRIPT_COMPAT 0x02000000 /* Compile, used in exec */
#define PCRE_NO_START_OPTIMIZE 0x04000000 /* Compile, exec, DFA exec */
#define PCRE_NO_START_OPTIMISE 0x04000000 /* Synonym */
#define PCRE_PARTIAL_HARD 0x08000000 /* Exec, DFA exec */
#define PCRE_NOTEMPTY_ATSTART 0x10000000 /* Exec, DFA exec */
#define PCRE_UCP          0x20000000 /* Compile, used in exec, DFA exec */

/* Exec-time and get/set-time error codes */

#define PCRE_ERROR_NOMATCH (-1)
#define PCRE_ERROR_NULL (-2)
#define PCRE_ERROR_BADOPTION (-3)
#define PCRE_ERROR_BADMAGIC (-4)
#define PCRE_ERROR_UNKNOWN_OPCODE (-5)
#define PCRE_ERROR_UNKNOWN_NODE (-5) /* For backward compatibility */
#define PCRE_ERROR_NOMEMORY (-6)
#define PCRE_ERROR_NOSUBSTRING

```

(-7)

```
#define PCRE_ERROR_MATCHLIMIT      (-8)
#define PCRE_ERROR_CALLOUT        (-9) /* Never used by PCRE itself */
#define PCRE_ERROR_BADUTF8        (-10) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF16      (-10) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF8_OFFSET (-11) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF16_OFFSET (-11) /* Same for 8/16 */
#define PCRE_ERROR_PARTIAL        (-12)
#define PCRE_ERROR_BADPARTIAL     (-13)
#define PCRE_ERROR_INTERNAL       (-14)
#define PCRE_ERROR_BADCOUNT     (-15)
#define PCRE_ERROR_DFA_UITEM     (-16)
#define PCRE_ERROR_DFA_UCOND     (-17)
#define PCRE_ERROR_DFA_UMLIMIT   (-18)
#define PCRE_ERROR_DFA_WSSIZE    (-19)
#define PCRE_ERROR_DFA_RECURSE  (-20)
#define PCRE_ERROR_RECURSIONLIMIT (-21)
#define PCRE_ERROR_NULLWSLIMIT   (-22) /* No longer actually used */
#define PCRE_ERROR_BADNEWLINE    (-23)
#define PCRE_ERROR_BADOFFSET     (-24)
#define PCRE_ERROR_SHORTUTF8     (-25)
#define
  PCRE_ERROR_SHORTUTF16        (-25) /* Same for 8/16 */
#define PCRE_ERROR_RECURSELOOP  (-26)
#define PCRE_ERROR_JIT_STACKLIMIT (-27)
#define PCRE_ERROR_BADMODE      (-28)
#define PCRE_ERROR_BADENDIANNESS (-29)
#define PCRE_ERROR_DFA_BADRESTART (-30)
```

/ Specific error codes for UTF-8 validity checks */*

```
#define PCRE_UTF8_ERR0      0
#define PCRE_UTF8_ERR1      1
#define PCRE_UTF8_ERR2      2
#define PCRE_UTF8_ERR3      3
#define PCRE_UTF8_ERR4      4
#define PCRE_UTF8_ERR5      5
#define PCRE_UTF8_ERR6      6
#define PCRE_UTF8_ERR7      7
#define PCRE_UTF8_ERR8      8
#define PCRE_UTF8_ERR9      9
#define PCRE_UTF8_ERR10     10
#define PCRE_UTF8_ERR11     11
#define PCRE_UTF8_ERR12     12
#define PCRE_UTF8_ERR13     13
#define PCRE_UTF8_ERR14     14
#define PCRE_UTF8_ERR15     15
#define PCRE_UTF8_ERR16     16
```

```

#define PCRE_UTF8_ERR17
    17
#define PCRE_UTF8_ERR18    18
#define PCRE_UTF8_ERR19    19
#define PCRE_UTF8_ERR20    20
#define PCRE_UTF8_ERR21    21

/* Specific error codes for UTF-16 validity checks */

#define PCRE_UTF16_ERR0    0
#define PCRE_UTF16_ERR1    1
#define PCRE_UTF16_ERR2    2
#define PCRE_UTF16_ERR3    3
#define PCRE_UTF16_ERR4    4

/* Request types for pcre_fullinfo() */

#define PCRE_INFO_OPTIONS    0
#define PCRE_INFO_SIZE      1
#define PCRE_INFO_CAPTURECOUNT  2
#define PCRE_INFO_BACKREFMAX  3
#define PCRE_INFO_FIRSTBYTE   4
#define PCRE_INFO_FIRSTCHAR   4 /* For backwards compatibility */
#define PCRE_INFO_FIRSTTABLE  5
#define PCRE_INFO_LASTLITERAL  6
#define PCRE_INFO_NAMEENTRYSIZE  7
#define PCRE_INFO_NAMECOUNT  8
#define PCRE_INFO_NAMETABLE  9
#define PCRE_INFO_STUDYSIZE  10
#define PCRE_INFO_DEFAULT_TABLES
    11
#define PCRE_INFO_OKPARTIAL  12
#define PCRE_INFO_JCHANGED  13
#define PCRE_INFO_HASCORLRF  14
#define PCRE_INFO_MINLENGTH  15
#define PCRE_INFO_JIT        16
#define PCRE_INFO_JITSIZE    17
#define PCRE_INFO_MAXLOOKBEHIND  18

/* Request types for pcre_config(). Do not re-arrange, in order to remain
compatible. */

#define PCRE_CONFIG_UTF8      0
#define PCRE_CONFIG_NEWLINE  1
#define PCRE_CONFIG_LINK_SIZE  2
#define PCRE_CONFIG_POSIX_MALLOC_THRESHOLD  3
#define PCRE_CONFIG_MATCH_LIMIT  4
#define PCRE_CONFIG_STACKRECURSE  5

```

```

#define PCRE_CONFIG_UNICODE_PROPERTIES    6
#define PCRE_CONFIG_MATCH_LIMIT_RECURSION 7
#define PCRE_CONFIG_BSR                   8
#define PCRE_CONFIG_JIT                   9
#define PCRE_CONFIG_UTF16                 10
#define PCRE_CONFIG_JITTARGET             11

/* Request types for pcre_study(). Do not re-arrange, in order to remain
compatible.
*/

#define PCRE_STUDY_JIT_COMPILE            0x0001
#define PCRE_STUDY_JIT_PARTIAL_SOFT_COMPILE 0x0002
#define PCRE_STUDY_JIT_PARTIAL_HARD_COMPILE 0x0004

/* Bit flags for the pcre[16]_extra structure. Do not re-arrange or redefine
these bits, just add new ones on the end, in order to remain compatible. */

#define PCRE_EXTRA_STUDY_DATA            0x0001
#define PCRE_EXTRA_MATCH_LIMIT           0x0002
#define PCRE_EXTRA_CALLOUT_DATA         0x0004
#define PCRE_EXTRA_TABLES                0x0008
#define PCRE_EXTRA_MATCH_LIMIT_RECURSION 0x0010
#define PCRE_EXTRA_MARK                  0x0020
#define PCRE_EXTRA_EXECUTABLE_JIT       0x0040

/* Types */

struct real_pcre;          /* declaration; the definition is private */
typedef struct real_pcre pcre;

struct real_pcre16;       /* declaration; the definition is private */
typedef struct real_pcre16 pcre16;

struct real_pcre_jit_stack; /* declaration; the definition is private */
typedef
struct real_pcre_jit_stack pcre_jit_stack;

struct real_pcre16_jit_stack; /* declaration; the definition is private */
typedef struct real_pcre16_jit_stack pcre16_jit_stack;

/* If PCRE is compiled with 16 bit character support, PCRE_UCHAR16 must contain
a 16 bit wide signed data type. Otherwise it can be a dummy data type since
pcre16 functions are not implemented. There is a check for this in pcre_internal.h. */
#ifndef PCRE_UCHAR16
#define PCRE_UCHAR16 unsigned short
#endif

```

```

#ifndef PCRE_SPTR16
#define PCRE_SPTR16 const PCRE_UCHAR16 *
#endif

/* When PCRE is compiled as a C++ library, the subject pointer type can be
replaced with a custom type. For conventional use, the public interface is a
const char *. */

#ifndef PCRE_SPTR
#define PCRE_SPTR const char *
#endif

/* The structure for passing additional data to pcre_exec(). This is defined in
such a way as to be extensible. Always add new fields at the end, in order to
remain compatible. */

typedef struct pcre_extra {
    unsigned long int flags; /* Bits for which fields are set */
    void *study_data; /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data; /* Data passed back in callouts */
    const unsigned char *tables; /* Pointer to character tables */
    unsigned long int match_limit_recursion; /* Max recursive calls to match() */
    unsigned char **mark; /* For passing back a mark pointer */
    void *executable_jit; /* Contains a pointer to a compiled jit code */
} pcre_extra;

/* Same structure as above, but with 16 bit char pointers. */

typedef struct pcre16_extra {
    unsigned long int flags; /* Bits for which fields are set */
    void *study_data; /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data; /* Data passed back in callouts */
    const unsigned char
*tables; /* Pointer to character tables */
    unsigned long int match_limit_recursion; /* Max recursive calls to match() */
    PCRE_UCHAR16 **mark; /* For passing back a mark pointer */
    void *executable_jit; /* Contains a pointer to a compiled jit code */
} pcre16_extra;

/* The structure for passing out data via the pcre_callout_function. We use a
structure so that new fields can be added on the end in future versions,
without changing the API of the function, thereby allowing old clients to work
without modification. */

typedef struct pcre_callout_block {
    int version; /* Identifies version of block */

```

```

/* ----- Version 0 ----- */
int    callout_number; /* Number compiled into pattern */
int    *offset_vector; /* The offset vector */
PCRE_SPTR subject;    /* The subject being matched */
int    subject_length; /* The length of the subject */
int
    start_match; /* Offset to start of this match attempt */
int    current_position; /* Where we currently are in the subject */
int    capture_top; /* Max current capture */
int    capture_last; /* Most recently closed capture */
void   *callout_data; /* Data passed in with the call */
/* ----- Added for Version 1 ----- */
int    pattern_position; /* Offset to next item in the pattern */
int    next_item_length; /* Length of next item in the pattern */
/* ----- Added for Version 2 ----- */
const unsigned char *mark; /* Pointer to current mark or NULL */
/* ----- */
} pcre_callout_block;

```

/* Same structure as above, but with 16 bit char pointers. */

```

typedef struct pcre16_callout_block {
int    version; /* Identifies version of block */
/* -----
Version 0 ----- */
int    callout_number; /* Number compiled into pattern */
int    *offset_vector; /* The offset vector */
PCRE_SPTR16 subject; /* The subject being matched */
int    subject_length; /* The length of the subject */
int    start_match; /* Offset to start of this match attempt */
int    current_position; /* Where we currently are in the subject */
int    capture_top; /* Max current capture */
int    capture_last; /* Most recently closed capture */
void   *callout_data; /* Data passed in with the call */
/* ----- Added for Version 1 ----- */
int    pattern_position; /* Offset to next item in the pattern */
int    next_item_length; /* Length of next item in the pattern */
/* ----- Added for Version 2 ----- */
const PCRE_UCHAR16 *mark;
    /* Pointer to current mark or NULL */
/* ----- */
} pcre16_callout_block;

```

/* Indirection for store get and free functions. These can be set to alternative malloc/free functions if required. Special ones are used in the non-recursive case for "frames". There is also an optional callout function that is triggered by the (?) regex item. For Virtual Pascal, these definitions have to take another form. */

```

#ifndef VPCOMPAT
PCRE_EXP_DECL void>(*pcre_malloc)(size_t);
PCRE_EXP_DECL void(*pcre_free)(void*);
PCRE_EXP_DECL void>(*pcre_stack_malloc)(size_t);
PCRE_EXP_DECL void(*pcre_stack_free)(void*);
PCRE_EXP_DECL int(*pcre_callout)(pcre_callout_block*);

PCRE_EXP_DECL void(*pcre16_malloc)(size_t);
PCRE_EXP_DECL void(*pcre16_free)(void*);
PCRE_EXP_DECL void>(*pcre16_stack_malloc)(size_t);
PCRE_EXP_DECL void(*pcre16_stack_free)(void*);
PCRE_EXP_DECL int(*pcre16_callout)(pcre16_callout_block*);
#else
/* VPCOMPAT */
PCRE_EXP_DECL void*pcre_malloc(size_t);
PCRE_EXP_DECL void pcre_free(void*);
PCRE_EXP_DECL void*pcre_stack_malloc(size_t);
PCRE_EXP_DECL void pcre_stack_free(void*);
PCRE_EXP_DECL int pcre_callout(pcre_callout_block*);

PCRE_EXP_DECL void*pcre16_malloc(size_t);
PCRE_EXP_DECL void pcre16_free(void*);
PCRE_EXP_DECL void*pcre16_stack_malloc(size_t);
PCRE_EXP_DECL void pcre16_stack_free(void*);
PCRE_EXP_DECL int pcre16_callout(pcre16_callout_block*);
#endif /* VPCOMPAT */

/* User defined callback which provides a stack just before the match starts. */

typedef pcre_jit_stack>(*pcre_jit_callback)(void*);
typedef pcre16_jit_stack>(*pcre16_jit_callback)(void*);

/* Exported PCRE functions */

PCRE_EXP_DECL pcre*pcre_compile(const char*, int, const char**, int*,
    const unsigned char*);
PCRE_EXP_DECL pcre16*pcre16_compile(PCRE_SPTR16, int, const char**, int*,
    const unsigned char*);
PCRE_EXP_DECL pcre*pcre_compile2(const
    char*, int, int*, const char**,
    int*, const unsigned char*);
PCRE_EXP_DECL pcre16*pcre16_compile2(PCRE_SPTR16, int, int*, const char**,
    int*, const unsigned char*);
PCRE_EXP_DECL int pcre_config(int, void*);
PCRE_EXP_DECL int pcre16_config(int, void*);
PCRE_EXP_DECL int pcre_copy_named_substring(const pcre*, const char*,
    int*, int, const char*, char*, int);

```



```

PCRE_EXP_DECL int pcre16_copy_named_substring(const pcre16 *, PCRE_SPTR16,
    int *, int, PCRE_SPTR16, PCRE_UCHAR16 *, int);
PCRE_EXP_DECL int pcre_copy_substring(const char *, int *, int, int,
    char *, int);
PCRE_EXP_DECL int pcre16_copy_substring(PCRE_SPTR16, int *, int, int,
    PCRE_UCHAR16 *, int);
PCRE_EXP_DECL int pcre_dfa_exec(const pcre *, const pcre_extra *,
    const char *, int, int, int *, int *, int *, int);
PCRE_EXP_DECL int pcre16_dfa_exec(const pcre16 *, const
    pcre16_extra *,
    PCRE_SPTR16, int, int, int *, int *, int *, int);
PCRE_EXP_DECL int pcre_exec(const pcre *, const pcre_extra *, PCRE_SPTR,
    int, int, int, int *, int);
PCRE_EXP_DECL int pcre16_exec(const pcre16 *, const pcre16_extra *,
    PCRE_SPTR16, int, int, int, int *, int);
PCRE_EXP_DECL void pcre_free_substring(const char *);
PCRE_EXP_DECL void pcre16_free_substring(PCRE_SPTR16);
PCRE_EXP_DECL void pcre_free_substring_list(const char **);
PCRE_EXP_DECL void pcre16_free_substring_list(PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_fullinfo(const pcre *, const pcre_extra *, int,
    void *);
PCRE_EXP_DECL int pcre16_fullinfo(const pcre16 *, const pcre16_extra *, int,
    void *);
PCRE_EXP_DECL int pcre_get_named_substring(const pcre *, const char *,
    int *, int, const char *, const char **);
PCRE_EXP_DECL int pcre16_get_named_substring(const pcre16 *, PCRE_SPTR16,
    int
    *, int, PCRE_SPTR16, PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_get_stringnumber(const pcre *, const char *);
PCRE_EXP_DECL int pcre16_get_stringnumber(const pcre16 *, PCRE_SPTR16);
PCRE_EXP_DECL int pcre_get_stringtable_entries(const pcre *, const char *,
    char **, char **);
PCRE_EXP_DECL int pcre16_get_stringtable_entries(const pcre16 *, PCRE_SPTR16,
    PCRE_UCHAR16 **, PCRE_UCHAR16 **);
PCRE_EXP_DECL int pcre_get_substring(const char *, int *, int, int,
    const char **);
PCRE_EXP_DECL int pcre16_get_substring(PCRE_SPTR16, int *, int, int,
    PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_get_substring_list(const char *, int *, int,
    const char ***);
PCRE_EXP_DECL int pcre16_get_substring_list(PCRE_SPTR16, int *, int,
    PCRE_SPTR16 **);
PCRE_EXP_DECL const unsigned char *pcre_maketables(void);
PCRE_EXP_DECL const unsigned char *pcre16_maketables(void);
PCRE_EXP_DECL int pcre_refcount(pcre *,
    int);
PCRE_EXP_DECL int pcre16_refcount(pcre16 *, int);
PCRE_EXP_DECL pcre_extra *pcre_study(const pcre *, int, const char **);

```

```

PCRE_EXP_DECL pcre16_extra *pcre16_study(const pcre16 *, int, const char **);
PCRE_EXP_DECL void pcre_free_study(pcre_extra *);
PCRE_EXP_DECL void pcre16_free_study(pcre16_extra *);
PCRE_EXP_DECL const char *pcre_version(void);
PCRE_EXP_DECL const char *pcre16_version(void);

/* Utility functions for byte order swaps. */
PCRE_EXP_DECL int pcre_pattern_to_host_byte_order(pcre *, pcre_extra *,
    const unsigned char *);
PCRE_EXP_DECL int pcre16_pattern_to_host_byte_order(pcre16 *, pcre16_extra *,
    const unsigned char *);
PCRE_EXP_DECL int pcre16_utf16_to_host_byte_order(PCRE_UCHAR16 *,
    PCRE_SPTR16, int, int *, int);

/* JIT compiler related functions. */

PCRE_EXP_DECL pcre_jit_stack *pcre_jit_stack_alloc(int, int);
PCRE_EXP_DECL pcre16_jit_stack *pcre16_jit_stack_alloc(int, int);
PCRE_EXP_DECL void pcre_jit_stack_free(pcre_jit_stack
    *);
PCRE_EXP_DECL void pcre16_jit_stack_free(pcre16_jit_stack *);
PCRE_EXP_DECL void pcre_assign_jit_stack(pcre_extra *,
    pcre_jit_callback, void *);
PCRE_EXP_DECL void pcre16_assign_jit_stack(pcre16_extra *,
    pcre16_jit_callback, void *);

#ifdef __cplusplus
} /* extern "C" */
#endif

#endif /* End of pcre.h */

```

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[This is the first released version of the library GPL. It is
numbered 2 because it goes with version 2 of the ordinary GPL.]

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```

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<signature of Ty Coon>, 1 April 1990

Ty Coon, President of Vice

That's all there is to it!

```
/* GLIB - Library of useful routines for C programming
```

```
* Copyright (C) 1995-1997 Peter Mattis, Spencer Kimball and Josh MacDonald
```

```
*
```

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```

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```

```
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```

```
*
```

```
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```

```
*
```

```
* You should have received a copy of the GNU Lesser General Public
```

```
* License along with this library; if not, see <http://www.gnu.org/licenses/>.
```

```
*/
```

```
/*
```

```
* Modified by the GLib Team and others 1997-2000. See the AUTHORS
```

```
* file for a list of people on the GLib Team. See the ChangeLog
```

```
* files for a list of changes. These files are distributed
```

```
with
```

```
* GLib at ftp://ftp.gtk.org/pub/gtk/.
```

```
*/
```

```
#ifndef __G_LIB_H__
```

```
#define __G_LIB_H__
```

```
#define __GLIB_H_INSIDE__
```

```
#include <glib/galloca.h>
```

```
#include <glib/garray.h>
```

```
#include <glib/gasyncqueue.h>
```

```
#include <glib/gatomic.h>
```

```
#include <glib/gbacktrace.h>
```

```
#include <glib/gbase64.h>
```

```
#include <glib/gbitlock.h>
```

```
#include <glib/gbookmarkfile.h>
```

```
#include <glib/gbytes.h>
#include <glib/gcharset.h>
#include <glib/gchecksum.h>
#include <glib/gconvert.h>
#include <glib/gdataset.h>
#include <glib/gdate.h>
#include <glib/gdatettime.h>
#include <glib/gdir.h>
#include <glib/genviron.h>
#include <glib/gerror.h>
#include <glib/gfileutils.h>
#include <glib/ggettext.h>
#include <glib/ghash.h>
#include <glib/ghmac.h>
#include <glib/ghook.h>
#include <glib/ghostutils.h>
#include <glib/giochannel.h>
#include <glib/gkeyfile.h>
#include <glib/glist.h>
#include <glib/gmacros.h>
#include <glib/gmain.h>
#include <glib/gmappedfile.h>
#include <glib/gmarkup.h>
#include <glib/gmem.h>
#include <glib/gmessages.h>
#include
  <glib/gnode.h>
#include <glib/goption.h>
#include <glib/gpattern.h>
#include <glib/gpoll.h>
#include <glib/gprimes.h>
#include <glib/gqsort.h>
#include <glib/gquark.h>
#include <glib/gqueue.h>
#include <glib/grand.h>
#include <glib/gregex.h>
#include <glib/gscanner.h>
#include <glib/gsequence.h>
#include <glib/gshell.h>
#include <glib/gslice.h>
#include <glib/gslist.h>
#include <glib/gspawn.h>
#include <glib/gstrfuncs.h>
#include <glib/gstring.h>
#include <glib/gstringchunk.h>
#include <glib/gtestutils.h>
#include <glib/gthread.h>
#include <glib/gthreadpool.h>
```

```

#include <glib/gtimer.h>
#include <glib/gtimezone.h>
#include <glib/gtrashstack.h>
#include <glib/gtree.h>
#include <glib/gtypes.h>
#include <glib/gunicode.h>
#include <glib/gurifuncs.h>
#include <glib/gutils.h>
#include <glib/gvarianttype.h>
#include <glib/gvariant.h>
#include <glib/gversion.h>
#include <glib/gversionmacros.h>
#ifdef G_PLATFORM_WIN32
#include <glib/gwin32.h>
#endif

#ifndef G_DISABLE_DEPRECATED
#include <glib/deprecated/gallocator.h>
#include
  <glib/deprecated/gcache.h>
#include <glib/deprecated/gcompletion.h>
#include <glib/deprecated/gmain.h>
#include <glib/deprecated/grel.h>
#include <glib/deprecated/gthread.h>
#endif /* G_DISABLE_DEPRECATED */

#include <glib/glib-autocleanups.h>

#undef __GLIB_H_INSIDE__

#endif /* __G_LIB_H__ */

```

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1.31 libtool 2.4.6

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1.33 tomcat-annotations-api 8.5.32

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1.35 jaxb-core 2.3.0

1.36 zlib 1.2.3

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1.38 hk2-locator 2.5.0

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1.46 hikaricp-java7 2.4.13

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zip/com/zaxxer/hikari/pool/PoolBase.java
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zip/com/zaxxer/hikari/HikariPoolMXBean.java
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zip/com/zaxxer/hikari/util/FastList.java

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1.47 commons-http-client 3.1

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The original XML Schemas for Java EE Deployment Descriptors:

- javaee_5.xsd
- javaee_web_services_1_2.xsd
- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd

- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd

may be obtained from:

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For the following XML

Schemas for Java EE Deployment Descriptors:

- javaee_5.xsd
- javaee_web_services_1_2.xsd
- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd
- javaee_8.xsd
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1.49 bzip2 1.0.8

1.50 Idns 1.7.1

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#!/bin/sh

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libtool (GNU libtool) 2.4.6

Provide generalized library-building support services.

Written by Gordon Matzigkeit <gord@gnu.ai.mit.edu>, 1996

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/* From openssl 4.3p2 filename openssl-compat/fake-rfc2553.h */

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/*

* FILE: sha2.c

* AUTHOR: Aaron D. Gifford - <http://www.aarongifford.com/>

*

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*

* Modified by Jelte Jansen to fit in ldns, and not clash with any

* system-defined SHA code.

* Changes:

* - Renamed (external) functions and constants to fit ldns style

* - Removed _End and _Data functions

* - Added ldns_shaX(data, len, digest) convenience functions

* - Removed prototypes of _Transform functions and made those static

*

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* \$Id:

sha2.c,v 1.1 2001/11/08 00:01:51 adg Exp adg \$

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/* from openssh 4.3p2 compat/strncpy.c */

/*

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/* From openssl4.3p2 compat/inet_aton.c */

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1.51 zlib 1.2.8

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/* zlib.h -- interface of the 'zlib' general purpose compression library
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1.52 zlib 1.2.5

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* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/GZIPOutputStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/ZInputStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Deflater.java
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*
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* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/JZlib.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/GZIPInputStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/CRC32.java
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* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/InflaterInputStream.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Checksum.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/ZOutputStream.java
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* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/InfTree.java
* /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Tree.java
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*/

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- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Inflate.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/ZStream.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Adler32.java
- * /opt/cola/permits/1000011954_1651084626.73/0/jzlib-1-1-3-sources-jar/com/jcraft/jzlib/Deflate.java

1.56 expat 2.0.0

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1.58 sbsigntool 1.3

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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- b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.
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@page

@heading Appendix: How to Apply These Terms to Your New Programs

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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the ``copyright" line and a pointer to where the full notice is found.

@smallexample

@var{one line to give the program's name
and a brief idea of what it does.}

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@end smallexample

Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

```
@smallexample
Gnomovision version
69, Copyright (C) @var{year} @var{name of author}
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
@end smallexample
```

The hypothetical commands @samp{show w} and @samp{show c} should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than @samp{show w} and @samp{show c}; they could even be mouse-clicks or menu items---whatever suits your program.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a ``copyright disclaimer" for the program, if necessary. Here is a sample; alter the names:

```
@example
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
@var{signature of Ty Coon}, 1 April 1989
Ty Coon, President of Vice
@end example
```

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The

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1.62 jffi 1.0.1

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1.64 libdaemon 0.14

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Version 2.1, February 1999

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1.65 bzip2 1.0.7

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1.66 commons-codec 1.9

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@item

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@item

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1.73 jetty-util 9.4.16.v20190411

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The following artifacts are EPL.

* org.eclipse.jetty.orbit:org.eclipse.jdt.core

The following artifacts are EPL and ASL2.

* org.eclipse.jetty.orbit:javax.security.auth.message

The following artifacts are EPL and CDDL 1.0.

* org.eclipse.jetty.orbit:javax.mail.glassfish

Oracle

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

* javax.servlet:javax.servlet-api

* javax.annotation:javax.annotation-api

* javax.transaction:javax.transaction-api

* javax.websocket:javax.websocket-api

Oracle OpenJDK

If ALPN is used to negotiate HTTP/2 connections, then the following artifacts may be included in the distribution or downloaded when ALPN module is selected.

* java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

OW2

The following artifacts are licensed by the OW2 Foundation according to the terms of <http://asm.ow2.org/license.html>

org.ow2.asm:asm-commons

org.ow2.asm:asm

Apache

The following artifacts are ASL2 licensed.

org.apache.taglibs:taglibs-standard-spec

org.apache.taglibs:taglibs-standard-impl

MortBay

The following artifacts are ASL2 licensed. Based on selected classes from following Apache Tomcat jars, all ASL2 licensed.

org.mortbay.jasper:apache-jsp

org.apache.tomcat:tomcat-jasper

org.apache.tomcat:tomcat-juli

org.apache.tomcat:tomcat-jsp-api

org.apache.tomcat:tomcat-el-api

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-api

org.apache.tomcat:tomcat-util-scan
org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el
org.apache.tomcat:tomcat-jasper-el
org.apache.tomcat:tomcat-el-api

Mortbay

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

Assorted

The UnixCrypt.java code implements the one way cryptography used by Unix systems for simple password protection. Copyright 1996 Aki Yoshida, modified April 2001 by Iris Van den Broeke, Daniel Deville.

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1.74 jetty-servlets 9.4.16.v20190411

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Eclipse

The following artifacts are EPL.

- * org.eclipse.jetty.orbit:org.eclipse.jdt.core

The following artifacts are EPL and ASL2.

- * org.eclipse.jetty.orbit:javax.security.auth.message

The following artifacts are EPL and CDDL 1.0.

- * org.eclipse.jetty.orbit:javax.mail.glassfish

Oracle

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

- * javax.servlet:javax.servlet-api
- * javax.annotation:javax.annotation-api
- * javax.transaction:javax.transaction-api
- * javax.websocket:javax.websocket-api

Oracle OpenJDK

If ALPN is used to negotiate HTTP/2 connections, then the following artifacts may be included in the distribution or downloaded when ALPN module is selected.

- * java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

OW2

The following artifacts are licensed by the OW2 Foundation according to the terms of <http://asm.ow2.org/license.html>

org.ow2.asm:asm-commons

org.ow2.asm:asm

Apache

The following artifacts are ASL2 licensed.

org.apache.taglibs:taglibs-standard-spec

org.apache.taglibs:taglibs-standard-impl

MortBay

The following artifacts are ASL2 licensed. Based on selected classes from following Apache Tomcat jars, all ASL2 licensed.

org.mortbay.jasper:apache-jsp

org.apache.tomcat:tomcat-jasper

org.apache.tomcat:tomcat-juli

org.apache.tomcat:tomcat-jsp-api

org.apache.tomcat:tomcat-el-api

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-api

org.apache.tomcat:tomcat-util-scan

org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-el-api

Mortbay

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

Assorted

The UnixCrypt.java code implements the one way cryptography used by Unix systems for simple password protection. Copyright 1996 Aki Yoshida, modified April 2001 by Iris Van den Broeke, Daniel Deville.

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1.75 sqlite 3.5.9

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1.76 json-glib 1.2.8

1.77 libnet 1.2

1.77.1 Available under license :

\$Id: COPYING,v 1.1.1.1 2003/06/26 21:55:10 route Exp \$

libnet 1.1.x

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1.78 ipmi-tool 1.8.18

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1.79 tcl 8.5.19

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1.80 libnettle6 3.4

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and Nathaniel Talbott <nathaniel@talbott.ws>.

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= Contributors to Ruby

The following list might be incomplete. Feel free to add your name if your
patch was accepted into Ruby.

== A

Ayumu AIZAWA (ayumin)

* committer

AKIYOSHI, Masamichi (akiyoshi)

* committer

* He had maintained the VMS support on 2003-2004.

Muhammad Ali

* wrote rdoc for Fiber

Minero Aoki (amine)

* committer

* He is the maintainer of:

* fileutils

* net/http, net/https

* net/pop

* net/smtp

* racc

* ripper

* strscan

Wakou Aoyama (wakou)

* committer

* He was the maintainer of some standard libraries.

Koji Arai

* committer

arton

* He is the distributor of ActiveScriptRuby and experimental 1.9.0-x installers for win32.

* Wrote patches for win32ole, gc.c, tmpdir.rb

Sergey Avseyev

* Added IO#pread and IO#pwrite.

== B

Daniel Berger

- * a patch for irb
- * documentation
- * He wrote forwardable.rb

David Black (dblack)

- * committer
- * He is the maintainer of scanf

Ken Bloom

- * a patch for REXML.

Oliver M. Bolzer

- * a patch for soap

Alexey Borzenkov

- * a patch
for mkmf.rb

Evan Brodie

- * a patch for documentation of Float#round

Richard Brown

- * a patch for configure.in

Dirkjan Bussink

- * a patch for date.rb

Daniel Bovensiepen

- * documentation
- * a patch for irb

== C

Brian Candler

- * a patch for configure.in, net/telnet

keith cascio

- * a patch for optparse.rb

Frederick Cheung

- * a patch for test/ruby/test_symbol.rb

Christoph

- * patches for set.rb

Sean Chittenden

- * patches for net/http, cgi

William D. Clinger

* ruby_strtod is based on his paper.

== D

Ryan Davis (ryan)

* committer

* He wrote and is the maintainer of miniunit

Guy Decoux (ts)

* committer

Zach Dennis

Martin Duerst (duerst)

* committer

* M17N

Paul Duncan

* patches for rdoc

Alexander Dymo

* a patch for lib/benchmark.rb

== E

Yusuke Endoh (mame)

* committer

* He wrote and is the maintainer of base64 library (1.9)

* did much upon YARV compiler.

erlercw

* wrote Integer::gcd2

== F

Frank S. Fejes

* a patch for net/pop

Fundakowski Feldman

* a patch for process.c

Mauricio Fernandez

*

patches for parse.y

David Flanagan (davidflanagan)

* committer

* M17N

Takeyuki Fujioka (xibbar)

* committer

* He is the maintainer of cgi/*

FUKUMOTO, Atsushi

* a patch for tracer.rb

Shota Fukumori (sorah)

* committer

* #4415 parallel unit/test

Tadayoshi Funaba (tadf)

* committer

* He wrote and is the maintainer of

* date

* parsedate (1.8)

* He ported rational.rb and complex.rb, which 1.8 contains, into rational.c and complex.c of 1.9.

== G

David M. Gay

* ruby_strtod

Florian Gilcher

* documentation

GOTOU, Kentaro (gotoken)

* committer

* He wrote benchmark.rb

* He is the maintainer of:

* benchmark.rb

* open3

GOTOU, Yuuzou (gotoyuzo)

* committer

James Edward Gray II (jeg2)

* committer

* He wrote the faster implementation of CSV and is the maintainer of csv.

* Wrote documentation for rdoc

== H

Phil Hagelberg

* patch for ruby-mode.el's documentation.

Kirk Haines (wyhaines)

- * committer
- * the maintainer of ruby_1_8_6 branch

Shinichiro Hamaji

- * fixed memory leaks
(marshal.c, string.c)

Shin-ichiro HARA

- * the developer and the sysop of ruby-{dev,list,core,talk} archive.
- * a patch for numeric.c

Chris Heath (traumdeutung)

- * a patch for proc.c

HIROKAWA Hisashi

- * fixed socket/socket.c

Daniel Hob

- * He wrote:
 - * SMTP-TLS support for net/smtp.
 - * POP3S support

Eric Hodel (drbrain)

- * committer
- * He is the maintainer of:
 - * rdoc
 - * ri
 - * rubygems

Erik Hollensbe

- * a patch for delegate.rb

Johan Holmberg

- * a patch for dir.c
- * documentation

Erik Huelsmann

Dae San Hwang

- * built a continuous integration environment on OpenSolaris.

== I

Nobuhiro IMAI

- * a patch for logger.rb

"incorporate"

* a patch for sprintf.c

Keiju Ishitsuka (keiju)

* committer

* He wrote and is the maintainer of:

* cmath.rb (1.9)

* complex.rb (1.8)

* e2mmap.rb

* forwardable.rb

* irb

* mathn

* matrix.rb

* mutex_m.rb

* rational.rb (1.8)

* sync.rb

* shell/*

* thwait.rb

* tracer.rb

== J

Curtis Jackson

* missing/dup2.c

Alan Johnson

* a patch for net/ftp

Lyle

Johnson

* patches for nkf, bigdecimal, numeric.c

== K

Yoshihiro Kambayashi

* a patch for enc/trans/single_byte.trans.

* He wrote supports for some encodings.

Yutaka Kanemoto

* patches for common.mk, AIX AF_INET6 support

Motoyuki Kasahara

* He wrote getoptlong.rb

Masahiro Kawato

* a patch for shellwords.rb

Wataru Kimura

* a patch for configure.in

Michael Klishin

- * patch for make help.

Noritada Kobayashi

- * a patch for optparse.rb

Shigeo Kobayashi (shigek)

- * committer
- * He is the maintainer of bigdecimal

KONISHI, Hiromasa (H_Konishi)

- * committer
- * He had maintained the bcc32 support in 2004.

Kornelius "murphy" Kalnbach

- * documentation

K.Kosako (kosako)

- * committer
- * He wrote Oniguruma.

Takehiro Kubo

- * patches for dl 64bit support.

== L

Marc-Andre Lafortune (marcandre)

- * committer
- * patches for hash.c, array.c, thread.c, enumc, string.c, range.c and rdoc documentation.

Hongli Lai

- * improved pstore.rb
- * patch for tool/file2lastrev.rb.

raspberry lemon

- * a patch for
webrick/httpproxy.rb.

Christian Loew

- * a patch for fileutils.rb

== M

Shugo Maeda (shugo)

- * committer
- * A system administrator of ruby-lang.org servers.
- * He wrote and is the maintainer of:

- * monitor.rb
- * net/ftp
- * net/imap

Stephan Maka (mathew)

- * documentation

Yukihiro Matsumoto (matz)

- * Matz -- the founder, language designer of Ruby.
- * committer
- * Ruby itself, most of Ruby.
- * He is the maintainer of:
 - * singleton
 - * timeout
 - * gdbm
 - * sdbm

Konrad Meyer

- * documentation

Mib Software

- * missing/vsnprintf.c

Todd C. Miller

- * missing/strlcat.c
- * missing/strncpy.c

MIYASAKA, Masaru

- * a patch for cgi.rb

Stefan Monnier

- * regex.c was fixed with based on his Emacs21 patch.

Marcel Moolenaar

- * patches for eval.c and gc.c.

moonwolf

- * a patch for REXML, xmlrpc

Hiroshi Moriyama

- * a patch for yaml.

Kyosuke Morohashi

- * a patch for gem_prelude.rb

Kenta Murata

- * patches for json, bignum.c

Akinori MUSHA (knu)

* committer

* He wrote and is the maintainer of:

* abbrev.rb

* generator

(1.8)

* enumerator (1.8)

* set

* ipaddr.rb

* digest/*

* syslog

* He is the branch maintainer of ruby_1_8, the release manager of 1.8 series.

== N

Hidetoshi NAGAI (nagai)

* committer

* He is the maintainer of tk/*

Nobuyoshi Nakada (nobu)

* committer

* a.k.a. the "patch monster"

* He wrote and is the maintainer of:

* optparse

* stringio

* io/wait

* iconv

Satoshi Nakagawa

* patches for util.c

Narihiro Nakamura (nari)

* committer

* a.k.a. authorNari

* working at GC

NAKAMURA, Hiroshi (nahi)

* committer

* He is the maintainer of:

* csv.rb (1.8)

* logger.rb

* soap/* (1.8)

* wsdl/* (1.8)

* xsd/* (1.8)

NAKAMURA, Usaku (usa)

* committer

* a.k.a. unak

* He is the maintainer of mswin32 and mswin64 support.

NARUSE, Yui (naruse)

* committer

* a.k.a. "nurse"

* Did much upon m17n.

* He is the maintainer of:

* json

* nkf

Christian Neukirchen

* a patch for webrick/httputils

Michael Neumann (mneumann)

* committer

* He is the maintainer of

* xmlrpc (1.8)

* gserver (1.8)

NISHIO Hirokazu

* wrote a patch for CVE-2010-0541

Kazuhiro NISHIYAMA (kazu)

* committer

* a.k.a. znz

Go Noguchi

Martin Nordholts

* misc/rdebug.el

nmu

* a patch for socket

== O

okkez

* He is a sysop of the Ruby Reference Manual Renewal Project.

* fixed ipaddr.rb, ext/etc

Haruhiko Okumura

* some of missing/* is based on his book:

* missing/erf.c

* missing/lgamma_r.c

* missing/tgamma.c

OMAE, jun

* a patch for debug.rb

Eugene Ossintsev

- * documentation

== P

Heesob Park

- * a patch for win32/win32.c.

pegacorn

- * a patch for instruby.rb

== Q

== R

Gaston Ramos

- * documentation

The Regents of the University of California

- * missing/crypt.c

- * missing/vsnprintf.c

Sam Roberts

- * patch for socket

- * documentation

Michal Rokos (michal)

- * committer

- * He was the maintainer of DJGPP support.

rubikitch

- * a patch for io.c

Marcus Rueckert

- * a patch for mkconfig.rb.

Run Paint Run Run

- * patch for enc/unicode.c

- * documentation

Sean Russell (ser)

- * committer

- * He wrote and

is the maintainer of REXML.

== S

Kazuo Saito (ksaito)

- * committer
- * M17N

Tadashi Saito

- * patches for test/ruby/test_math.rb, thread_*.c, bignum.c
- * working upon BigDecimal.
- * did much upon documentation

Masahiro Sakai

- * a patch for io.c

Laurent Sansonetti

- * a patch for tool/ytab.sed

Jeff Saracco

- * documentation

Koichi Sasada (ko1)

- * committer
- * He wrote YARV.

Hugh Sasse

- * a patch for net/http
- * documentation

Charlie Savage

- * a patch for win32/Makefile.sub

Michael Scholz

- * a patch for ruby-mode.el

Arthur Schreiber

- * patch for net/http and rdoc.

Masatoshi SEKI (seki)

- * committer
- * He wrote and is the maintainer of:
 - * drb/*
 - * erb
 - * rinda

Roman Shterenzon

- * a patch for open-uri.

Kent Sibilev

Gavin Sinclair (gsinclair)

* committer

John W. Small

* He wrote gserver.rb

Yuki Sonoda (yugui)

* committer

* She is the maintainer of man/* manual pages and is the release manager of 1.9 series.

* She wrote prime.rb.

* A developer and a sysop of redmine.ruby-lang.org.

SOUMA,

Yutaka

* a patch for pack.c.

Tatsuki Sugiura

* WebDAV support for net/http

Masaki Suketa (suke)

* committer

* He is the maintainer of win32ole

sheepman

* patches for ruby.c, thread.c, stringio, enum.c, webrick, net/http

Siena. (siena)

* committer

Kirill A. Shutemov

* a patch for parse.y

Darren Smith

* a patch for golf_prelude.rb

Richard M. Stallman

* missing/alloca.c

Robin Stocker

* documentation

Joshua Stowers

* a patch for array.c

Marcus Stollsteimer (stomar)

* committer

* a maintainer of www.ruby-lang.org

* patches for cgi (HTML5 tag maker), numeric.c, bigdecimal, ostruct.rb, prime.rb, and others

* documentation

Adam Strzelecki

- * a patch for compile.c

Masashi Sumi

- * improved net/pop.rb

Eric Sunshine

- * NeXT OpenStep, Rhapsody support

Kouhei Sutou (kou)

- * committer
- * He wrote and is the maintainer of rss/*

David Symonds

- * documentation

== T

TAKANO Mitsuhiro (takano32)

- * committer
- * He is the maintainer of IA-64 support.
- * BigDecimal

TAKAO, Kouji (kouji)

- * committer
- *

He is the maintainer of readline.

Nathaniel Talbott (ntalbott)

- * committer
- * He was the maintainer of test/unit, runit, rubyunit.

TANAKA, Akira (akr)

- * committer
- * Did much upon m17n.
- * And he is the maintainer of:
 - * open-uri
 - * pathname
 - * pp
 - * resolv-replace
 - * resolv
 - * time
 - * tsort

Takaaki Tateishi (ttate)

- * committer
- * He was the maintainer of dl

Technorama Ltd. (technorama)

- * committer
- * openssl

Andrew Thompson

- * a patch for socket.c IRIX support.

Dave Thomas (dave)

- * committer
- * a.k.a. the Pragmatic Programmer.
- * He wrote rdoc.

Tietew

- * patches for win32 support

Masahiro Tomita

- * a patch for cgi.rb

Jakub Travnik

- * a patch for eval.c

Tom Truscott

- * missing/crypt.c

== U

UEDA, Satoshi

- * a patch for uri

Takaaki Uematsu (uema2)

- * committer
- * He was the maintainer of WinCE support.

UENO, Katsuhiko (katsu)

- * committer
- * He is the maintainer of zlib

Hajimu UMEMOTO

- * He wrote ipaddr.rb

URABE, Shyouhei (shyouhei)

- * committer
- * a.k.a. mput.
- * He is the branch maintainer
of ruby_1_8_6 and ruby_1_8_7
* and is the release manager of 1.8.x-pXXX.

== V

Joel VanderWerf

* a patch for numeric.c

Peter Vanbroekhoven

Corinna Vinschen

== W

wanabe (wanabe)

* committer

* fixed YARV and Oniguruma.

Chun Wang

* a patch for time.rb

WATANABE, Hirofumi (eban)

* committer

* He is the maintainer of

* ftools (1.8)

* tmpdir

* un

* Win32API

WATANABE, Tetsuya

* a patch for ruby.c

William Webber (wew)

* committer

Jim Weirich (jim)

* committer

* He wrote Rake.

Nathan Weizenbaum

* fixed misc/ruby-mode.el.

why the lucky stiff (why)

* committer

* He is the maintainer of syck

Caley Woods

* documentation

Gary Wright

* documentation

== X

== Y

Akira Yamada (akira)

* committer

* He is the maintainer of ruby related packages at Debian project.

Keita Yamaguchi

* patches for enum.c, parse.y

* documentation

Hirokazu Yamamoto (ocean)

* committer

Hirotaaka Yoshioka

* a patch for improving SEGV handling

== Z

Aristarkh A Zagorodnikov

* a patch for io.c

Alexander

Zavorine

* committer

* He is the maintainer for Symbian OS.

Chiyuan Zhang

* a patch for misc/ruby-mode.el.

Dee Zsombor (zunda)

* a patch for thread_pthread.c

Dan Zwell

* a patch for net/pop

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include/ruby/onigmo.h::

include/ruby/oniguruma.h::

regcomp.c::

regenc.c::

regenc.h::

regerror.c::

regex.c::

regint.h::

regparse.c::

regparse.h::

enc/ascii.c::

enc/big5.c::

enc/cp949.c::

enc/emacs_mule.c::

enc/encdb.c::

enc/euc_jp.c::

enc/euc_kr.c::

enc/euc_tw.c::

enc/gb18030.c::

enc/gb2312.c::

enc/gbk.c::
enc/iso_8859_1.c::
enc/iso_8859_10.c::
enc/iso_8859_11.c::
enc/iso_8859_13.c::
enc/iso_8859_14.c::
enc/iso_8859_15.c::
enc/iso_8859_16.c::
enc/iso_8859_2.c::
enc/iso_8859_3.c::
enc/iso_8859_4.c::
enc/iso_8859_5.c::
enc/iso_8859_6.c::
enc/iso_8859_7.c::
enc/iso_8859_8.c::
enc/iso_8859_9.c::
enc/koi8_r.c::
enc/koi8_u.c::
enc/shift_jis.c::
enc/unicode.c::
enc/us_ascii.c::
enc/utf_16be.c::
enc/utf_16le.c::
enc/utf_32be.c::
enc/utf_32le.c::
enc/utf_8.c::
enc/windows_1251.c::

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enc/trans/GB/UCS%GB12345.src::

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enc/trans/JIS/UCS%JISX0201-KANA.src::

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enc/trans/JIS/UCS@BMP%JISX0213-2.src::

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>>>

A C-program for MT19937, with initialization improved 2002/2/10.

Coded by Takuji Nishimura and Makoto Matsumoto.

This is a faster version by taking Shawn Cokus's optimization,
Matthe Bellew's simplification, Isaku Wada's real version.

Before using, initialize the state by using `init_genrand(seed)`

or `init_by_array(init_key, key_length)`.

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<http://www.math.keio.ac.jp/matsumoto/emt.html>

email: matumoto@math.keio.ac.jp

The Wayback Machine url:

<http://web.archive.org/web/19990429082237/http://www.math.keio.ac.jp/matsumoto/emt.html>

`vm_dump.c:procstat_vm::`

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\$FreeBSD: head/usr.bin/procstat/procstat_vm.c 261780 2014-02-11 21:57:37Z jhb \$

vsnprintf.c::

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missing/memmove.c::
missing/strchr.c::
missing/strerror.c::
missing/strstr.c::
missing/tgamma.c::
ext/date/date_strftime.c::
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(Arranged for libc by Todd C. Miller)

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1.85 less 551

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{signature of Ty Coon}, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

Format: <http://www.debian.org/doc/packaging-manuals/copyright-format/1.0/>

Upstream-Name: ls-config

Source: <https://github.com/lucas-net-pl/ls-config>

Files: *

Copyright: 2013 ukasz A. Grabowski <www@lucas.net.pl>

License: GPL-2.0+

Files: debian/*

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different copyright/license attached and list them here.

Please avoid to pick license terms that are more restrictive than the

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@c \input texinfo.tex @c -*-texinfo-*-

@c

@c %**start of header

@c All text is ignored before the setfilename.

@setfilename LGPL.info

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@center Version 2.1, February 1999

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@center Preamble

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<signature of Ty Coon>, 1 April 1990
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1.92 pcre 8.35

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=====

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1.94 postgres-sql 7.4.2

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1.98 libidn 1.35

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1.99 libpcap 0.8.3

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1.100 libpcap 1.9.1

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1.101 libxml2 2.9.10

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1.102 Isof 4.93.2

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```
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```

<signature of Ty Coon>, 1 April 1989

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Dustin Sallings <dustin@spy.net>

Jason Mooberry <jasonmoo@me.com>

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1.108 xerces-j 2.9.1

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1.109 bouncycastle-fips 1.0.2

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1.110 ncurses 6.2

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1.111 snake-yaml 1.26

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1.112 logrus 1.5.0

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1.113 xz 5.2.5

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ghost@aladdin.com

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Upstream-Contact: Andy Cress <arcress@users.sourceforge.net>

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1.116 properties 1.8.1

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1.117 quartz 2.3.2

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1.118 mitchellh-mapstructure 1.3.0

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1.119 goleveldb 1.0.0

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1.120 cobra 1.0.0

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1.121 go-playground-validator 10.2.0

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1.122 gorilla 1.7.4

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Dave Cheney <dave@cheney.net>

Saulius Gurklys <s4uliu5@gmail.com>

John Eikenberry <jae@zhar.net>

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procfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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Anton Povarov <anton.povarov@gmail.com>

Brian Goff <cpuguy83@gmail.com>

Clayton Coleman <ccoleman@redhat.com>

Denis Smirnov <denis.smirnov.91@gmail.com>

DongYun Kang <ceram1000@gmail.com>

Dwayne Schultz <dschultz@pivotal.io>

Georg Apitz <gapitz@pivotal.io>

Gustav Paul <gustav.paul@gmail.com>

Johan Brandhorst <johan.brandhorst@gmail.com>

John Shahid <jvshahid@gmail.com>

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#

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#

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#

Please keep the list sorted.

#

Use the following command to generate the list

#

git shortlog -se | awk '{print \$2 " " \$3 " " \$4}'

#

and do appropriate cleanups.

DanB <danb@cgrates.org>

David Wilkie <dwilkie@gmail.com>

Seyi Ogunyemi <micrypt@users.noreply.github.com>

Tapio <fitase@emblacom.com>

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```

```
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```

```
Damian Gryski <dgryski@gmail.com>
```

```
Jan Mercl <0xjnm@gmail.com>
```

```
Kai Backman <kaib@golang.org>
```

```
Marc-Antoine Ruel <maruel@chromium.org>
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compiler/cpp/src/generate/t_rb_generator.cc
compiler/cpp/src/generate/t_st_generator.cc
compiler/cpp/src/generate/t_xsd_generator.cc
compiler/cpp/src/main.cc
compiler/cpp/src/parse/t_field.h
compiler/cpp/src/parse/t_program.h
compiler/cpp/src/platform.h
compiler/cpp/src/thriftl.ll
compiler/cpp/src/thrifty.yy
lib/csharp/src/Protocol/TBinaryProtocol.cs
lib/csharp/src/Protocol/TField.cs
lib/csharp/src/Protocol/TList.cs
lib/csharp/src/Protocol/TMap.cs
lib/csharp/src/Protocol/TMessage.cs

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lib/csharp/src/Protocol/TProtocolException.cs
lib/csharp/src/Protocol/TProtocolFactory.cs
lib/csharp/src/Protocol/TProtocolUtil.cs
lib/csharp/src/Protocol/TSet.cs
lib/csharp/src/Protocol/TStruct.cs
lib/csharp/src/Protocol/TType.cs
lib/csharp/src/Server/TServer.cs
lib/csharp/src/Server/TSimpleServer.cs
lib/csharp/src/Server/TThreadPoolServer.cs
lib/csharp/src/TApplicationException.cs
lib/csharp/src/Thrift.csproj
lib/csharp/src/Thrift.sln
lib/csharp/src/TProcessor.cs
lib/csharp/src/Transport/TServerSocket.cs
lib/csharp/src/Transport/TServerTransport.cs
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lib/csharp/src/Transport/TStreamTransport.cs
lib/csharp/src/Transport/TTransport.cs
lib/csharp/src/Transport/TTransportException.cs
lib/csharp/src/Transport/TTransportFactory.cs
lib/csharp/ThriftMSBuildTask/Properties/AssemblyInfo.cs
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lib/rb/lib/thrift.rb
lib/st/README
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test/OptionalRequiredTest.cpp
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Go support for Protocol Buffers - Google's data interchange format

<http://github.com/golang/protobuf/>

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Support for streaming Protocol Buffer messages for the Go language (golang).

https://github.com/matttproud/golang_protobuf_extensions

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Paul Borman <borman@google.com>

bmatsuo

shawnps

theory

jboverfelt

dsymonds

cd1

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Protocol Buffers for Go with Gadgets

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The following individuals contributed in part to the Network Time Protocol Distribution

Version 4 and are acknowledged as authors of this work.

1. [1]Takao Abe <takao_abe@xurb.jp> Clock driver for JJY receivers
2. [2]Mark Andrews <mark_andrews@isc.org> Leitch atomic clock controller
3. [3]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
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6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
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11. [12]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support
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17. [18]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
18. [19]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
19. [20]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing
20. [21]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite
21. [22]Claas Hilbrecht <neoclock4x@linum.com> NeoClock4X clock driver
22. [23]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
23. [24]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
24. [25]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
25. [26]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
26. [27]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or [28]<H.Lambermont@chello.nl> ntpsweep
27. [29]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
28. [30]Frank Kardel [31]<kardel (at) ntp (dot) org> PARSE <GENERIC> (driver 14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
29. [32]Johannes Maximilian Kuehn <kuehn@ntp.org> Rewrote sntp to comply with NTPv4 specification, ntpq saveconfig
30. [33]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
31. [34]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
32. [35]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
33. [36]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
34. [37]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
35. [38]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
36. [39]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
37. [40]David L. Mills <mills@udel.edu> Version 4 foundation, precision kernel; clock drivers: 1, 3, 4, 6, 7, 11, 13, 18, 19, 22, 36
38. [41]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
39. [42]Jeffrey Mogul <mogul@pa.dec.com> ntprtrace utility
40. [43]Tom Moore <tmoore@fieval.daytonoh.ncr.com> i386 svr4 port
41. [44]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
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44. [48]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de>
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 49. [53]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
 50. [54]Michael Shields <shields@tembel.org> USNO clock driver
 51. [55]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
 52. [56]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure
makeover, various other bits (see the ChangeLog)
 53. [57]Kenneth Stone <ken@sdd.hp.com> HP-UX port
 54. [58]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
 55. [59]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK
clock driver
 56. [60]Brian Utterback <brian.utterback@oracle.com> General codebase,
Solaris issues
 57. [61]Loganaden Velvindron <loganaden@gmail.com> Sandboxing
(libseccomp) support
 58. [62]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic
TrueTime clock driver
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validated HTML documents according to the HTML DTD
-

References

1. mailto:%20takao_abe@xurb.jp
2. mailto:%20mark_andrews@isc.org
3. <mailto:%20altmeier@atsoft.de>
4. <mailto:%20vbais@mailman1.intel.co>
5. <mailto:%20kirkwood@striderfm.intel.com>
6. <mailto:%20michael.barone@lmco.com>
7. <mailto:%20karl@owl.HQ.ileaf.com>
8. <mailto:%20greg.brackley@bigfoot.com>
9. <mailto:%20Marc.Brett@westgeo.com>
10. <mailto:%20Piete.Brooks@cl.cam.ac.uk>
11. <mailto:%20nelson@bolyard.me>
12. <mailto:%20Jean-Francois.Boudreault@viagenie.qc.ca>
13. <mailto:%20reg@dwf.com>
14. <mailto:%20clift@ml.csiro.au>
15. <mailto:%20casey@csc.co.za>
16. mailto:%20Sven_Dietrich@trimble.COM
17. <mailto:%20dundas@salt.jpl.nasa.gov>
18. <mailto:%20duwe@immd4.informatik.uni-erlangen.de>
19. <mailto:%20dennis@mrbill.canet.ca>
20. <mailto:%20jhay@icomtek.csiro.co.za>

21. mailto:%20davehart@davehart.com
22. mailto:%20neoclock4x@linum.com
23. mailto:%20glenn@herald.usask.ca
24. mailto:%20iglesias@uci.edu
25. mailto:%20jagubox.gsfc.nasa.gov
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37. mailto:%20louie@ni.umd.edu
38. mailto:%20thorinn@diku.dk
39. mailto:%20mayer@ntp.org
40. mailto:%20mills@udel.edu
41. mailto:%20moeller@gwdgv1.dnet.gwdg.de
42. mailto:%20mogul@pa.dec.com
43. mailto:%20tmoore@fielvel.daytonoh.ncr.com
44. mailto:%20kamal@whence.com
45. mailto:%20derek@toybox.demon.co.uk
46. mailto:%20d@hd.org
47. mailto:%20neal@ntp.org
48. mailto:%20Rainer.Pruy@informatik.uni-erlangen.de
49. mailto:%20dirce@zk3.dec.com
50. mailto:%20wsanchez@apple.com
51. mailto:%20mrapple@quack.kfu.com
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59. mailto:%20tsuruoka@nc.fukuoka-u.ac.jp
60. mailto:%20brian.utterback@oracle.com
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1.132 jersey 2.28

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1.134 openssh 8.3p1

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* @version 3.0 (December 2000)

*

* Optimised ANSI C code for the Rijndael cipher (now AES)

*

* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

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Font	License	Distinguishing feature
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Image	License	Distinguishing feature
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1.136 bouncycastle-fips 1.0.3

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```
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 * <p>
 * The authorizer attempts to perform matching (including the use of the wildcard) in accordance with RFC 6125.
 * </p>
 * <p>
 * Known suffixes is a list of public domain suffixes that can't be used as wild cards for
 * example *.com, or c*c.com, as a dns wildcard could match every/most .com domains if a registrar were issue it.
 * If *.com is in the known suffixes list will not be allowed to match.
 * </p>
 *
 * @param knownSuffixes a set of suffixes that cannot be wild-carded, e.g. { ".com", ".net", ".org" }
 */
```

Found in path(s):

```
* /opt/ws_local/PERMITS_SQL/1089947271_1601369618.24/0/bcpkix-fips-1-0-3-sources-
jar/org/bouncycastle/est/jcajce/JsseDefaultHostnameAuthorizer.java
```

1.137 libevent 2.1.12

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```
=====
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1.138 flex 2.6.4

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* org.eclipse.jetty.orbit:javax.security.auth.message

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<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

* javax.servlet:javax.servlet-api

* javax.annotation:javax.annotation-api

* javax.transaction:javax.transaction-api

* javax.websocket:javax.websocket-api

Oracle OpenJDK

If ALPN is used to negotiate HTTP/2 connections, then the following artifacts may be included in the distribution or downloaded when ALPN module is selected.

* java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

OW2

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org.ow2.asm:asm-commons

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Apache

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org.apache.taglibs:taglibs-standard-spec

org.apache.taglibs:taglibs-standard-impl

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org.mortbay.jasper:apache-jsp
org.apache.tomcat:tomcat-jasper
org.apache.tomcat:tomcat-juli
org.apache.tomcat:tomcat-jsp-api
org.apache.tomcat:tomcat-el-api
org.apache.tomcat:tomcat-jasper-el
org.apache.tomcat:tomcat-api
org.apache.tomcat:tomcat-util-scan
org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el
org.apache.tomcat:tomcat-jasper-el
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org.eclipse.jetty.toolchain:jetty-schemas

Assorted

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1.151 traceback2 1.4.0

1.151.1 Available under license :

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Metadata-Version: 1.1

Name: traceback2

Version: 1.4.0

Summary: Backports of the traceback module

Home-page: <https://github.com/testing-cabal/traceback2>

Author: Testing-cabal

Author-email: testing-cabal@lists.launchpad.net

License: UNKNOWN

Description: A backport of traceback to older supported Pythons.

```
>>> import traceback2 as traceback
```

Profit.

Things to be aware of!

In Python 2.x, unlike traceback, traceback2 creates unicode output (because it depends on the linecache2 module).

Exception frame clearing silently does nothing if the interpreter in use does not support it.

traceback2._some_str, which while not an official API is so old its likely in use behaves similarly to the Python3 version - objects where unicode(obj) fails but str(obj) works will be shown as b'thestrvaluerepr'.

Platform: UNKNOWN
Classifier: Development Status :: 6 - Mature
Classifier: Intended Audience :: Developers
Classifier: License :: OSI Approved :: Python Software Foundation License
Classifier: Operating System :: OS Independent
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.6
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
Classifier: Programming Language :: Python :: 3.2
Classifier: Programming Language :: Python :: 3.3
Classifier: Programming Language :: Python :: 3.4
Classifier: Topic :: Software Development

Found in path(s):

* /opt/cola/permits/1110814192_1606854315.91/0/traceback2-1-4-0-1-tar-gz/traceback2-1.4.0/traceback2.egg-info/PKG-INFO
* /opt/cola/permits/1110814192_1606854315.91/0/traceback2-1-4-0-1-tar-gz/traceback2-1.4.0/PKG-INFO

1.152 linecache 1.0.0

1.152.1 Available under license :

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Metadata-Version: 1.1
Name: linecache2
Version: 1.0.0
Summary: Backports of the linecache module
Home-page: <https://github.com/testing-cabal/linecache2>
Author: Testing-cabal
Author-email: testing-cabal@lists.launchpad.net
License: UNKNOWN
Description: A backport of linecache to older supported Pythons.

```
>>> import linecache2 as linecache
```

Profit.

Platform: UNKNOWN
Classifier: Development Status :: 6 - Mature
Classifier: Intended Audience :: Developers
Classifier: License :: OSI Approved :: Python Software Foundation License
Classifier: Operating System :: OS Independent

Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.6
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
Classifier: Programming Language :: Python :: 3.2
Classifier: Programming
Language :: Python :: 3.3
Classifier: Programming Language :: Python :: 3.4
Classifier: Topic :: Software Development

Found in path(s):

* /opt/cola/permits/1110814177_1606854861.13/0/linecache2-1-0-0-1-tar-gz/linecache2-1.0.0/linecache2.egg-info/PKG-INFO
* /opt/cola/permits/1110814177_1606854861.13/0/linecache2-1-0-0-1-tar-gz/linecache2-1.0.0/PKG-INFO

1.153 pyparsing 2.4.7

1.153.1 Available under license :

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under certain conditions; type 'show c' for details.
```

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"""
```

1.154 idna 2.10

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Found in path(s):

```
* /opt/cola/permits/1110905341_1606871385.15/0/kjd-idna-v2-10-0-ge5c11db-1-tar-gz/kjd-idna-b8defb9/LICENSE.rst
```

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```
"""
```

A library to support the Internationalised Domain Names in Applications (IDNA) protocol as specified in RFC 5890 et.al. This new methodology, known as IDNA 2008, can generate materially different results to the previous standard. The library can act as a drop-in replacement for the "encodings.idna" module.

```
"""
```

```
import io, sys
from setuptools import setup
```

```
def main():
```

```
    python_version = sys.version_info[:2]
    if python_version < (2,7):
        raise SystemExit("Sorry, Python 2.7 or newer required")
```

```
    package_data = {}
    exec(open('idna/package_data.py').read(), package_data)
```

```
    arguments = {
        'name': 'idna',
        'packages': ['idna'],
        'version': package_data['__version__'],
        'description': 'Internationalized Domain Names in Applications (IDNA)',
        'long_description': io.open("README.rst", encoding="UTF-8").read(),
        'author': 'Kim Davies',
        'author_email': 'kim@cynosure.com.au',
        'license': 'BSD-like',
        'url': 'https://github.com/kjd/idna',
        'classifiers': [
            'Development Status :: 5 - Production/Stable',
            'Intended Audience :: Developers',
            'Intended Audience :: System Administrators',
            'License :: OSI Approved :: BSD License',
            'Operating System :: OS Independent',
            'Programming Language :: Python',
            'Programming Language :: Python :: 2',
            'Programming Language :: Python :: 2.7',
            'Programming Language :: Python :: 3',
            'Programming Language :: Python :: 3.4',
            'Programming Language :: Python :: 3.5',
```

```

'Programming Language :: Python :: 3.6',
'Programming Language :: Python :: 3.7',
'Programming Language :: Python :: 3.8',
'Programming Language :: Python :: Implementation :: CPython',
'Programming Language :: Python :: Implementation :: PyPy',
'Topic :: Internet :: Name Service (DNS)',
'Topic :: Software Development :: Libraries :: Python Modules',
'Topic :: Utilities',
],
'python_requires': '>=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*',
}

```

```

setup(**arguments)

```

```

if __name__ == '__main__':
    main()

```

Found in path(s):

* /opt/cola/permits/1110905341_1606871385.15/0/kjd-idna-v2-10-0-ge5c11db-1-tar-gz/kjd-idna-b8defb9/setup.py

1.155 linenoise 1.0

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1.156 snowball-stemmer 1.2.1

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Snowball stemming library collection for Python

=====

This document pertains to the Python version of the stemmer library distribution, available for download from:

* <https://github.com/shibukawa/snowball/>

This repository includes Python source code generator

* https://github.com/shibukawa/snowball_python/

This repository includes generated Python source codes

Original program is maintained at following place:

* <http://snowball.tartarus.org/>

Original Snowball product created by Dr Martin Porter and Richard Boulton (Java porting). Original Snowball and my products are released under BSD license.

How to use library

The ``snowballstemmer`` module has two functions.

The ``snowballstemmer.algorithms`` function returns a list of available algorithm name' string.

The ``snowballstemmer.stemmer`` function accepts algorithm name and returns ``Stemmer`` objects.

``Stemmer`` objects have ``Stemmer.stemWord(word)`` method and ``Stemmer.stemWords(word[])`` method.

.. code-block:: python

```
import snowballstemmer
```

```
stemmer = snowballstemmer.stemmer('english');  
print(stemmer.stemWords("We are the world".split()));
```


`Stemmer` objects have `Stemmer.maxCacheSize` property. They cache result within the value. Default is `10000`.

Accerarates Stemming

if `PyStemmer` is installed, `snowballstemmer.stemmer` returns `PyStemmer`'s `Stemmer` objects. This `Stemmer` object has same methods (`Stemmer.stemWord()`, `Stemmer.stemWords()`).

`PyStemmer` is a Snowball's `libstemmer_c` wrapper module and it returns 100% compatible result with `snowballstemmer`.

`PyStemmer` has faster speed because it uses C-lang module, and `snowballstemmer` has higher usability because it is pure Python module.

* `PyStemmer` <<http://pypi.python.org/pypi/PyStemmer/>>`_

Benchmark

~~~~~

Test Case: Snowball stemmer

check data (16 algorithms, total 582560 words, cache hit 0%)

Computer: MacBook Pro 3rd Gen Corei7 2.3GHz

\* Python 2.7 + `snowballstemmer` : 2m 30s

\* PyPy 1.9 + `snowballstemmer` : 45s

\* Python 2.7 + `PyStemmer` : 5s

This test case is much harder than usual usecases!

The TestApp example

-----

The `testapp.py` example program allows you to run any of the stemmers on a sample vocabulary.

Usage::

```
testapp.py <algorithm> "sentences ... "
```

.. code-block:: bash

```
$ python testapp.py English "sentences... "
```

Thanks

-----

\* Original Snowball authors

\* Emil Stenström

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\*

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Metadata-Version: 1.1

Name: snowballstemmer

Version: 1.2.1

Summary: This package provides 16 stemmer algorithms (15 + Porter English stemmer) generated from Snowball algorithms.

Home-page: [https://github.com/shibukawa/snowball\\_py](https://github.com/shibukawa/snowball_py)

Author: Yoshiki Shibukawa

Author-email: yoshiki at shibu.jp

License: BSD

Description:

It includes following language algorithms:

- \* Danish
- \* Dutch
- \* English (Standard, Porter)
- \* Finnish
- \* French
- \* German
- \* Hungarian
- \* Italian
- \* Norwegian
- \* Portuguese
- \* Romanian
- \* Russian
- \* Spanish
- \* Swedish
- \* Turkish

This is a pure Python stemming library. If `PyStemmer` <<http://pypi.python.org/pypi/PyStemmer>>`\_ is available, this module uses it to accelerate.

Keywords: stemmer

Platform: UNKNOWN

Classifier: Development Status :: 5 - Production/Stable

Classifier:

Intended Audience :: Developers

Classifier: License :: OSI Approved :: BSD License

Classifier: Programming Language :: Python

Classifier: Natural Language :: Danish

Classifier: Natural Language :: Dutch

Classifier: Natural Language :: English

Classifier: Natural Language :: Finnish

Classifier: Natural Language :: French

Classifier: Natural Language :: German

Classifier: Natural Language :: Hungarian

Classifier: Natural Language :: Italian

Classifier: Natural Language :: Norwegian

Classifier: Natural Language :: Portuguese

Classifier: Natural Language :: Romanian

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Classifier: Natural Language :: Spanish

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Classifier: Natural Language :: Turkish

Classifier: Operating System :: OS Independent

Classifier: Programming Language :: Python :: 2.5  
Classifier: Programming Language :: Python :: 2.6  
Classifier: Programming Language :: Python :: 2.7  
Classifier: Programming Language :: Python :: 3.3  
Classifier:  
Programming Language :: Python :: 3.4  
Classifier: Programming Language :: Python :: Implementation :: PyPy  
Classifier: Topic :: Database  
Classifier: Topic :: Internet :: WWW/HTTP :: Indexing/Search  
Classifier: Topic :: Text Processing :: Indexing  
Classifier: Topic :: Text Processing :: Linguistic

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# 1.157 image-size 1.1.0

## 1.157.1 Available under license :

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imagesize

=====

.. image:: [https://travis-ci.org/shibukawa/imagesize\\_py.svg?branch=master](https://travis-ci.org/shibukawa/imagesize_py.svg?branch=master)  
:target: [https://travis-ci.org/shibukawa/imagesize\\_py](https://travis-ci.org/shibukawa/imagesize_py)

This module analyzes JPEG/JPEG 2000/PNG/GIF/TIFF image headers and returns image size.

.. code:: python

```
import imagesize

width, height = imagesize.get("test.png")
print(width, height)
```

This module is a pure Python module.

API

-----

\* ``imagesize.get(filepath)``

Returns image size (width, height).

Benchmark

-----

It only parses headers, and ignores pixel data. So it is much faster than Pillow.

```
.. list-table::  
:header-rows: 1  
  
- * module  
  * result  
- * imagesize (pure Python)  
  * 1.077 seconds per 100 000 times  
- * Pillow  
  * 10.569 seconds per 100 000 times
```

I tested on MacBookPro (2014/Core i7) with 125kB PNG files.

#### Restriction

-----

\* TIFF

It can return only first picture's size because of restriction of API design.

It supports only small TIFF file. BigTIFF support is not implemented.

#### Development

-----

Run test with the following command:

```
.. code:: bash  
  
python -m unittest
```

#### License

-----

MIT License

#### Thanks

-----

I referred to the following code:

\* <http://markasread.net/post/17551554979/get-image-size-info-using-pure-python-code>  
\* <https://stackoverflow.com/questions/8032642/how-to-obtain-image-size-using-standard-python-class-without-using-external-lib>

I use sample image from here:

\* <https://www.nightprogrammer.org/development/multipage-tiff-example-download-test-image-file/>

Thank you for feedback:

- \* tk0miya (<https://github.com/tk0miya>)
- \* shimizukawa (<https://github.com/shimizukawa>)
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- \* Ivan Zakharyashev (<https://github.com/imz>)
- \* Jon Dufresne (<https://github.com/jdufresne>)
- \* Geoff Lankow (<https://github.com/darktrojan>)
- \* Hugo (<https://github.com/hugovk>)

Found in path(s):

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Found in path(s):

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Metadata-Version: 1.2

Name: imagesize

Version: 1.1.0

Summary: Getting image size from png/jpeg/jpeg2000/gif file

Home-page: [https://github.com/shibukawa/imagesize\\_py](https://github.com/shibukawa/imagesize_py)

Author: Yoshiki Shibukawa

Author-email: [yoshiki@shibu.jp](mailto:yoshiki@shibu.jp)

License: MIT

Description:

It parses image files' header and return image size.

\* PNG

\* JPEG

\* JPEG2000

\* GIF

\* TIFF (experimental)

This is a pure Python library.

Platform: UNKNOWN

Classifier: Development Status :: 5 - Production/Stable

Classifier: Intended Audience :: Developers

Classifier: License :: OSI Approved :: MIT License

Classifier: Programming Language :: Python

Classifier: Operating System :: OS Independent

Classifier: Programming Language :: Python

Classifier: Programming Language :: Python :: 2

Classifier: Programming Language :: Python :: 2.7

Classifier: Programming Language :: Python :: 3

Classifier: Programming

Language :: Python :: 3.4

Classifier: Programming Language :: Python :: 3.5

Classifier: Programming Language :: Python :: 3.6

Classifier: Programming Language :: Python :: 3.7

Classifier: Programming Language :: Python :: Implementation :: CPython

Classifier: Programming Language :: Python :: Implementation :: PyPy

Classifier: Topic :: Multimedia :: Graphics

Requires-Python: >=2.7, !=3.0.\*, !=3.1.\*, !=3.2.\*, !=3.3.\*

Found in path(s):

\* /opt/cola/permits/1111648332\_1606948805.44/0/imagesize-1-1-0-tar-gz/imagesize-1.1.0/imagesize.egg-info/PKG-INFO

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# 1.158 orderreddict 1.1

## 1.158.1 Available under license :

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# 1.159 funcsigns 1.0.2

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# 1.160 ip-address 1.0.23

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## 1.161 packaging 19.2

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## 1.162 cryptography 2.9.2

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## 1.165 jackson-xc 2.9.8

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# 1.166 icu 68

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## Commands to generate dependency files
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE= -Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation
```

```

rules
%. $(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%. $(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s^\($*\)\.o[ :]*\^1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s^\($*\)\.o[ :]*\^1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

## Versioned libraries rules

%. $(SO).$(SO_TARGET_VERSION_MAJOR): %. $(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%. $(SO): %. $(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}. $(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

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## 1.168 go-playground-validator

### 9.31.0+incompatible

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## 1.169 python-certifi 2020.12.5

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This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php\_curl, or with an Apache+mod\_ssl webserver for SSL client authentication. Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$

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## 1.170 curl 7.74.0

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## 1.172 pcre 10.36

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THE BASIC LIBRARY FUNCTIONS

-----

Written by: Philip Hazel  
Email local part: Philip.Hazel  
Email domain: gmail.com

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Cambridge,  
England.

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Written by: Zoltan Herczeg

Email local part: hzmester

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## 1.173 zstd 1.4.8

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## 1.174 llvm 11.0.1

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; NOTE: Assertions have been autogenerated by utils/update\_llc\_test\_checks.py

; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

```
define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
; CHECK-LABEL: PR41535:
; CHECK:      // %bb.0:
; CHECK-NEXT:  ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT:  mov v0.d[1], v0.d[0]
; CHECK-NEXT:  ret
%cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
%cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
%r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
ret <4 x i32> %r
}
```

; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s

;

; Test that early if conversion produces LOCR with operands of the right

; register classes.

```
define void @autogen_SD4739(i8*) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
%L34 = load i8, i8* %0
%Cmp56 = icmp sgt i8 undef, %L34
br label %CF246

CF246:                                ; preds = %CF246, %BB
%S1163 = select i1 %Cmp56, i8 %L34, i8 undef
br i1 undef, label %CF246, label %CF248

CF248:                                ; preds = %CF248, %CF246
store i8 %S1163, i8* %0
br label %CF248
}
```

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; NOTE: Assertions have been autogenerated by utils/update\_llc\_test\_checks.py

; RUN: llc < %s -mtriple=x86\_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s

;

; D31946

; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.

; Additionally ensure that the output code actually put fp128 values in SSE registers.

```

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestSelect:
; CHECK:      # %bb.0:
; CHECK-NEXT: pushq %rbx
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: subq $32, %rsp
; CHECK-NEXT: .cfi_def_cfa_offset 48
; CHECK-NEXT: .cfi_offset %rbx, -16
; CHECK-NEXT: movaps %xmm1, {[[-0-9]+]}(%r{[sb]})p) # 16-byte Spill
; CHECK-NEXT: movaps %xmm0, (%rsp) # 16-byte Spill
; CHECK-NEXT: callq __gttf2
; CHECK-NEXT: movl %eax, %ebx
; CHECK-NEXT: movaps (%rsp), %xmm0 # 16-byte Reload
; CHECK-NEXT: movaps {[[-0-9]+]}(%r{[sb]})p),
    %xmm1 # 16-byte Reload
; CHECK-NEXT: callq __subtf3
; CHECK-NEXT: testl %ebx, %ebx
; CHECK-NEXT: jg .LBB0_2
; CHECK-NEXT: # %bb.1:
; CHECK-NEXT: xorps %xmm0, %xmm0
; CHECK-NEXT: .LBB0_2:
; CHECK-NEXT: addq $32, %rsp
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: popq %rbx
; CHECK-NEXT: .cfi_def_cfa_offset 8
; CHECK-NEXT: retq
    %cmp = fcmp ogt fp128 %a, %b
    %sub = fsub fp128 %a, %b
    %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
    ret fp128 %res
}

define fp128 @TestFabs(fp128 %a) {
; CHECK-LABEL: TestFabs:
; CHECK:      # %bb.0:
; CHECK-NEXT: andps {[.*]}(%rip), %xmm0
; CHECK-NEXT: retq
    %res = call fp128 @llvm.fabs.f128(fp128 %a)
    ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestCopysign:
; CHECK:      # %bb.0:

```

```

; CHECK-NEXT: andps {{.*}}(%rip), %xmm1
; CHECK-NEXT: andps {{.*}}(%rip), %xmm0
; CHECK-NEXT: orps %xmm1, %xmm0
; CHECK-NEXT: retq
%res = call
fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {
; CHECK-LABEL: TestFneg:
; CHECK: # %bb.0:
; CHECK-NEXT: pushq %rax
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: movaps %xmm0, %xmm1
; CHECK-NEXT: callq __multf3
; CHECK-NEXT: xorps {{.*}}(%rip), %xmm0
; CHECK-NEXT: popq %rax
; CHECK-NEXT: .cfi_def_cfa_offset 8
; CHECK-NEXT: retq
%mul = fmul fp128 %a, %a
%res = fsub fp128 0xL00000000000000008000000000000000, %mul
ret fp128 %res
}

; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
ret void
}

define internal void @innerNoAttribute() {
ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
call void @innerLarge()
ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
call void @innerLarge()
}

```

```

ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
  call void @innerSmall()
  ret void
}

```

; We should remove the attribute after inlining since the callee's  
vector width requirements are unknown.

```

define void @outerAttribute() "min-legal-vector-width"="128" {
  call void @innerNoAttribute()
  ret void
}

```

```

; CHECK: define void @outerNoAttribute() {
; CHECK:
define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }

```

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```

; RUN: llc -march=hexagon < %s

```

```

; REQUIRES: asserts

```

```

; The two loads based on %struct.0, loading two different data types

```

; cause LSR to assume type "void" for the memory type. This would then  
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

```
target triple = "hexagon"
```

```
%struct.0 = type { i8*, i8, %union.anon.0 }
```

```
%union.anon.0 = type { i8* }
```

```
define hidden fastcc void @fred() unnamed_addr #0 {
```

```
entry:
```

```
br i1 undef, label %while.end, label %while.body.lr.ph
```

```
while.body.lr.ph:                ; preds = %entry
```

```
br label %while.body
```

```
while.body:                        ; preds = %exit.2, %while.body.lr.ph
```

```
%lshr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
```

```
switch i32 undef, label %exit [
```

```
  i32 1, label %sw.bb.i
```

```
  i32 2, label %sw.bb3.i
```

```
]
```

```
sw.bb.i:                            ; preds = %while.body
```

```
unreachable
```

```
sw.bb3.i:                            ; preds = %while.body
```

```
unreachable
```

```
exit:
```

```
                ; preds = %while.body
```

```
switch i32 undef, label %exit.2 [
```

```
  i32 1, label %sw.bb.i17
```

```
  i32 2, label %sw.bb3.i20
```

```
]
```

```
sw.bb.i17:                          ; preds = %exit
```

```
%0 = bitcast %struct.0* %lshr.iv to i32*
```

```
%1 = load i32, i32* %0, align 4
```

```
unreachable
```

```
sw.bb3.i20:                          ; preds = %exit
```

```
%2 = bitcast %struct.0* %lshr.iv to i8**
```

```
%3 = load i8*, i8** %2, align 4
```

```
unreachable
```

```
exit.2:                              ; preds = %exit
```

```
%cgep22 = getelementptr %struct.0, %struct.0* %lshr.iv, i32 1
```

```
br label %while.body
```

```
while.end:                ; preds = %entry
ret void
}
```

```
attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
```

---

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```
; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
```

```
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {  
; CHECK-LABEL: test_sitofp_fixed:
```

```
    ; First, extend each i32 to i64  
; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0  
; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0  
; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0  
; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0  
; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0  
; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0  
; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0  
; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
```

```
    ; Next, convert each to double.  
; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
```

```

; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]

; CHECK: ret
%flt = sitofp <16 x i32>
%in to <16 x double>
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,
double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,
double 64.0>
ret <16 x double> %res
}

```

; This one is small enough to satisfy isSimple, but still illegally large.

```

define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:

```

```

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

```

```

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}

```

# People who have agreed to one of the CLAs and can contribute patches.

```

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# lists people. For example, Google employees are listed here
# but not in AUTHORS, because Google holds the copyright.
#

```

```

# Names should be added to this file only after verifying that
# the individual or the individual's organization has agreed to
# the appropriate Contributor License Agreement, found here:
#

```

```

# https://developers.google.com/open-source/cla/individual
# https://developers.google.com/open-source/cla/corporate
#

```

```

# The agreement for individuals can be filled out on the web.
#

```

```

# When adding J Random Contributor's name to this file,
# either J's name or J's organization's name should be
# added to the AUTHORS file, depending on whether the
# individual or corporate CLA was used.
#

```

# Names should be added to this file as:

# Name <email address>

#

# Please keep the list sorted.

Albert Pretorius <pretoalb@gmail.com>

Arne Beer <arne@twobeer.de>

Billy

Robert O'Neal III <billy.oneal@gmail.com> <bion@microsoft.com>

Chris Kennelly <ckennelly@google.com> <ckennelly@ckennelly.com>

Christopher Seymour <chris.j.seymour@hotmail.com>

David Coeurjolly <david.coeurjolly@liris.cnrs.fr>

Deniz Evrenci <denizevrenci@gmail.com>

Dominic Hamon <dma@stripysock.com> <dominic@google.com>

Dominik Czarnota <dominik.b.czarnota@gmail.com>

Eric Fiselier <eric@efcs.ca>

Eugene Zhuk <eugene.zhuk@gmail.com>

Evgeny Safronov <division494@gmail.com>

Felix Homann <linuxaudio@showlabor.de>

Ismael Jimenez Martinez <ismael.jimenez.martinez@gmail.com>

Jern-Kuan Leong <jernkuan@gmail.com>

JianXiong Zhou <zhoujianxiong2@gmail.com>

Joao Paulo Magalhaes <joaoppmagalhaes@gmail.com>

John Millikin <jmillikin@stripe.com>

Jussi Knuutila <jussi.knuutila@gmail.com>

Kai Wolf <kai.wolf@gmail.com>

Kishan Kumar <kumar.kishan@outlook.com>

Kaito Udagawa <umireon@gmail.com>

Lei Xu <eddyxu@gmail.com>

Matt Clarkson <mattyclarkson@gmail.com>

Maxim Vafin <maxvafin@gmail.com>

Nick Hutchinson

<nshutchinson@gmail.com>

Oleksandr Sochka <sasha.sochka@gmail.com>

Pascal Leroy <phl@google.com>

Paul Redmond <paul.redmond@gmail.com>

Pierre Phaneuf <pphaneuf@google.com>

Radoslav Yovchev <radoslav.tm@gmail.com>

Raul Marin <rmrodriguez@cartodb.com>

Ray Glover <ray.glover@uk.ibm.com>

Robert Guo <robert.guo@mongodb.com>

Roman Lebedev <lebedev.ri@gmail.com>

Shuo Chen <chenshuo@chenshuo.com>

Tobias Ulvgrd <tobias.ulvgard@dirac.se>

Tom Madams <tom.ej.madams@gmail.com> <tmadams@google.com>

Yixuan Qiu <yixuanq@gmail.com>

Yusuke Suzuki <utatane.tea@gmail.com>

Zbigniew Skowron <zbychs@gmail.com>

```
; RUN: opt -consthoist -S -o - %s | FileCheck %s
target datalayout = "e-m:e-p:32:32-i64:64-v128:64:128-a:0:32-n32-S64"
target triple = "thumbv6m-none--musleabi"
```

```
; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT: %3 = load volatile i8, i8* %2
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 31
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8*
%4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 7
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
; CHECK-NEXT: %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT: store i8 %9, i8* %10
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C4]], 31
; CHECK-NEXT: %11 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT: store i8 %7, i8* %11
; CHECK-NEXT: %12 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT: store i8 %5, i8* %12
; CHECK-NEXT: %[[M5:const_mat[0-9]?]] = add i32 %[[C3]], 7
; CHECK-NEXT: %13 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT: store i8 %3, i8* %13
; CHECK-NEXT: %[[M6:const_mat[0-9]?]] = add i32 %[[C1]], 80
; CHECK-NEXT: %14 = inttoptr i32 %[[M6]] to i8*
; CHECK-NEXT: store i8* %14, i8** @goo
```

```
@goo = global i8* undef
```

```
define void @foo_i8() {
entry:
%0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
%1 = load volatile
i8, i8* inttoptr (i32 805874692 to i8*)
%2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
```

```

%3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
%4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
store i8 %4, i8* inttoptr(i32 805873688 to i8*)
store i8 %3, i8* inttoptr(i32 805873719 to i8*)
store i8 %2, i8* inttoptr(i32 805873720 to i8*)
store i8 %1, i8* inttoptr(i32 805873727 to i8*)
store i8* inttoptr(i32 805874800 to i8*), i8** @goo
ret void
}

```

```

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32
%[[C2]], 32
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C2]], 62
; CHECK-NEXT: %6 = inttoptr i32 %[[M3]] to i16*
; CHECK-NEXT: %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]] to i16*
; CHECK-NEXT: %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 22
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i16*
; CHECK-NEXT: %11 = load volatile i16, i16* %10, align 2

```

```

define void @foo_i16() {
entry:
%0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
%1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
%2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
%3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
%4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774
to i16*), align 2
ret void
}

```

```

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874816 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32

```



```

; CHECK: %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 124
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i32*
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 8
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i32*
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]],
12
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i32*
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4

```

```

define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}

```

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; NOTE: Assertions have been autogenerated by utils/update\_test\_checks.py UTC\_ARGS: --function-signature --scrub-attributes

; RUN: opt -S -argpromotion < %s | FileCheck %s

; RUN: opt -S -passes=argpromotion < %s | FileCheck %s



; Test that we only promote arguments when the caller/callee have compatible  
; function attributes.

target triple = "x86\_64-unknown-linux-gnu"

; This should promote

```
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg,  
<8 x i64>* readonly %arg1) #0 {  
; CHECK-LABEL: define {[^@]+}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512  
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])  
; CHECK-NEXT: bb:  
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]  
; CHECK-NEXT: ret void  
;  
bb:  
%tmp = load <8 x i64>, <8 x i64>* %arg1  
store <8 x i64> %tmp, <8 x i64>* %arg  
ret void  
}
```

```
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0  
{  
; CHECK-LABEL: define {[^@]+}@avx512_legal512_prefer512_call_avx512_legal512_prefer512  
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])  
; CHECK-NEXT: bb:  
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32  
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32  
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*  
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)  
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]  
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x  
i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])  
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32  
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2  
; CHECK-NEXT: ret void  
;  
bb:  
%tmp = alloca <8 x i64>, align 32  
%tmp2 = alloca <8 x i64>, align 32  
%tmp3 = bitcast <8 x i64>* %tmp to i8*  
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64  
32, i1 false)  
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x  
i64>* %tmp)  
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32  
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2  
ret void  
}
```

```

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{{^@+}}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

```

```

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
;
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x
i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

```

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{{^@+}}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256

```

```

; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
; CHECK-LABEL: define {{{^@+}}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca
<8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x
i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret
void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
; CHECK-LABEL: define {{{^@+}}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void

```

```

;
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {{{^@}+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8*
  align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x
i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8
x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{{^@}+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg

```

```

ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
; CHECK-LABEL: define {{{^@+}}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0,
i64 32, i1 false)
; CHECK-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x
i64>* [[TMP2]], <8 x i64>* [[TMP]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #2 {
; CHECK-LABEL: define {{{^@+}}} @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
;
CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {

```

```

; CHECK-LABEL: define {{{^@+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8
x i64>* [[TMP2]], <8 x i64>* [[TMP]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

; This should promote

```

define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #3 {

```

```

; CHECK-LABEL: define {{{^@+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256

```

```

; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])

```

```

; CHECK-NEXT: bb:

```

```

; CHECK-NEXT:

```

```

    store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]

```

```

; CHECK-NEXT: ret void

```

```

;

```

```

bb:

```

```

%tmp = load <8 x i64>, <8 x i64>* %arg1

```

```

store <8 x i64> %tmp, <8 x i64>* %arg

```

```

ret void

```

```

}

```

```

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {

```

```

; CHECK-LABEL: define {{{^@+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256

```

```

; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])

```

```

; CHECK-NEXT: bb:

```

```

; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32

```

```

; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32

```

```

; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*

```

```

; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>*
[[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store
<8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #4 {
; CHECK-LABEL: define {{{^@+}}}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64>
%tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
; CHECK-LABEL: define {{{^@+}}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>*
[[TMP2]], <8 x i64> [[TMP_VAL]])

```

```

; CHECK-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>,
align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```

; If the arguments are scalar, its ok to promote.

```

define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %X, i32*
%Y) #2 {
; CHECK-LABEL: define
{{{^@+}}}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT:  [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT:  ret i32 [[C]]
;
%A = load i32, i32* %X
%B = load i32, i32* %Y
%C = add i32 %A, %B
ret i32 %C
}

```

```

define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {{{^@+}}}@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
;
CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT:  [[A:%.*]] = alloca i32
; CHECK-NEXT:  store i32 1, i32* [[A]]
; CHECK-NEXT:  [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT:  [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT:  [[C:%.*]] = call i32
@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:  ret i32 [[C]]
;
%A = alloca i32
store i32 1, i32* %A
%C = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
ret i32 %C

```



```
}
```

```
; If the arguments are scalar, its ok to promote.
```

```
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %X, i32* %Y) #2 {  
; CHECK-LABEL: define  
{[{^@+}] }@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256  
; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])  
; CHECK-NEXT:  [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]  
; CHECK-NEXT:  ret  
i32 [[C]]  
;  
%A = load i32, i32* %X  
%B = load i32, i32* %Y  
%C = add i32 %A, %B  
ret i32 %C  
}
```

```
define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {  
; CHECK-LABEL: define {[{^@+}] }@scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256  
; CHECK-SAME: (i32* [[B:%.*]])  
; CHECK-NEXT:  [[A:%.*]] = alloca i32  
; CHECK-NEXT:  store i32 1, i32* [[A]]  
; CHECK-NEXT:  [[A_VAL:%.*]] = load i32, i32* [[A]]  
; CHECK-NEXT:  [[B_VAL:%.*]] = load i32, i32* [[B]]  
; CHECK-NEXT:  [[C:%.*]] = call i32  
@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])  
; CHECK-NEXT:  ret i32 [[C]]  
;  
%A = alloca i32  
store i32 1, i32* %A  
%C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %A, i32* %B)  
ret i32 %C  
}
```

```
; Function Attrs: argmemonly nounwind
```

```
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5
```

```
attributes #0 = { inlinehint norecurse nounwind  
uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }  
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-  
width"="512" "prefer-vector-width"="256" }  
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-  
width"="256" "prefer-vector-width"="256" }  
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512"  
"prefer-vector-width"="256" }  
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256"  
"prefer-vector-width"="256" }
```

attributes #5 = { argmemonly nounwind }  
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```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s
--check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit,avx512vbmi |
FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for
vpmaddwd+vpadd
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 | FileCheck %s --check-
prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cascadelake | FileCheck %s --
check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cooperlake | FileCheck %s --
check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=cannonlake | FileCheck %s --check-
prefixes=CHECK,CHECK-VBMI
;
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-client | FileCheck %s --
check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-server | FileCheck %s --
check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=tigerlake | FileCheck %s --check-
prefixes=CHECK,CHECK-VBMI
```

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

```
define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpadd 32(%rsi), %ymm1, %ymm1
```







```

; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32
23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: pmaddwd_32_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpmaddwd (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13,
i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

```

```

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-
width"="256" {

```

```

; CHECK-LABEL: psubus_64i8_max_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1

```

```

; CHECK-NEXT: vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr)
"min-legal-vector-width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

```

```

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="256" {
; CHECK-LABEL: _Z9test_charPcS_i_256:
; CHECK: # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB8_1: # %vector.body
; CHECK-NEXT:
# =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm3
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm4

```

```

; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT: vpadd %ymm2, %ymm3, %ymm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %ymm3
; CHECK-NEXT: vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT: vpadd %ymm1, %ymm3, %ymm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB8_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpadd %ymm0, %ymm1, %ymm1
; CHECK-NEXT: vpadd %ymm0, %ymm2, %ymm0
; CHECK-NEXT: vpadd %ymm0, %ymm1, %ymm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsltd { {.*#+} } xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsltd { {.*#+} } xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0,
%eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
%3 = zext i32 %2 to i64
br label %vector.body

```

vector.body:

```

%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

```

middle.block:

```

%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22,

```

```

i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

```

```

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-
width"="512" {

```

```

; CHECK-LABEL: _Z9test_charPcS_i_512:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT:  movl %edx, %eax
; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:  xorl %ecx, %ecx
; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:  .p2align 4, 0x90
; CHECK-NEXT: .LBB9_1: # %vector.body
; CHECK-NEXT:  # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:  vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-NEXT:  vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-NEXT:  vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT:  vpaddd %zmm1, %zmm2, %zmm1
; CHECK-NEXT:  addq $32, %rcx
; CHECK-NEXT:  cmpq %rcx, %rax
; CHECK-NEXT:  jne .LBB9_1

```

```

; CHECK-NEXT:
# %bb.2: # %middle.block
; CHECK-NEXT:  vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:  vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT:  vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpslufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpslufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vmovd %xmm0, %eax
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef>

```

```

i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

```

```

@a = global [1024 x i8] zeroinitializer, align 16
@b = global [1024 x i8] zeroinitializer, align 16

```

```

define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
; CHECK-LABEL: sad_16i8_256:
; CHECK:      # %bb.0: # %entry
; CHECK-NEXT: vpxor %xmm0, %xmm0,
%xmm0
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB10_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm2
; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm2, %xmm2
; CHECK-NEXT: vpadd %ymm1, %ymm2, %ymm1
; CHECK-NEXT: addq $4, %rax
; CHECK-NEXT: jne .LBB10_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpadd %ymm0, %ymm1, %ymm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpshufd { {. *#+ } } xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpshufd { {. *#+ } } xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:

```

```
br label %vector.body
```

```
vector.body:
```

```
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi
= phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body
```

```
middle.block:
```

```
%rdx.shuf = shufflevector
<16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}
```

```
define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
```

```
; CHECK-LABEL: sad_16i8_512:
```

```
; CHECK:      # %bb.0: # %entry
```

```

; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB11_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm1
; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: addq $4, %rax
; CHECK-NEXT: jne .LBB11_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsltd
{{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsltd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
br label %vector.body

vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:

```



```

%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32
13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32
2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

```

```

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f32_256:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vcvt dq2ps %ymm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vcvt dq2ps %ymm1, %ymm1
; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
;
; CHECK-NEXT:  vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

```

```

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vcvt dq2ps %zmm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq

```

```

%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f64_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT:
    vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvt dq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvt dq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vcvt dq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvt dq2pd %xmm2, %ymm2
; CHECK-NEXT: vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vmovaps %ymm3, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT: vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f64_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vcvt dq2pd %ymm0, %zmm1
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT:
    vcvt dq2pd %ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res

```

```

ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f32_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT: vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm1
; CHECK-NEXT: vpsrld $31, %ymm1, %ymm1
; CHECK-NEXT: vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT: vmovaps %ymm1, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16
x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vpsrld $31, %ymm0, %ymm0

```

```

; CHECK-NEXT:   vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT:   vextracti128 $1, %ymm0,
%ymm0
; CHECK-NEXT:   vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT:   vpmovm2d %k0, %ymm2
; CHECK-NEXT:   vpsrld $31, %ymm2, %ymm2
; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT:   vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT:   vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT:   vmovaps %ymm3, (%rdi)
; CHECK-NEXT:   vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT:   vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

```

```

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {

```

```

; CHECK-LABEL: ubto16f64_512:
; CHECK:      # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm1
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtdq2pd
%ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

```

```

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="256" {

```

```

; CHECK-LABEL: test_16f32toub_256:
; CHECK:      # %bb.0:
; CHECK-NEXT: vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k0

```

```

; CHECK-NEXT:   vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:   vpslld $31, %ymm1, %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k1
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1
; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="512" {
; CHECK-LABEL: test_16f32toub_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:   vpslld $31, %zmm1, %zmm1
; CHECK-NEXT:   vpmovd2m %zmm1, %k1
; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="256" {
; CHECK-LABEL: test_16f32tosb_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k0
; CHECK-NEXT:   vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k1
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1
; CHECK-NEXT:   vmovdqu16
%ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-
width"="512" {
; CHECK-LABEL: test_16f32tosb_512:

```

```

; CHECK:    # %bb.0:
; CHECK-NEXT:  vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:  vpmovd2m %zmm1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: mul256:
; CHECK-AVX512:    # %bb.0:
; CHECK-AVX512-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-AVX512-NEXT:
    vmovdqa 32(%rdi), %ymm1
; CHECK-AVX512-NEXT:  vmovdqa (%rsi), %ymm2
; CHECK-AVX512-NEXT:  vmovdqa 32(%rsi), %ymm3
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} %ymm4 =
ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} %ymm5 =
ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:  vpmullw %ymm4, %ymm5, %ymm4
; CHECK-AVX512-NEXT:  vmovdqa {{.*#+}} %ymm5 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm4, %ymm4
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} %ymm3 =
ymm3[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} %ymm1 =
ymm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpmullw %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpand %ymm5,
    %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpackuswb %ymm4, %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} %ymm3 =
ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} %ymm4 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:  vpmullw %ymm3, %ymm4, %ymm3
; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm3, %ymm3
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} %ymm2 =
ymm2[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} %ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpmullw %ymm2, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpackuswb %ymm3, %ymm0, %ymm0

```

```

; CHECK-AVX512-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-AVX512-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-AVX512-NEXT:
    vzeroupper
; CHECK-AVX512-NEXT: retq
;
; CHECK-VBMI-LABEL: mul256:
; CHECK-VBMI:    # %bb.0:
; CHECK-VBMI-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-VBMI-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-VBMI-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-VBMI-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm5 =
ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm3 =
ymm3[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm1 =
ymm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT: vmovdqa {{.*#+}}
ymm3 = [0,2,4,6,8,10,12,14,32,34,36,38,40,42,44,46,16,18,20,22,24,26,28,30,48,50,52,54,56,58,60,62]
; CHECK-VBMI-NEXT: vpermt2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm5 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm2 =
ymm2[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpmullw %ymm2, %ymm0, %ymm0
; CHECK-VBMI-NEXT: vpermt2b %ymm4, %ymm3, %ymm0
; CHECK-VBMI-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-VBMI-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-VBMI-NEXT: vzeroupper
; CHECK-VBMI-NEXT:
    retq
    %d = load <64 x i8>, <64 x i8>* %a
    %e = load <64 x i8>, <64 x i8>* %b
    %f = mul <64 x i8> %d, %e
    store <64 x i8> %f, <64 x i8>* %c
    ret void
}

```

```

define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
; CHECK-AVX512-LABEL: mul512:
; CHECK-AVX512:    # %bb.0:
; CHECK-AVX512-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-AVX512-NEXT:  vmovdqa64 (%rsi), %zmm1
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} zmm2 =
zmm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} zmm3 =
zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-AVX512-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-AVX512-NEXT:  vmovdqa64
  {{.*#+}} zmm3 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,2
55,255,255,255,255]
; CHECK-AVX512-NEXT:  vpandq %zmm3, %zmm2, %zmm2
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} zmm1 =
zmm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} zmm0 =
zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-AVX512-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT:  vpandq %zmm3, %zmm0, %zmm0
; CHECK-AVX512-NEXT:  vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-AVX512-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-AVX512-NEXT:  vzeroupper
; CHECK-AVX512-NEXT:  retq
;
; CHECK-VBMI-LABEL: mul512:
; CHECK-VBMI:    # %bb.0:
; CHECK-VBMI-NEXT:
  vmovdqa64 (%rdi), %zmm0
; CHECK-VBMI-NEXT:  vmovdqa64 (%rsi), %zmm1
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} zmm2 =
zmm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} zmm3 =
zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-VBMI-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT:  vpunpcklbw {{.*#+}} zmm1 =
zmm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT:  vpunpcklbw {{.*#+}} zmm0 =
zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]

```



```

;
CHECK-VBMI-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT:  vmovdqa64 {{.*#+}} zmm1 =
[0,2,4,6,8,10,12,14,64,66,68,70,72,74,76,78,16,18,20,22,24,26,28,30,80,82,84,86,88,90,92,94,32,34,36,38,40,42,44,
46,96,98,100,102,104,106,108,110,48,50,52,54,56,58,60,62,112,114,116,118,120,122,124,126]
; CHECK-VBMI-NEXT:  vpermi2b %zmm2, %zmm0, %zmm1
; CHECK-VBMI-NEXT:  vmovdqa64 %zmm1, (%rdx)
; CHECK-VBMI-NEXT:  vzeroupper
; CHECK-VBMI-NEXT:  retq
% d = load <64 x i8>, <64 x i8>* % a
% e = load <64 x i8>, <64 x i8>* % b
% f = mul <64 x i8> % d, % e
store <64 x i8> % f, <64 x i8>* % c
ret void
}

```

; This threw an assertion at one point.

```

define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-
width"="256" {
; CHECK-LABEL: mload_v4i32:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vptestnmd %xmm0, %xmm0, %k1
; CHECK-NEXT:  vblendmd (%rdi), %xmm1, %xmm0 {%k1}
;
CHECK-NEXT:  retq
%mask = icmp eq <4 x i32> %trigger, zeroinitializer
%res = call <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>* %addr, i32 4, <4 x i1> %mask, <4 x i32>
%dst)
ret <4 x i32> %res
}
declare <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>*, i32, <4 x i1>, <4 x i32>)

```

```

define <16 x i32> @trunc_v16i64_v16i32(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i32:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT:  vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT:  vpmovqd %ymm0, %xmm0
; CHECK-NEXT:  vpmovqd %ymm1, %xmm1
; CHECK-NEXT:  vinserti128 $1, %xmm1, %ymm0, %ymm0
; CHECK-NEXT:  vpmovqd %ymm2, %xmm1
; CHECK-NEXT:  vpmovqd %ymm3, %xmm2
; CHECK-NEXT:  vinserti128 $1, %xmm2, %ymm1, %ymm1
; CHECK-NEXT:  retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i32>

```

```

ret <16 x i32> %b
}

define
<16 x i8> @trunc_v16i64_v16i8(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i8:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT:  vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT:  vpmovqb %ymm3, %xmm3
; CHECK-NEXT:  vpmovqb %ymm2, %xmm2
; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm2 = xmm2[0],xmm3[0],xmm2[1],xmm3[1]
; CHECK-NEXT:  vpmovqb %ymm1, %xmm1
; CHECK-NEXT:  vpmovqb %ymm0, %xmm0
; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm2[0]
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i8>
ret <16 x i8> %b
}

define <16 x i8> @trunc_v16i32_v16i8(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i8:
; CHECK:    # %bb.0:
; CHECK-NEXT:
    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovdb %ymm1, %xmm1
; CHECK-NEXT:  vpmovdb %ymm0, %xmm0
; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = load <16 x i32>, <16 x i32>* %x
%b = trunc <16 x i32> %a to <16 x i8>
ret <16 x i8> %b
}

define <8 x i8> @trunc_v8i64_v8i8(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i8:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovqb %ymm1, %xmm1
; CHECK-NEXT:  vpmovqb %ymm0, %xmm0

```

```

; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = load <8 x i64>, <8 x i64>* %x
%b = trunc <8 x i64> %a to <8 x i8>
ret <8 x i8> %b
}

define <8 x i16> @trunc_v8i64_v8i16(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i16:
;
CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovqw %ymm1, %xmm1
; CHECK-NEXT:  vpmovqw %ymm0, %xmm0
; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = load <8 x i64>, <8 x i64>* %x
%b = trunc <8 x i64> %a to <8 x i16>
ret <8 x i16> %b
}

define <8 x i32> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpsrlq $48, 32(%rdi), %ymm1
; CHECK-NEXT:  vpsrlq $48, (%rdi), %ymm2
; CHECK-NEXT:  vmovdqa {{.*#+}} ymm0 = [0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30]
; CHECK-NEXT:  vpermi2w %ymm1, %ymm2, %ymm0
; CHECK-NEXT:  retq
%a = load <8 x i64>, <8 x i64>* %x
%b = lshr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
%c = trunc <8 x i64> %b to <8 x i32>
ret <8 x i32> %c
}

define
<16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm1
; CHECK-NEXT:  vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT:  vpermi2w 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT:  retq
%a = load <16 x i32>, <16 x i32>* %x
%b = lshr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>

```





```

; CHECK-NEXT: vpmovzxwq {{.*#+}} ymm3 =
xmm3[0],zero,zero,zero,xmm3[1],zero,zero,zero,xmm3[2],zero,zero,zero,xmm3[3],zero,zero,zero
; CHECK-NEXT: vpmovzxwq {{.*#+}} ymm1 =
xmm1[0],zero,zero,zero,xmm1[1],zero,zero,zero,xmm1[2],zero,zero,zero,xmm1[3],zero,zero,zero
;
CHECK-NEXT: vpmovzxbq {{.*#+}} ymm0 =
xmm0[0],zero,zero,zero,zero,zero,zero,zero,xmm0[1],zero,zero,zero,zero,zero,zero,zero,xmm0[2],zero,zero,zero,zer
o,zero,zero,zero,xmm0[3],zero,zero,zero,zero,zero,zero,zero
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)
; CHECK-NEXT: vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT: vmovdqa %ymm2, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = zext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

```

```

define void @sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovsxbw %xmm0, %ymm0
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpmovsxdq %xmm1, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT: vpshufd {{.*#+}} xmm3 = xmm2[2,3,0,1]
; CHECK-NEXT: vpmovsxdq %xmm3, %ymm3
;
CHECK-NEXT: vpmovsxdq %xmm0, %ymm0
; CHECK-NEXT: vpmovsxdq %xmm2, %ymm2
; CHECK-NEXT: vmovdqa %ymm2, 64(%rdi)
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = sext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

```

```

define void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>*
%r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i16_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3

```

```

; CHECK-NEXT: vpcmpeqw %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrb $4, %k1, %k2
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8
x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i16> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

```

```

define void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>*
%r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i32_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqd %ymm1, %ymm0, %k1
; CHECK-NEXT: kshiftrb $4, %k1, %k2
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i32> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

```

```

define
void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r)
"min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i8_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqb %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrw $8, %k1, %k2

```

```

; CHECK-NEXT: vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <16 x i32>, <16 x i32>* %p
%y = load <16 x i32>, <16 x i32>* %q
%a = icmp eq <16 x i8> %s, %t
%b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
store <16 x i32> %b, <16 x i32>* %r
ret void
}

define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x
i32>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i16_setcc:
;
CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqw %ymm1, %ymm0, %k1
; CHECK-NEXT: kshiftrw $8, %k1, %k2
; CHECK-NEXT: vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <16 x i32>, <16 x i32>* %p
%y = load <16 x i32>, <16 x i32>* %q
%a = icmp eq <16 x i16> %s, %t
%b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
store <16 x i32> %b, <16 x i32>* %r
ret void
}

define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_packus_v16i32_v16i8:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT: vpmovuswb %ymm0, %xmm0
; CHECK-NEXT:
vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %p

```



```

    %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %d = icmp sgt <16 x i32> %c, zeroinitializer
    %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
    %f = trunc <16 x i32> %e to <16 x i8>
    ret <16 x i8> %f
}

```

```

define void @trunc_packus_v16i32_v16i8_store(<16 x i32>* %p, <16 x i8>* %q) "min-legal-vector-width"="256"
{
; CHECK-LABEL: trunc_packus_v16i32_v16i8_store:
; CHECK:      # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpermq {{.*#+}} %ymm0 = %ymm0[0,2,1,3]
; CHECK-NEXT: vpmovuswb %ymm0, (%rsi)
;
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
    %a = load <16 x i32>, <16 x i32>* %p
    %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %d = icmp sgt <16 x i32> %c, zeroinitializer
    %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
    %f = trunc <16 x i32> %e to <16 x i8>
    store <16 x i8> %f, <16 x i8>* %q
    ret void
}

```

```

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_argument_return:
; CHECK:      # %bb.0:
; CHECK-NEXT: retq
    ret <64 x i1> %x
}

```

```

define void @v64i1_shuffle(<64 x i8>* %x, <64 x i8>* %y) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_shuffle:
; CHECK:
    # %bb.0: # %entry
; CHECK-NEXT: vmovdqa (%rdi), %ymm1
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm0
; CHECK-NEXT: vptestnmb %ymm1, %ymm1, %k0
; CHECK-NEXT: kshiftrd $1, %k0, %k1

```

```

; CHECK-NEXT:  movq $-3, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiflq $63, %k0, %k2
; CHECK-NEXT:  kshiftrq $62, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-5, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $3, %k0, %k2
; CHECK-NEXT:  kshiflq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $61, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-9, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $2, %k0, %k2
; CHECK-NEXT:  kshiflq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $60, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-17, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:
    kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $5, %k0, %k2
; CHECK-NEXT:  kshiflq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $59, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-33, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $4, %k0, %k2
; CHECK-NEXT:  kshiflq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $58, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-65, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $7, %k0, %k2
; CHECK-NEXT:  kshiflq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $57, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-129, %rax
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $6, %k0, %k2
; CHECK-NEXT:  kshiflq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $56, %k2, %k2

```

```

; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-257, %rax # imm = 0xFEFF
;
CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $9, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $55, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-513, %rax # imm = 0xFDFD
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $8, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $54, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-1025, %rax # imm = 0xFBFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $11, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $53, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-2049, %rax # imm = 0xF7FF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $10, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq
$52, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-4097, %rax # imm = 0xEFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $13, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $51, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-8193, %rax # imm = 0xDFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $12, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $50, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-16385, %rax # imm = 0xBFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1

```

```

; CHECK-NEXT: kshiftrd $15, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $49, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-32769, %rax # imm = 0xFFFF7FFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1,
%k1
; CHECK-NEXT: kshiftrd $14, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $48, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-65537, %rax # imm = 0xFFFEFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $17, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $47, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-131073, %rax # imm = 0xFFFDFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $16, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $46, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-262145, %rax # imm = 0xFFFBFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $19, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $45, %k2, %k2
; CHECK-NEXT: korq %k2, %k1,
%k1
; CHECK-NEXT: movq $-524289, %rax # imm = 0xFFF7FFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $18, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $44, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-1048577, %rax # imm = 0xFFEFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $21, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $43, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1

```

```

; CHECK-NEXT:  movq $-2097153, %rax # imm = 0xFFDFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $20, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $42, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-4194305, %rax # imm = 0xFFBFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd
$23, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $41, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-8388609, %rax # imm = 0xFF7FFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $22, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $40, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-16777217, %rax # imm = 0xFEFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $25, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $39, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-33554433, %rax # imm = 0xFDFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $24, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $38, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:
    movq $-67108865, %rax # imm = 0xFBFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $27, %k0, %k2
; CHECK-NEXT:  kshiftrlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $37, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-134217729, %rax # imm = 0xF7FFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $26, %k0, %k2

```

```

; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $36, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-268435457, %rax # imm = 0xEFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $29, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $35, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-536870913, %rax # imm = 0xDFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $28,
%k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $34, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-1073741825, %rax # imm = 0xBFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $31, %k0, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $33, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movabsq $-2147483649, %rax # imm = 0xFFFFFFFF7FFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k2
; CHECK-NEXT: vptestnmb %ymm0, %ymm0, %k1
; CHECK-NEXT: kshiftrd $30, %k0, %k0
; CHECK-NEXT: kshiftrlq $63, %k0, %k0
; CHECK-NEXT: kshiftrq $32, %k0, %k0
; CHECK-NEXT: korq %k0, %k2, %k0
; CHECK-NEXT: movabsq $-4294967297, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $1, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq
$31, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-8589934593, %rax # imm = 0xFFFFFFFFDFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrlq $63, %k1, %k2
; CHECK-NEXT: kshiftrq $30, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-17179869185, %rax # imm = 0xFFFFFFFFBFFFFFFF

```

```

; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $3, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $29, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-34359738369, %rax # imm = 0xFFFFFFFF7FFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $2, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $28, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-68719476737, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax,
%k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $5, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $27, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-137438953473, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $4, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $26, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-274877906945, %rax # imm = 0xFFFFFFFFBFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $7, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $25, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-549755813889, %rax # imm = 0xFFFFFFFF7FFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $6, %k1, %k2
; CHECK-NEXT: kshiftrlq
$63, %k2, %k2
; CHECK-NEXT: kshiftrq $24, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-1099511627777, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $9, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2

```

```

; CHECK-NEXT: kshiftrq $23, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-2199023255553, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $8, %k1, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $22, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-4398046511105, %rax # imm = 0xFFFFFBFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $11, %k1, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $21, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT:
    movabsq $-8796093022209, %rax # imm = 0xFFFFF7FFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $10, %k1, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $20, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-17592186044417, %rax # imm = 0xFFFFEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $13, %k1, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $19, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-35184372088833, %rax # imm = 0xFFFFDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $12, %k1, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $18, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-70368744177665, %rax # imm = 0xFFFFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
;
CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $15, %k1, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $17, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-140737488355329, %rax # imm = 0xFFFF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2

```



```

; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $14, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $16, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-281474976710657, %rax # imm = 0xFFFEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $17, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $15, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-562949953421313, %rax # imm = 0xFFFDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $16, %k1, %k2
; CHECK-NEXT:
kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $14, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-1125899906842625, %rax # imm = 0xFFFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $19, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $13, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-2251799813685249, %rax # imm = 0xFFF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $18, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $12, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-4503599627370497, %rax # imm = 0xFFEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $21, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $11, %k2, %k2
; CHECK-NEXT: korq %k2,
%k0, %k0
; CHECK-NEXT: movabsq $-9007199254740993, %rax # imm = 0xFFDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $20, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $10, %k2, %k2

```

```

; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-18014398509481985, %rax # imm = 0xFFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $23, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $9, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-36028797018963969, %rax # imm = 0xFF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $22, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $8, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-72057594037927937, %rax # imm = 0xFEFFFFFFFFFFFFFFF
; CHECK-NEXT:
    kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $25, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $7, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-144115188075855873, %rax # imm = 0xFDFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $24, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $6, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-288230376151711745, %rax # imm = 0xFBFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $27, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $5, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-576460752303423489, %rax # imm = 0xF7FFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd
    $26, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $4, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-1152921504606846977, %rax # imm = 0xEFFFFFFFFFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0

```

```

; CHECK-NEXT: kshiftrd $29, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $3, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-2305843009213693953, %rax # imm = 0xDFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $28, %k1, %k2
; CHECK-NEXT: kshiftrlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $2, %k2, %k2
; CHECK-NEXT: korq %k2, %k0, %k0
; CHECK-NEXT: movabsq $-4611686018427387905, %rax # imm = 0xBFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k0, %k0
; CHECK-NEXT: kshiftrd $31, %k1, %k2
; CHECK-NEXT: kshiftrlq $62, %k2, %k2
; CHECK-NEXT: korq %k2,
%k0, %k0
; CHECK-NEXT: kshiftrd $30, %k1, %k1
; CHECK-NEXT: kshiftrlq $1, %k0, %k0
; CHECK-NEXT: kshiftrq $1, %k0, %k0
; CHECK-NEXT: kshiftrlq $63, %k1, %k1
; CHECK-NEXT: korq %k1, %k0, %k1
; CHECK-NEXT: vmovdqu8 %ymm1, (%rsi) {%k1}
; CHECK-NEXT: kshiftrq $32, %k1, %k1
; CHECK-NEXT: vmovdqu8 %ymm0, 32(%rsi) {%k1}
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
%a = load <64 x i8>, <64 x i8>* %x
%b = icmp eq <64 x i8> %a, zeroinitializer
%shuf = shufflevector <64 x i1> %b, <64 x i1> undef, <64 x i32> <i32 1, i32 0, i32 3, i32 2, i32 5, i32 4, i32 7, i32
6, i32 9, i32 8, i32 11, i32 10, i32 13, i32 12, i32 15, i32 14, i32 17, i32 16, i32 19, i32 18, i32 21, i32 20, i32 23, i32
22, i32 25, i32 24, i32 27, i32 26, i32 29, i32 28, i32 31, i32 30, i32 33, i32 32, i32 35, i32 34, i32 37, i32 36, i32 39,
i32 38, i32 41, i32 40, i32 43, i32 42, i32 45, i32 44, i32 47, i32 46, i32 49, i32 48, i32 51, i32 50, i32 53, i32 52, i32
55, i32 54, i32 57, i32 56, i32
59, i32 58, i32 61, i32 60, i32 63, i32 62>
call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, i32 1, <64 x i1> %shuf)
ret void
}
declare void @llvm.masked.store.v64i8.p0v64i8(<64 x i8>, <64 x i8>*, i32, <64 x i1>)

@mem64_dst = global i64 0, align 8
@mem64_src = global i64 0, align 8
define i32 @v64i1_inline_asm() "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_inline_asm:
; CHECK: # %bb.0:
; CHECK-NEXT: kmovq {{.*}}(%rip), %k0

```

```

; CHECK-NEXT: #APP
; CHECK-NEXT: #NO_APP
; CHECK-NEXT: kmovq %k0, {{.*}}(%rip)
; CHECK-NEXT: movl -{[0-9]+}(%rsp), %eax
; CHECK-NEXT: retq
%1 = alloca i32, align 4
%2 = load i64, i64* @mem64_src, align 8
%3 = call i64 @asm "", "=k,k,~{dirflag},~{fpsr},~{flags}"(i64 %2)
store i64 %3, i64* @mem64_dst, align 8
%4 = load i32, i32* %1, align 4
ret i32 %4
}

define void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_sext:
;
CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
%ext = sext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_zext:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpsrlq $63, %ymm1, %ymm1
; CHECK-NEXT: vpsrlq $63, %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0,
(%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper

```

```

; CHECK-NEXT:  retq
%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
%ext = zext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: var_rotate_v16i8:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
; CHECK-NEXT:  vpsubb %xmm1, %xmm2, %xmm2
; CHECK-NEXT:  vpmovzxbw {{.*#+}} ymm1 =
xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zer
o,xmm1[8],zero,xmm1[9],zero,xmm1[10],zero,xmm1[11],zero,xmm1[12],zero,xmm1[13],zero,xmm1[14],zero,xm
m1[15],zero
; CHECK-NEXT:  vpmovzxbw {{.*#+}} ymm0 =
xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zer
o,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xm
m0[15],zero
;
CHECK-NEXT:  vpsllvw %ymm1, %ymm0, %ymm1
; CHECK-NEXT:  vpmovzxbw {{.*#+}} ymm2 =
xmm2[0],zero,xmm2[1],zero,xmm2[2],zero,xmm2[3],zero,xmm2[4],zero,xmm2[5],zero,xmm2[6],zero,xmm2[7],zer
o,xmm2[8],zero,xmm2[9],zero,xmm2[10],zero,xmm2[11],zero,xmm2[12],zero,xmm2[13],zero,xmm2[14],zero,xm
m2[15],zero
; CHECK-NEXT:  vpsrlvw %ymm2, %ymm0, %ymm0
; CHECK-NEXT:  vpor %ymm0, %ymm1, %ymm0
; CHECK-NEXT:  vpmovwb %ymm0, %xmm0
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%b8 = sub <16 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
%shl = shl <16 x i8> %a, %b
%lshr = lshr <16 x i8> %a, %b8
%or = or <16 x i8> %shl, %lshr
ret <16 x i8> %or
}

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: var_rotate_v32i8:
; CHECK:      # %bb.0:
; CHECK-NEXT:
vpsllw $4, %ymm0, %ymm2
; CHECK-NEXT:  vpsrlw $4, %ymm0, %ymm3
; CHECK-NEXT:  vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
; CHECK-NEXT:  vpsllw $5, %ymm1, %ymm1

```



```

%shl = shl <32 x i8> %a, %splat
%lshr = lshr <32 x i8> %a, %splat8
%or = or <32 x i8> %shl, %lshr
ret <32 x i8> %or
}

define <32 x i8> @constant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: constant_rotate_v32i8:
; CHECK-AVX512:    # %bb.0:
; CHECK-AVX512-NEXT:  vpsllw $4, %ymm0, %ymm1
; CHECK-AVX512-NEXT:  vpand {{.*}}(%rip), %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vbroadcasti128 {{.*#+}} ymm2 =
[8192,24640,41088,57536,57344,41152,24704,8256,8192,24640,41088,57536,57344,41152,24704,8256]
;
; CHECK-AVX512-NEXT:  # ymm2 = mem[0,1,0,1]
; CHECK-AVX512-NEXT:  vpbldvb %ymm2, %ymm1, %ymm0, %ymm1
; CHECK-AVX512-NEXT:  vpsllw $2, %ymm1, %ymm3
; CHECK-AVX512-NEXT:  vpand {{.*}}(%rip), %ymm3, %ymm3
; CHECK-AVX512-NEXT:  vpaddb %ymm2, %ymm2, %ymm2
; CHECK-AVX512-NEXT:  vpbldvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpaddb %ymm1, %ymm1, %ymm3
; CHECK-AVX512-NEXT:  vpaddb %ymm2, %ymm2, %ymm2
; CHECK-AVX512-NEXT:  vpbldvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpxor %xmm2, %xmm2, %xmm2
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} ymm3 =
ymm0[8],ymm2[8],ymm0[9],ymm2[9],ymm0[10],ymm2[10],ymm0[11],ymm2[11],ymm0[12],ymm2[12],ymm0[13
],ymm2[13],ymm0[14],ymm2[14],ymm0[15],ymm2[15],ymm0[24],ymm2[24],ymm0[25],ymm2[25],ymm0[26],ymm
2[26],ymm0[27],ymm2[27],ymm0[28],ymm2[28],ymm0[29],ymm2[29],ymm0[30],ymm2[30],ymm0[31],ymm2[
31]
; CHECK-AVX512-NEXT:  vpsllvw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-AVX512-NEXT:
; CHECK-AVX512-NEXT:  vpsrlw $8, %ymm3, %ymm3
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} ymm0 =
ymm0[0],ymm2[0],ymm0[1],ymm2[1],ymm0[2],ymm2[2],ymm0[3],ymm2[3],ymm0[4],ymm2[4],ymm0[5],ymm2[
5],ymm0[6],ymm2[6],ymm0[7],ymm2[7],ymm0[16],ymm2[16],ymm0[17],ymm2[17],ymm0[18],ymm2[18],ymm0[
19],ymm2[19],ymm0[20],ymm2[20],ymm0[21],ymm2[21],ymm0[22],ymm2[22],ymm0[23],ymm2[23]
; CHECK-AVX512-NEXT:  vpsllvw {{.*}}(%rip), %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpsrlw $8, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpackuswb %ymm3, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpor %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:  retq
;
; CHECK-VBMI-LABEL: constant_rotate_v32i8:
; CHECK-VBMI:    # %bb.0:
; CHECK-VBMI-NEXT:  vpsllw $4, %ymm0, %ymm1
; CHECK-VBMI-NEXT:  vpand {{.*}}(%rip), %ymm1, %ymm1
; CHECK-VBMI-NEXT:  vbroadcasti128 {{.*#+}} ymm2 =
[8192,24640,41088,57536,57344,41152,24704,8256,8192,24640,41088,57536,57344,41152,24704,8256]

```







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# Please keep the list sorted.

Albert Pretorius <pretoalb@gmail.com>

Arne Beer <arne@twobeer.de>

Billy

Robert O'Neal III <billy.oneal@gmail.com> <bion@microsoft.com>  
Chris Kennelly <ckennelly@google.com> <ckennelly@ckennelly.com>  
Christopher Seymour <chris.j.seymour@hotmail.com>  
Cyrille Faucheux <cyrille.faucheux@gmail.com>  
David Coeurjolly <david.coeurjolly@liris.cnrs.fr>  
Deniz Evrenci <denizevrenci@gmail.com>  
Dominic Hamon <dma@stripsock.com> <dominic@google.com>  
Dominik Czarnota <dominik.b.czarnota@gmail.com>  
Eric Fiselier <eric@efcs.ca>  
Eugene Zhuk <eugene.zhuk@gmail.com>  
Evgeny Safronov <division494@gmail.com>  
Federico Ficarelli <federico.ficarelli@gmail.com>  
Felix Homann <linuxaudio@showlabor.de>  
Ismael Jimenez Martinez <ismael.jimenez.martinez@gmail.com>  
Jern-Kuan Leong <jernkuan@gmail.com>  
JianXiong Zhou <zhoujianxiong2@gmail.com>  
Joao Paulo Magalhaes <joaoppmagalhaes@gmail.com>  
John Millikin <jmillikin@stripe.com>  
Jussi Knuuttila <jussi.knuuttila@gmail.com>  
Kai Wolf <kai.wolf@gmail.com>  
Kishan Kumar <kumar.kishan@outlook.com>  
Kaito Udagawa <umireon@gmail.com>  
Lei Xu <eddyxu@gmail.com>  
Matt  
Clarkson <mattyclarkson@gmail.com>  
Maxim Vafin <maxvafin@gmail.com>  
Nick Hutchinson <nshutchinson@gmail.com>  
Oleksandr Sochka <sasha.sochka@gmail.com>  
Ori Livneh <ori.livneh@gmail.com>  
Pascal Leroy <phl@google.com>  
Paul Redmond <paul.redmond@gmail.com>  
Pierre Phaneuf <pphaneuf@google.com>  
Radoslav Yovchev <radoslav.tm@gmail.com>  
Raul Marin <rmrodriguez@cartodb.com>  
Ray Glover <ray.glover@uk.ibm.com>  
Robert Guo <robert.guo@mongodb.com>  
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Shuo Chen <chenshuo@chenshuo.com>  
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add\_lldb\_library(lldbPluginObjectContainerBSDArchive PLUGIN  
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LLVM Team

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```
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --
scrub-attributes
; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -
attributor-max-iterations=4 -S < %s | FileCheck %s --check-
prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS_____OP
M,IS__TUNIT_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal -attributor-max-iterations-verify -
attributor-annotate-decl-cs -attributor-max-iterations=3 -S < %s | FileCheck %s --check-
prefixes=CHECK,NOT_CGSCC_OPM,NOT_CGSCC_NPM,NOT_TUNIT_OPM,IS__TUNIT____,IS_____NP
M,IS__TUNIT_NPM
; RUN: opt -attributor-cgsc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --
check-
prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_NPM,IS__CGSCC____,IS_____OP
M,IS__CGSCC_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor-cgsc -attributor-manifest-internal
-attributor-annotate-decl-cs -S < %s | FileCheck %s --check-
prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_OPM,IS__CGSCC____,IS_____NP
M,IS__CGSCC_NPM
; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
```

```

define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
{{{^@+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:
ret void
;
; IS__TUNIT_NPM-LABEL: define
{{{^@+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
;
; IS__TUNIT_OPM-LABEL: define {{{^@+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
;
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void

```

```

;
; IS__TUNIT_NPM-LABEL: define {{^[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture
writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{^[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
;
IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL:
define {{^[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])

```

```

; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]],
<8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
;
; NOT_TUNIT_NPM-LABEL: define
{[^[@]+]}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:
[[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;
; IS__TUNIT_NPM-LABEL: define
{[^[@]+]}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:  ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

```

```

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8
x i64>* %arg) #1 {
;
; IS__TUNIT_OPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]],
align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT:
store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64

```

```

dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT:  [[TMP4:%.*]]
= load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:  ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
;
; NOT_TUNIT_NPM-LABEL: define
{{{^@+}}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;

```

```

; IS__TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret
void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
;
; IS__TUNIT_OPM-LABEL: define {[^[^@]+]}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {[^[^@]+]}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture

```

```

nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{[^\@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture
nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{[^\@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8
x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias
nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

```



```

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>*
  readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
  {{{^@+}}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
  [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;
; IS__TUNIT_NPM-LABEL: define
  {{{^@+}}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
  [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:
  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:  ret void
;
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
;
; IS__TUNIT_OPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
  dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT:  call fastcc void
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8
  x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree
  nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64

```

```

; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] =
load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64)
[[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8*
nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly

```

```

align 64 dereferenceable(64) [[TMP]]
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4,
<8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
;
; NOT_TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;
; IS__TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* noalias nocapture nofree nonnull
readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:  ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {

```

```

;
; IS__TUNIT_OPM-LABEL: define {{[^\@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]],
i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{[^\@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{[^\@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:
call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0,
i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64

```

```

; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]]
= bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #2 {
;
; NOT_TUNIT_NPM-LABEL: define
{{{^@+}}} @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{{{^@+}}} @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly

```

```

align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64
dereferenceable(64) [[ARG1:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
;
; IS__TUNIT_OPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8*
nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT:
call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0,
i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;

```

```

; IS__CGSCC_OPM-LABEL: define {{^[^@]+}} @avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:
    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{^[^@]+}} @avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:
    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8
x i64>* %tmp2, <8 x i64>* %tmp)
    %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8

```

```

x i64>* readonly %arg1) #3 {
;
; NOT_TUNIT_NPM-LABEL: define
{{{^@+}}} @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}} @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME:
(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>
[[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
;
; IS__TUNIT_OPM-LABEL: define {{{^@+}}} @avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]]
= alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly
align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}} @avx2_legal256_prefer256_call_avx2_legal512_prefer256

```



```

; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT:
  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT:  [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT:  call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT:  ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
;
IS__CGSCC_OPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:  call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly
align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__CGSCC_OPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:  ret void
;
; IS__CGSCC_NPM-LABEL: define {{{^@+}}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64)
[[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2

```

```

; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x
i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #4 {
;
; NOT_TUNIT_NPM-LABEL: define
{[^[^@]+]}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:
  ret void
;
; IS__TUNIT_NPM-LABEL: define {[^[^@]+]}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:  ret void
;
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
;
; IS__TUNIT_OPM-LABEL: define {[^[^@]+]}@avx2_legal512_prefer256_call_avx2_legal256_prefer256

```

```

; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
;
IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly
align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{{^@+}}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly
[[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{{^@+}}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
;
IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly
align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64)
[[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2

```

```

; IS__CGSCC_OPM-NEXT:  ret void
;
; IS__CGSCC_NPM-LABEL: define {[^@]+} @avx2_legal512_prefer256_call_avx2_legal256_prefer256
;
IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:
    ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>*
%tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="256" "prefer-vector-width"="256"
}
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512"
"prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256"
"prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

```

```

; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-
prefixes=CHECK,VEC256,AVX
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+prefer-256-bit |
FileCheck %s --check-prefixes=CHECK,VEC256,AVX512VL256
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,-prefer-256-bit |
FileCheck %s --check-prefixes=CHECK,VEC512,AVX512VL512
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -
mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-
prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -
mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-
prefixes=CHECK,VEC512,SKX512

```

```

define void @zext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'zext256'
; VEC256-NEXT: Cost
Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'zext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8>
undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'zext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>

```

```

; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of
0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

define void @zext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'zext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'zext512'
;
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to
<32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'zext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated
cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>

```

```

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT:
    Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'zext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
    %A = zext <8 x i16> undef to <8 x i64>
    %B = zext <8 x i32> undef to <8 x i64>
    %C = zext <16 x i8> undef to <16 x i32>
    %D = zext <16 x i16> undef to <16 x i32>
    %E = zext <32 x i8> undef to <32 x i16>
    ret void
}

define void @sext256()
    "min-legal-vector-width"="256" {
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x

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i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL512-NEXT: Cost
Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x
i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'sext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>

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; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef
to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'sext512'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction:
%D = sext <16 x i8> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to
<32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT:
Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>

```

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; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL:
'sext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F
= sext <32 x i8> undef to <32 x i16>
ret void
}

define void @trunc256() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc256'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'trunc256'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A =
trunc <8 x i64> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to
<8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef
to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef

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to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to
<16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to
<32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'trunc256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to
<8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to
<8 x i16>
; AVX512VL512-NEXT:
Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef
to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to
<16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %F = trunc <32 x i16> undef to
<32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'trunc256'
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 6 for instruction:
%D = trunc <16 x i32> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %F = trunc <32 x i16> undef to <32 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'trunc256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to <8 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to <8 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef to <16 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to <16 x
i8>

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; SKX512-NEXT: Cost Model: Found an estimated
cost of 2 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = trunc <8 x i64> undef to <8 x i32>
%B = trunc <8 x i64> undef to <8 x i16>
%C = trunc <8 x i64> undef to <8 x i8>
%D = trunc <16 x i32> undef to <16 x i16>
%E = trunc <16 x i32> undef to <16 x i8>
%F = trunc <32 x i16> undef to <32 x i8>
ret void
}

define i32 @zext256_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'zext256_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x
i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x
i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = zext <8 x i1> undef to <8 x
i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = zext <2 x i1> undef to <2 x
i32>
; AVX-NEXT: Cost Model: Found
an estimated cost of 1 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = zext <8 x i1> undef to <8 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = zext <16 x i1> undef to <16 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = zext <2 x i1> undef to <2 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = zext <4 x i1> undef to <4 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = zext <8 x i1> undef to <8 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = zext <16 x i1> undef to <16 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = zext <32 x i1> undef to <32 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX-NEXT:
Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = zext <16 x i1> undef to <16 x
i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = zext <32 x i1> undef to <32 x
i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x

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i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL256-LABEL: 'zext256_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef
to <2 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef
to <4 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction:
%V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef
to <2 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef
to <4 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef
to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1>
undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i16 = zext <2 x i1> undef
to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i16 = zext <4 x i1> undef
to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i16 = zext <8 x i1> undef
to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i16 = zext <16 x i1>
undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %V32i16 = zext <32 x i1>
undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V2i8 = zext <2 x i1> undef to
<2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V4i8 = zext <4 x i1> undef to
<4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V8i8 = zext <8 x i1> undef to
<8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i8 = zext <16 x i1>
undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 25 for instruction: %V32i8 = zext <32 x i1>
undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 50 for instruction: %V64i8 = zext <64 x i1>
undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'zext256_vXi1'
;
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef
to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef
to <4 x i64>

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; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>  
; AVX512VL512-NEXT: Cost Model:  
Found an estimated cost of 4 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 19 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>  
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef  
;  
; SKX256-LABEL: 'zext256\_vXi1'  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to

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<16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to
<16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = zext <32 x i1> undef to
<32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x
i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for
instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16
x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32
x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = zext <64 x i1> undef to <64
x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'zext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x
i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x
i32>
; SKX512-NEXT:
Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x
i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to
<16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x
i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to
<16 x i16>

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; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = zext <32 x i1> undef to
<32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction:
%V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%V2i64 = zext <2 x i1> undef to <2 x i64>
%V4i64 = zext <4 x i1> undef to <4 x i64>
%V8i64 = zext <8 x i1> undef to <8 x i64>

%V2i32 = zext <2 x i1> undef to <2 x i32>
%V4i32 = zext <4 x i1> undef to <4 x i32>
%V8i32 = zext <8 x i1> undef to <8 x i32>
%V16i32 = zext <16 x
i1> undef to <16 x i32>

%V2i16 = zext <2 x i1> undef to <2 x i16>
%V4i16 = zext <4 x i1> undef to <4 x i16>
%V8i16 = zext <8 x i1> undef to <8 x i16>
%V16i16 = zext <16 x i1> undef to <16 x i16>
%V32i16 = zext <32 x i1> undef to <32 x i16>

%V2i8 = zext <2 x i1> undef to <2 x i8>
%V4i8 = zext <4 x i1> undef to <4 x i8>
%V8i8 = zext <8 x i1> undef to <8 x i8>
%V16i8 = zext <16 x i1> undef to <16 x i8>
%V32i8 = zext <32 x i1> undef to <32 x i8>
%V64i8 = zext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @sext256_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'sext256_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x
i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x

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i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7
for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = sext <4 x i1> undef to <4 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = sext <8 x i1> undef to <8 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = sext <16 x i1> undef to <16 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = sext <4 x i1> undef to <4 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction:
%V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x
i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = sext <16 x i1> undef to <16 x
i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = sext <32 x i1> undef to <32 x
i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction:
%V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL256-LABEL: 'sext256_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef
to <2 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef
to <4 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef
to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef
to <2 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef
to <4 x i32>

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; AVX512VL256-NEXT:
  Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1>
undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = sext <2 x i1> undef
to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = sext <4 x i1> undef
to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = sext <8 x i1> undef
to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i16 = sext <16 x i1>
undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V32i16 = sext <32 x i1>
undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found
an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i8 = sext <2 x i1> undef to
<2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i8 = sext <4 x i1> undef to
<4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = sext <8 x i1> undef to
<8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i8 = sext <16 x i1>
undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 21 for instruction: %V32i8 = sext <32 x i1>
undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 42 for instruction: %V64i8 = sext <64 x i1>
undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'sext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction:
%I64 = sext i1 undef to i64
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef
to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef
to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef
to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef
to <2 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef
to <4 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef
to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1>

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undef to <16 x i32>
; AVX512VL512-NEXT:
  Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = sext <2 x i1> undef
to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef
to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef
to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1>
undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1>
undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = sext <2 x i1> undef to
<2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction:
  %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = sext <8 x i1> undef to
<8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = sext <16 x i1> undef
to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = sext <32 x i1> undef
to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = sext <64 x i1>
undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'sext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x
i64>
; SKX256-NEXT:
  Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1> undef to
<16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x
i16>

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; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef  
;  
; SKX512-LABEL: 'sext256\_vXi1'  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to

```

<16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to
<32 x i16>
; SKX512-NEXT:
  Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x
i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i8 = sext <64 x i1> undef to <64
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%I64 = sext i1 undef to i64
%V2i64 = sext <2 x i1> undef to <2 x i64>
%V4i64 = sext <4 x i1> undef to <4 x i64>
%V8i64 =
sext <8 x i1> undef to <8 x i64>

%I32 = sext i1 undef to i32
%V2i32 = sext <2 x i1> undef to <2 x i32>
%V4i32 = sext <4 x i1> undef to <4 x i32>
%V8i32 = sext <8 x i1> undef to <8 x i32>
%V16i32 = sext <16 x i1> undef to <16 x i32>

%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>

%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>

ret i32 undef
}

```

```

define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to
<2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to
<16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to
<32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 46 for instruction: %V64i64 = trunc <64 x i64> undef to
<64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction:
%V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to
<32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V64i32 = trunc <64 x i32> undef to
<64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16
x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = trunc <32 x i16> undef to <32
x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 18 for instruction: %V64i16 = trunc <64 x i16> undef to
<64 x i1>
; AVX-NEXT: Cost Model:
Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x
i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x

```

i1>  
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef  
;  
; AVX512VL256-LABEL: 'trunc\_vXi1'  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64>  
undef to <2 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4  
x i64> undef to <4 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64>  
undef to <8 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64>  
undef to <16 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64>  
undef to <32 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 44 for instruction: %V64i64 = trunc <64 x i64>  
undef to <64 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32>  
undef to <2 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32>  
undef to <4 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32>  
undef to <8 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32>  
undef  
to <16 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V32i32 = trunc <32 x i32>  
undef to <32 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V64i32 = trunc <64 x i32>  
undef to <64 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16>  
undef to <2 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = trunc <4 x i16>  
undef to <4 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16>  
undef to <8 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V16i16 = trunc <16 x i16>  
undef to <16 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 16 for instruction: %V32i16 = trunc <32 x i16>  
undef to <32 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 32 for instruction: %V64i16 = trunc <64 x i16>  
undef to  
<64 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef  
to <2 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef  
to <4 x i1>  
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef  
to <8 x i1>

```

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V16i8 = trunc <16 x i8>
undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 17 for instruction: %V32i8 = trunc <32 x i8>
undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 34 for instruction: %V64i8 = trunc <64 x i8>
undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'trunc_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64>
undef to <2 x i1>
; AVX512VL512-NEXT:
Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64>
undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64>
undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 44 for instruction: %V64i64 = trunc <64 x i64>
undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32>
undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32>
undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32>
undef to <8 x i1>
; AVX512VL512-NEXT:
Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V32i32 = trunc <32 x i32>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V64i32 = trunc <64 x i32>
undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16>
undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = trunc <4 x i16>
undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16>
undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = trunc <16 x i16>
undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = trunc <32 x i16>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model:
Found an estimated cost of 14 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef
to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef

```



```

to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef
to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = trunc <16 x i8>
undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = trunc <32 x i8>
undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = trunc <64 x i8>
undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'trunc_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated
cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4
x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8
x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to
<16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to
<32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to
<64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2
x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4
x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32>
undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to
<16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to
<32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V64i32 = trunc <64 x i32> undef to
<64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2
x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4
x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8
x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to
<16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = trunc <32 x i16> undef to
<32 x i1>
; SKX256-NEXT: Cost Model: Found

```

an estimated cost of 11 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>  
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef  
;  
; SKX512-LABEL: 'trunc\_vXi1'  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>  
; SKX512-NEXT: Cost Model:  
Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>  
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>

```

; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to
<32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction:
%V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x
i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x
i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x
i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to
<16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to
<32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = trunc <64 x i8> undef to
<64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%V2i64 = trunc <2 x i64> undef to <2 x i1>
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64>
undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>

%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>

%V2i16 = trunc <2 x i16> undef to <2 x i1>
%V4i16 = trunc <4 x i16> undef to <4 x i1>
%V8i16 = trunc <8 x i16> undef to <8 x i1>
%V16i16 = trunc <16 x i16> undef to <16 x i1>
%V32i16 = trunc <32 x i16> undef to <32 x i1>
%V64i16 = trunc <64 x i16> undef to <64 x i1>

%V2i8 = trunc <2 x i8> undef to <2 x i1>
%V4i8 = trunc <4 x i8> undef to <4 x i1>
%V8i8 = trunc <8 x i8> undef to <8 x i1>
%V16i8 = trunc <16 x i8> undef to <16 x i1>
%V32i8 = trunc <32 x i8> undef to <32 x i1>
%V64i8 = trunc <64 x i8> undef to <64 x i1>

ret i32 undef

```

}

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jar/org/apache/commons/logging/impl/NoOpLog.java
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/\*\*

\* This is a special exception that is used to stop parsing when  
\* search for an element. For instance, when searching  
\* for xml:stylesheet  
\* PIs, it is used to stop the parse once the document element is found.  
\* @see StylesheetPIHandler  
\* @xsl.usage internal  
\*/

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jar/org/apache/xalan/transformer/QueuedEvents.java

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jar/org/apache/xalan/xsltc/compiler/XslAttribute.java

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jar/org/apache/xml/dtm/ref/NodeLocator.java

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/opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/FuncBoolean.java

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\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/trax/TransformerHandlerImpl.java

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/When.java

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\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
jar/org/apache/xml/res/XMLEResources\_es.java  
\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
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\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/serialize/Serializer.java  
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 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/axes/BasicTestIterator.java  
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\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/trace/TraceListenerEx3.java  
\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/res/XSLTErrorResources\_pt\_BR.java  
\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/ToXMLSAXHandler.java  
\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/objects/XNumber.java  
\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/OutputPropertyUtils.java  
\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/TopLevelElement.java  
\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-

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jar/org/apache/xalan/xsltc/compiler/Copy.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
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jar/org/apache/xpath/functions/FuncNumber.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utis/Constants.java
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
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* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
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 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/XSLTVisitor.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/LiteralExpr.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/ApplyImports.java  
 \*  
 /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/sql/ConnectionPoolManager.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/ProcessorAttributeSet.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/operations/Quo.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/NSInfo.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/extensions/ExtensionHandlerJavaClass.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/dom3/DOMConstants.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/ref/ExpandedNameTable.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/QName.java  
 \*  
 /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/DTMManager.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/GenerateIdCall.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/QName.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/TransformSnapshot.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/utils/SerializerMessages\_ca.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/sql/QueryParameter.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/util/ErrorMessageMessages\_es.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/ProcessorOutputElem.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/Param.java  
 \*  
 /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/AttributeValueTemplate.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/extensions/ObjectFactory.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/res/XPATHErrorResources\_ru.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-

jar/org/apache/xalan/xsltc/compiler/util/SlotAllocator.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/xsltc/compiler/Number.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xml/utils/res/XResources\_el.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/transformer/TransformerHandlerImpl.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/xsltc/trax/DOM2TO.java  
 \*  
 /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/templates/Stylesheet.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/VariableStack.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/DTMWSFilter.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xpath/axes/FilterExprIteratorSimple.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/res/XSLTErrorResources\_zh\_CN.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/xsltc/dom/MultiValuedNodeHeapIterator.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/res/XSLTErrorResources\_zh\_TW.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/templates/ElemElement.java  
 \*  
 /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/xsltc/compiler/ConcatCall.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/res/XSLTErrorResources\_de.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/transformer/ResultNameSpace.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/processor/ProcessorLRE.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xml/serializer/utils/SerializerMessages\_fr.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xalan/xsltc/trax/ObjectFactory.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xml/utils/res/XResources\_ja\_JP\_A.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/operations/Gt.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xml/res/XMLErrorResources\_fr.java  
 \*  
 /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xml/res/XMLErrorResources\_cs.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
 jar/org/apache/xml/dtm/DTMConfigurationException.java  
 \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-

```
jar/org/apache/xml/dtm/ref/DTMNodeList.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/dtm/Axis.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/templates/XUnresolvedVariable.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xpath/functions/FuncFalse.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/SecuritySupport.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/util/ErrorMessage_sl.java
*
/opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/dom/AbsoluteIterator.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/dom/CurrentNodeListIterator.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/IllegalCharException.java
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```

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*/
/*
* $Id: Version.src 1225426 2011-12-29 04:13:08Z mrglavas $
*/
package org.apache.xalan;
```

```
/**
* Administrative
* class to keep track of the version number of
* the Xalan release.
* <P>This class implements the upcoming standard of having
* org.apache.project-name.Version.getVersion() be a standard way
* to get version information. This class will replace the older
```

```

* org.apache.xalan.processor.Version class.</P>
* <P>See also: org/apache/xalan/res/XSLTInfo.properties for
* information about the version of the XSLT spec we support.</P>
* @xsl.usage general
*/
public class Version
{

/**
 * Get the basic version string for the current Xalan release.
 * Version String formatted like
 * <CODE>"<B>Xalan</B> <B>Java</B> v.r[.dd| <B>D</B>]</CODE>."
 *
 * Futurework: have this read version info from jar manifest.
 *
 * @return String denoting our current version
 */
public static String getVersion()
{
    return getProduct()+" "+getImplementationLanguage()+" "
        +getMajorVersionNum()+"."+getReleaseVersionNum()+"."

        +( (getDevelopmentVersionNum() > 0) ?
            ("D"+getDevelopmentVersionNum()) : (""+getMaintenanceVersionNum()));
}

/**
 * Print the processor version to the command line.
 *
 * @param argv command line arguments, unused.
 */
public static void main(String argv[])
{
    System.out.println(getVersion());
}

/**
 * Name of product: Xalan.
 */
public static String getProduct()
{
    return "Xalan";
}

/**
 * Implementation Language: Java.
 */
public static String getImplementationLanguage()

```



```

{
    return "Java";
}

/**
 * Major version number.
 * Version number. This changes only when there is a
 *     significant, externally apparent enhancement from
 *     the previous release. 'n' represents the n'th
 *     version.
 *
 *     Clients should carefully consider the implications
 *     of new versions as external interfaces and behaviour
 *
 *     may have changed.
 */
public static int getMajorVersionNum()
{
    return @version.VERSION@;
}

/**
 * Release Number.
 * Release number. This changes when:
 *     - a new set of functionality is to be added, eg,
 *       implementation of a new W3C specification.
 *     - API or behaviour change.
 *     - its designated as a reference release.
 */
public static int getReleaseVersionNum()
{
    return @version.RELEASE@;
}

/**
 * Maintenance Drop Number.
 * Optional identifier used to designate maintenance
 *     drop applied to a specific release and contains
 *     fixes for defects reported. It maintains compatibility
 *     with the release and contains no API changes.
 *     When missing, it designates the final and complete
 *     development drop for a release.
 */
public static int getMaintenanceVersionNum()
{
    return

```

```
@version.MINOR@;
```

```
}
```

```
/**
```

```
* Development Drop Number.
```

```
* Optional identifier designates development drop of
```

```
* a specific release. D01 is the first development drop  
* of a new release.
```

```
*
```

```
* Development drops are works in progress towards a  
* completed, final release. A specific development drop  
* may not completely implement all aspects of a new  
* feature, which may take several development drops to  
* complete. At the point of the final drop for the  
* release, the D suffix will be omitted.
```

```
*
```

```
* Each 'D' drops can contain functional enhancements as  
* well as defect fixes. 'D' drops may not be as stable as  
* the final releases.
```

```
*/
```

```
public static int getDevelopmentVersionNum()
```

```
{
```

```
try {
```

```
    if ((new String("@version.DEVELOPER@")).length() == 0)
```

```
        return 0;
```

```
    else
```

```
        return Integer.parseInt("@version.DEVELOPER@");
```

```
    } catch (NumberFormatException nfe) {
```

```
        return 0;
```

```
    }
```

```
}
```

```
}
```

Found in path(s):

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/Version.src
```

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```
/*
```

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```
*
```

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\*/

/\*

\* \$Id: Version.src 468654 2006-10-28 07:09:23Z minchau \$

\*/

```
package org.apache.xml.serializer;
```

/\*\*

\* Administrative class to keep track of the version number of  
\* the Serializer release.  
\* <P>This class implements the upcoming standard of having  
\* org.apache.project-name.Version.getVersion() be a standard way  
\* to get version information.</P>

\* @xsl.usage general

\*/

```
public final class Version
```

```
{
```

/\*\*

\* Get the basic version string for the current Serializer.

\* Version String formatted like

\* <CODE>"<B>Serializer</B> <B>Java</B> v.r[.dd| <B>D</B>nn]"</CODE>.

\*

\* Futurework: have this read version info from jar manifest.

\*

\* @return String denoting our current version

\*/

```
public static String getVersion()
```

```
{
```

```
    return getProduct()+" "+getImplementationLanguage()+" "
```

```
        +getMajorVersionNum()+". "+getReleaseVersionNum()+". "
```

```
        +( (getDevelopmentVersionNum() > 0) ?
```

```
            ("D"+getDevelopmentVersionNum()) : (""+getMaintenanceVersionNum()));
```

```
}
```

/\*\*

\* Print the processor version to the command

line.

\*

\* @param argv command line arguments, unused.

\*/

```
public static void main(String argv[])
```

```
{
```

```
    System.out.println(getVersion());
```

```

}

/**
 * Name of product: Serializer.
 */
public static String getProduct()
{
    return "Serializer";
}

/**
 * Implementation Language: Java.
 */
public static String getImplementationLanguage()
{
    return "Java";
}

/**
 * Major version number.
 * Version number. This changes only when there is a
 *     significant, externally apparent enhancement from
 *     the previous release. 'n' represents the n'th
 *     version.
 *
 * Clients should carefully consider the implications
 * of new versions as external interfaces and behaviour
 * may have changed.
 */
public static int getMajorVersionNum()
{
    return @version.VERSION@;
}

/**
 * Release Number.
 * Release number. This
changes when:
 *     - a new set of functionality is to be added, eg,
 *     implementation of a new W3C specification.
 *     - API or behaviour change.
 *     - its designated as a reference release.
 */
public static int getReleaseVersionNum()
{
    return @version.RELEASE@;
}

```

```

}

/**
 * Maintenance Drop Number.
 * Optional identifier used to designate maintenance
 * drop applied to a specific release and contains
 * fixes for defects reported. It maintains compatibility
 * with the release and contains no API changes.
 * When missing, it designates the final and complete
 * development drop for a release.
 */
public static int getMaintenanceVersionNum()
{
    return @version.MINOR@;
}

/**
 * Development Drop Number.
 * Optional identifier designates development drop of
 * a specific release. D01 is the first development
drop
 * of a new release.
 *
 * Development drops are works in progress towards a
 * completed, final release. A specific development drop
 * may not completely implement all aspects of a new
 * feature, which may take several development drops to
 * complete. At the point of the final drop for the
 * release, the D suffix will be omitted.
 *
 * Each 'D' drops can contain functional enhancements as
 * well as defect fixes. 'D' drops may not be as stable as
 * the final releases.
 */
public static int getDevelopmentVersionNum()
{
    try {
        if ((new String("@version.DEVELOPER@")).length() == 0)
            return 0;
        else
            return Integer.parseInt("@version.DEVELOPER@");
    } catch (NumberFormatException nfe) {
        return 0;
    }
}
}
}

```

Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/Version.src  
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\*/

Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
jar/org/apache/xalan/xsltc/compiler/XPathLexer.java  
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\*/

// Proprietary

/\*\* The 'document-location()' id (Proprietary). \*/

Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
jar/org/apache/xpath/compiler/FunctionTable.java

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 * WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied.
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 * limitations under the License.
 */
```

```
/**
 * Major version number.
 * Version number. This changes only when there is a
 * significant, externally apparent
enhancement from
 * the previous release. 'n' represents the n'th
 * version.
 *
 * Clients should carefully consider the implications
 * of new versions as external interfaces and behaviour
 * may have changed.
 */
```

Found in path(s):

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/Version.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/processor/XSLProcessorVersion.java
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/Version.java
```

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 *
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```

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* limitations under the License.
*/
```

```
/*
* $Id: xpath.cup 1225752 2011-12-30 04:12:46Z mrglavas $
*/
```

```
/*
* @author Jacek Ambroziak
* @author
Santiago Pericas-Geertsen
* @author Morten Jorgensen
* @author G. Todd Miller
*/
```

```
package org.apache.xalan.xsltc.compiler;
```

```
import java.util.Stack;
import java.util.Vector;
import java.io.StringReader;
import java_cup.runtime.*;
```

```
import org.apache.xml.dtm.DTM;
import org.apache.xalan.xsltc.DOM;
import org.apache.xml.dtm.Axis;
import org.apache.xalan.xsltc.runtime.Operators;
import org.apache.xalan.xsltc.compiler.util.ErrorMsg;
```

```
parser code {:
```

```
/**
 * Used by function calls with no args.
 */
static public final Vector EmptyArgs = new Vector(0);
```

```
/**
 * Reference to non-existing variable.
 */
static public final VariableRef DummyVarRef = null;
```

```
/**
 * Reference to the Parser class.
 */
private Parser _parser;
```



```

private XSLTC _xslt;

/**
 * String representation of the expression being parsed.
 */
private String _expression;

/**
 * Line number where this expression/pattern
was declared.
 */
private int _lineNumber = 0;

/**
 * Reference to the symbol table.
 */
public SymbolTable _symbolTable;

public XPathParser(Parser parser) {
    _parser = parser;
    _xslt = parser.getXSLTC();
    _symbolTable = parser.getSymbolTable();
}

public int getLineNumber() {
    return _lineNumber;
}

public QName getQNameIgnoreDefaultNs(String name) {
    return _parser.getQNameIgnoreDefaultNs(name);
}

public QName getQName(String namespace, String prefix, String localname) {
    return _parser.getQName(namespace, prefix, localname);
}

public void setMultiDocument(boolean flag) {
    _xslt.setMultiDocument(flag);
}

public void setCallsNodeset(boolean flag) {
    _xslt.setCallsNodeset(flag);
}

public void setHasIdCall(boolean flag) {
    _xslt.setHasIdCall(flag);
}

```

```

/**
 * This
method is similar to findNodeType(int, Object) except that it
 * creates a StepPattern instead of just returning a node type. It also
 * differs in the way it handles "{uri}:*" and "{uri}:@*". The last two
 * patterns are expanded as "[namespace-uri() = 'uri']" and
 * "@*[namespace-uri() = 'uri']", respectively. This expansion considerably
 * simplifies the grouping of patterns in the Mode class. For this
 * expansion to be correct, the priority of the pattern/template must be
 * set to -0.25 (when no other predicates are present).
 */
public StepPattern createStepPattern(int axis, Object test, Vector predicates) {
int nodeType;

if (test == null) { // "*"
    nodeType = (axis == Axis.ATTRIBUTE) ? NodeTest.ATTRIBUTE :
(axis == Axis.NAMESPACE) ? -1 : NodeTest.ELEMENT;

    return new StepPattern(axis, nodeType, predicates);
}
else if (test instanceof Integer) {
nodeType = ((Integer) test).intValue();

return new StepPattern(axis, nodeType, predicates);
}
else {
QName name = (QName)test;
boolean setPriority = false;

if (axis == Axis.NAMESPACE) {
nodeType = (name.toString().equals("*")) ? -1
: _xsltC.registerNamespacePrefix(name);
}
else {
final String uri = name.getNamespace();
final String local = name.getLocalPart();
final QName namespace_uri =
_parser.getQNameIgnoreDefaultNs("namespace-uri");

// Expand {uri}:* to *[namespace-uri() = 'uri'] - same for @*
if (uri != null && (local.equals("") || local.equals("@*"))) {
    if (predicates == null) {
predicates = new Vector(2);
}

// Priority is set by hand if no other predicates exist

```

```

    setPriority = (predicates.size() == 0);

    predicates.add(
new Predicate(
    new EqualityExpr(Operators.EQ,
new NamespaceUriCall(namespace_uri),
new LiteralExpr(uri)));
}

if (local.equals("*"))
{
    nodeType = (axis == Axis.ATTRIBUTE) ? NodeTest.ATTRIBUTE
: NodeTest.ELEMENT;
}
else if (local.equals("@*")) {
    nodeType = NodeTest.ATTRIBUTE;
}
else {
    nodeType = (axis == Axis.ATTRIBUTE) ? _xsltc.registerAttribute(name)
: _xsltc.registerElement(name);
}
}

final StepPattern result = new StepPattern(axis, nodeType, predicates);

// Set priority for case prefix:* and prefix:@* (no predicates)
if (setPriority) {
result.setPriority(-0.25);
}

return result;
}
}

public int findNodeType(int axis, Object test) {
if (test == null) { // *
    return (axis == Axis.ATTRIBUTE) ?
NodeTest.ATTRIBUTE :
(axis == Axis.NAMESPACE) ? -1 : NodeTest.ELEMENT;
}
else if (test instanceof Integer) {
    return ((Integer)test).intValue();
}
else {
QName name = (QName)test;

if (axis == Axis.NAMESPACE) {
return (name.toString().equals("*"))

```

```

? -1
    : _xslt.registerNamespacePrefix(name);
    }

    if (name.getNamespace() == null) {
final String local = name.getLocalPart();

if (local.equals("*")) {
    return (axis == Axis.ATTRIBUTE) ? NodeTest.ATTRIBUTE
    : NodeTest.ELEMENT;
}
else if (local.equals("@*")) {
    return NodeTest.ATTRIBUTE;
}
}

    return (axis == Axis.ATTRIBUTE) ? _xslt.registerAttribute(name)
: _xslt.registerElement(name);
    }
}

/**
 * Parse the expression passed to the current scanner. If this
 * expression contains references to local variables and it will be
 * compiled in an external module (not in the main class) request
 * the current template to create a new variable stack frame.
 *
 * @param lineNumber Line where the current expression is defined.
 * @param external Set to <tt>true</tt> if this expression is
 * compiled in a separate module.
 *
 */
public Symbol parse(String expression, int lineNumber) throws Exception {
    try {
        _expression = expression;
        _lineNumber = lineNumber;
        return super.parse();
    }
    catch (IllegalCharException e) {
        ErrorMessage err = new ErrorMessage(ErrorMessage.ILLEGAL_CHAR_ERR,
            lineNumber, e.getMessage());
        _parser.reportError(Constants.FATAL, err);
    }
    return null;
}

/**

```

```

* Lookup a variable or parameter in the symbol table given its name.
*
* @param name Name of the symbol being looked up.
*/
final SyntaxTreeNode lookupName(QName name) {
    // Is it a local var or param ?
    final SyntaxTreeNode result = _parser.lookupVariable(name);
if (result != null)
    return(result);
    else
    return(_symbolTable.lookupName(name));
}

public final void addError(ErrorMsg error) {
_parser.reportError(Constants.ERROR,
error);
}

public void report_error(String message, Object info) {
final ErrorMsg err = new ErrorMsg(ErrorMsg.SYNTAX_ERR, _lineNumber,
_expression);
_parser.reportError(Constants.FATAL, err);
}

public void report_fatal_error(String message, Object info) {
// empty
}

public RelativeLocationPath insertStep(Step step, RelativeLocationPath rlp) {
if (rlp instanceof Step) {
return new ParentLocationPath(step, (Step) rlp);
}
else if (rlp instanceof ParentLocationPath) {
final ParentLocationPath plp = (ParentLocationPath) rlp;
final RelativeLocationPath newrlp = insertStep(step, plp.getPath());
return new ParentLocationPath(newrlp, plp.getStep());
}
else {
addError(new ErrorMsg(ErrorMsg.INTERNAL_ERR, "XPathParser.insertStep"));
return rlp;
}
}

/**
* Returns true if the axis applies to elements only. The axes
* child, attribute, namespace, descendant
result in non-empty
* nodesets only if the context node is of type element.

```

```

*/
public boolean isElementAxis(int axis) {
return (axis == Axis.CHILD || axis == Axis.ATTRIBUTE ||
axis == Axis.NAMESPACE || axis == Axis.DESCENDANT);
}
:}

terminal SLASH, DOT, LBRACK, RBRACK, VBAR, LPAREN, RPAREN, STAR, COMMA;
terminal DOLLAR, ATSIGN;
terminal DDOT, DCOLON, DSLASH;
terminal EQ, NE;
terminal LT, GT, LE, GE;
terminal PLUS, MINUS, DIV, MOD, MULT;
terminal String Literal;
terminal String QNAME;
terminal ID, KEY, TEXT, NODE, OR, AND, COMMENT, PI, PIPARAM, PRECEDINGSIBLING;
terminal SELF, PARENT, CHILD, ATTRIBUTE, ANCESTOR, ANCESTORORSELF, DESCENDANT;
terminal DESCENDANTORSELF, FOLLOWING, FOLLOWINGSIBLING, NAMESPACE, PRECEDING;
terminal Double REAL;
terminal Long INT;
terminal PATTERN, EXPRESSION;

non terminal SyntaxTreeNode TopLevel;

non terminal Expression Expr, Argument, LocationPath;
non terminal Expression Predicate, FilterExpr, Step;
non
terminal Expression OrExpr, AndExpr, EqualityExpr;
non terminal Expression RelationalExpr, AdditiveExpr;
non terminal Expression MultiplicativeExpr, UnaryExpr;
non terminal Expression VariableReference, FunctionCall;
non terminal Expression PrimaryExpr, UnionExpr, PathExpr, AbbreviatedStep;
non terminal Expression RelativeLocationPath, AbbreviatedRelativeLocationPath;
non terminal Expression AbsoluteLocationPath, AbbreviatedAbsoluteLocationPath;

non terminal Object NodeTest, NameTest;

non terminal IdKeyPattern IdKeyPattern;
non terminal Pattern Pattern;
non terminal Pattern LocationPathPattern;
non terminal StepPattern ProcessingInstructionPattern;
non terminal RelativePathPattern RelativePathPattern;
non terminal StepPattern StepPattern;
non terminal Object NodeTestPattern, NameTestPattern;

non terminal Vector Predicates, NonemptyArgumentList;
non terminal QName QName, FunctionName, VariableName;
non terminal Integer AxisName, AxisSpecifier;

```

non terminal

Integer ChildOrAttributeAxisSpecifier;

precedence left VBAR;

precedence left OR;

precedence left AND;

precedence nonassoc EQ, NE;

precedence left LT, GT, LE, GE;

precedence left PLUS, MINUS;

precedence left DIV, MOD, MULT;

precedence left DOLLAR;

precedence left ATSIGN;

precedence right DCOLON;

start with TopLevel;

TopLevel ::= PATTERN Pattern:pattern

{: RESULT = pattern; :}

| EXPRESSION Expr:expr

{: RESULT = expr; :};

/\* ----- Patterns ----- \*/

Pattern ::= LocationPathPattern:lpp

{: RESULT = lpp; :}

| LocationPathPattern:lpp VBAR Pattern:p

{: RESULT = new AlternativePattern(lpp, p); :};

LocationPathPattern ::= SLASH

{: RESULT = new AbsolutePathPattern(null); :}

| SLASH RelativePathPattern:rpp

{: RESULT = new AbsolutePathPattern(rpp); :}

| IdKeyPattern:ikp

{: RESULT = ikp; :}

| IdKeyPattern:ikp SLASH RelativePathPattern:rpp

{: RESULT = new ParentPattern(ikp, rpp); :}

| IdKeyPattern:ikp DSLASH RelativePathPattern:rpp

{: RESULT = new AncestorPattern(ikp, rpp); :}

| DSLASH RelativePathPattern:rpp

```

{: RESULT = new AncestorPattern(rpp); :}

| RelativePathPattern:rpp
{: RESULT = rpp; :};

IdKeyPattern ::= ID LPAREN Literal:l RPAREN
{: RESULT = new IdPattern(l);
  parser.setHasIdCall(true);
:}

| KEY LPAREN Literal:l1 COMMA Literal:l2 RPAREN
{: RESULT = new KeyPattern(l1, l2); :};

ProcessingInstructionPattern ::= PIPARAM LPAREN Literal:l RPAREN
{: RESULT = new ProcessingInstructionPattern(l); :};

RelativePathPattern ::= StepPattern:sp
{: RESULT = sp; :}

| StepPattern:sp SLASH RelativePathPattern:rpp
{: RESULT
= new ParentPattern(sp, rpp); :}

| StepPattern:sp DSLASH RelativePathPattern:rpp
{: RESULT = new AncestorPattern(sp, rpp); :};

StepPattern ::= NodeTestPattern:nt
{:
RESULT = parser.createStepPattern(Axis.CHILD, nt, null);
:}

| NodeTestPattern:nt Predicates:pp
{:
RESULT = parser.createStepPattern(Axis.CHILD, nt, pp);
:}

| ProcessingInstructionPattern:pip
{: RESULT = pip; :}

| ProcessingInstructionPattern:pip Predicates:pp
{: RESULT = (ProcessingInstructionPattern)pip.setPredicates(pp); :}

| ChildOrAttributeAxisSpecifier:axis NodeTestPattern:nt
{:
RESULT = parser.createStepPattern(axis.intValue(), nt, null);
:}

| ChildOrAttributeAxisSpecifier:axis

```



```

NodeTestPattern:nt Predicates:pp
  {
    RESULT = parser.createStepPattern(axis.intValue(), nt, pp);
  }

  | ChildOrAttributeAxisSpecifier,axis ProcessingInstructionPattern:pip
  {
    RESULT = pip; // TODO: report error if axis is attribute
  }

  | ChildOrAttributeAxisSpecifier,axis ProcessingInstructionPattern:pip
Predicates:pp
  {
    // TODO: report error if axis is attribute
    RESULT = (ProcessingInstructionPattern)pip.setPredicates(pp);
  };

```

```

NodeTestPattern ::= NameTestPattern:nt
  { : RESULT = nt; : }

  | NODE
  { : RESULT = new Integer(NodeTest.ANODE); : }

  | TEXT
  { : RESULT = new Integer(NodeTest.TEXT); : }

  | COMMENT
  { : RESULT = new Integer(NodeTest.COMMENT); : }

  | PI
  { : RESULT = new Integer(NodeTest.PI); : };

```

```

NameTestPattern ::= STAR
  { : RESULT = null; : }

  | QName:qn
  { : RESULT = qn; : };

```

```

ChildOrAttributeAxisSpecifier ::= ATSIGN

  { : RESULT = new Integer(Axis.ATTRIBUTE); : }

  | CHILD DCOLON
  { : RESULT = new Integer(Axis.CHILD); : }

  | ATTRIBUTE DCOLON
  { : RESULT = new Integer(Axis.ATTRIBUTE); : };

```

```

Predicates ::= Predicate:p
    { :
    Vector temp = new Vector();
    temp.addElement(p);
    RESULT = temp;
    : }

    | Predicate:p Predicates:pp
    { : pp.insertElementAt(p, 0); RESULT = pp; : };

```

```

Predicate ::= LBRACK Expr:e RBRACK
    { :
    RESULT = new Predicate(e);
    : };

```

/\* ----- Expressions ----- \*/

```

Expr ::= OrExpr:ex
    { : RESULT = ex; : };

```

```

OrExpr ::= AndExpr:ae
    { : RESULT = ae; : }

    | OrExpr:oe OR AndExpr:ae
    { : RESULT = new LogicalExpr(LogicalExpr.OR, oe, ae); : };

```

```

AndExpr ::= EqualityExpr:e
    { : RESULT = e; : }

    | AndExpr:ae AND EqualityExpr:ee
    { : RESULT = new LogicalExpr(LogicalExpr.AND,
    ae, ee); : };

```

```

EqualityExpr ::= RelationalExpr:re
    { : RESULT = re; : }

    | EqualityExpr:ee EQ RelationalExpr:re
    { : RESULT = new EqualityExpr(Operators.EQ, ee, re); : }

    | EqualityExpr:ee NE RelationalExpr:re
    { : RESULT = new EqualityExpr(Operators.NE, ee, re); : };

```

```

RelationalExpr ::= AdditiveExpr:ae
    { : RESULT = ae; : }

    | RelationalExpr:re LT AdditiveExpr:ae
    { : RESULT = new RelationalExpr(Operators.LT, re, ae); : }

```

| RelationalExpr:re GT AdditiveExpr:ae  
{: RESULT = new RelationalExpr(Operators.GT, re, ae); :}

| RelationalExpr:re LE AdditiveExpr:ae  
{: RESULT = new RelationalExpr(Operators.LE, re, ae); :}

| RelationalExpr:re GE AdditiveExpr:ae  
{: RESULT = new RelationalExpr(Operators.GE, re, ae); :};

AdditiveExpr ::= MultiplicativeExpr:me

{: RESULT = me; :}

| AdditiveExpr:ae PLUS MultiplicativeExpr:me  
{: RESULT = new BinOpExpr(BinOpExpr.PLUS,  
ae, me); :}

| AdditiveExpr:ae MINUS MultiplicativeExpr:me  
{: RESULT = new BinOpExpr(BinOpExpr.MINUS, ae, me); :};

MultiplicativeExpr ::= UnaryExpr:ue

{: RESULT = ue; :}

| MultiplicativeExpr:me MULT UnaryExpr:ue  
{: RESULT = new BinOpExpr(BinOpExpr.TIMES, me, ue); :}

| MultiplicativeExpr:me DIV UnaryExpr:ue  
{: RESULT = new BinOpExpr(BinOpExpr.DIV, me, ue); :}

| MultiplicativeExpr:me MOD UnaryExpr:ue  
{: RESULT = new BinOpExpr(BinOpExpr.MOD, me, ue); :};

UnaryExpr ::= UnionExpr:ue

{: RESULT = ue; :}

| MINUS UnaryExpr:ue  
{: RESULT = new UnaryOpExpr(ue); :};

UnionExpr ::= PathExpr:pe

{: RESULT = pe; :}

| PathExpr:pe VBAR UnionExpr:rest  
{: RESULT = new UnionPathExpr(pe, rest); :};

PathExpr ::= LocationPath:lp

{: RESULT = lp; :}

| FilterExpr:fexp  
{: RESULT = fexp; :}

```

    | FilterExpr:fexp
SLASH RelativeLocationPath:rlp
    {: RESULT = new FilterParentPath(fexp, rlp); :}

    | FilterExpr:fexp DSLASH RelativeLocationPath:rlp
    {:
        //
        // Expand '/' into '/descendant-or-self::node()' or
        // into '/descendant-or-self::*'
        //
        int nodeType = DOM.NO_TYPE;
        if (rlp instanceof Step &&
            parser.isElementAxis(((Step) rlp).getAxis()))
        {
            nodeType = DTM.ELEMENT_NODE;
        }
        final Step step = new Step(Axis.DESCENDANTORSELF, nodeType, null);
        FilterParentPath fpp = new FilterParentPath(fexp, step);
        fpp = new FilterParentPath(fpp, rlp);
        if (fexp instanceof KeyCall == false) {
            fpp.setDescendantAxis();
        }
        RESULT = fpp;
    :};

```

```

LocationPath ::= RelativeLocationPath:rlp
    {: RESULT = rlp; :}

```

```

    | AbsoluteLocationPath:alp
    {: RESULT = alp; :};

```

```

RelativeLocationPath ::= Step:step
    {: RESULT
= step; :}

```

```

    | RelativeLocationPath:rlp SLASH Step:step
    {:
        if (rlp instanceof Step && ((Step) rlp).isAbbreviatedDot()) {
            RESULT = step;    // Remove './' from the middle
        }
        else if (((Step) step).isAbbreviatedDot()) {
            RESULT = rlp;    // Remove './' from the end
        }
        else {
            RESULT =
                new ParentLocationPath((RelativeLocationPath) rlp, step);
        }
    }

```

```

:}

| AbbreviatedRelativeLocationPath:arlp
{: RESULT = arlp; :};

AbsolutePath ::= SLASH
{: RESULT = new AbsoluteLocationPath(); :}

| SLASH RelativeLocationPath:rlp
{: RESULT = new AbsoluteLocationPath(rlp); :}

| AbbreviatedAbsolutePath:aalp
{: RESULT = aalp; :};

AbbreviatedRelativeLocationPath ::= RelativeLocationPath:rlp DSLASH Step:step
{:
final Step right = (Step)step;
final
int axis = right.getAxis();
final int type = right.getNodeType();
final Vector predicates = right.getPredicates();

if ((axis == Axis.CHILD) && (type != NodeTest.ATTRIBUTE)) {
// Compress '//child:E' into 'descendant::E' - if possible
if (predicates == null) {
right.setAxis(Axis.DESCENDANT);
if (rlp instanceof Step && ((Step)rlp).isAbbreviatedDot()) {
RESULT = right;
}
else {
// Expand 'rlp//child::E' into 'rlp/descendant::E'
RelativeLocationPath left = (RelativeLocationPath)rlp;
RESULT = new ParentLocationPath(left, right);
}
}
else {
// Expand '//step' -> 'descendant-or-self::*step'
if (rlp instanceof Step && ((Step)rlp).isAbbreviatedDot()) {

Step left = new Step(Axis.DESCENDANTORSELF,
DTM.ELEMENT_NODE, null);
RESULT = new ParentLocationPath(left, right);
}
else {
// Expand 'rlp//step' -> 'rlp/descendant-or-self::*step'
RelativeLocationPath left = (RelativeLocationPath)rlp;
Step mid = new Step(Axis.DESCENDANTORSELF,
DTM.ELEMENT_NODE, null);

```

```

        ParentLocationPath ppl = new ParentLocationPath(mid, right);
        RESULT = new ParentLocationPath(left, ppl);
    }
}
}
else if ((axis == Axis.ATTRIBUTE) || (type == NodeTest.ATTRIBUTE)) {
    // Expand 'rlp//step' -> 'rlp/descendant-or-self::*step'
    RelativeLocationPath left = (RelativeLocationPath)rlp;
    Step middle = new Step(Axis.DESCENDANTORSELF,
DTM.ELEMENT_NODE, null);

    ParentLocationPath ppl = new ParentLocationPath(middle, right);
    RESULT = new ParentLocationPath(left, ppl);
}
else {
    // Expand 'rlp//step' -> 'rlp/descendant-or-self::node()/step'
    RelativeLocationPath left = (RelativeLocationPath)rlp;
    Step middle = new Step(Axis.DESCENDANTORSELF,
DOM.NO_TYPE, null);
    ParentLocationPath ppl = new ParentLocationPath(middle, right);
    RESULT = new ParentLocationPath(left, ppl);
}
};

```

AbbreviatedAbsolutePath ::= DSLASH RelativeLocationPath:rlp

```

{:
    //
    // Expand '/' into '/descendant-or-self::node()' or
    // into '/descendant-or-self::*'
    //
    int nodeType = DOM.NO_TYPE;
    if (rlp instanceof Step &&
        parser.isElementAxis(((Step) rlp).getAxis()))
    {
        nodeType = DTM.ELEMENT_NODE;
    }
    final Step step = new Step(Axis.DESCENDANTORSELF, nodeType, null);
    RESULT =
new AbsoluteLocationPath(parser.insertStep(step,
(RelativeLocationPath) rlp));
};

```

Step ::= NodeTest:ntest

```

{:
    if (ntest instanceof Step) {
        RESULT = (Step)ntest;
    }
}

```

```

    else {
RESULT = new Step(Axis.CHILD,
    parser.findNodeType(Axis.CHILD, ntest),
    null);
    }
:}

| NodeTest:ntest Predicates:pp
{:
    if (ntest instanceof Step) {
        Step step = (Step)ntest;
        step.addPredicates(pp);
        RESULT = (Step)ntest;
    }
    else {
        RESULT = new Step(Axis.CHILD,
            parser.findNodeType(Axis.CHILD, ntest), pp);
    }
:}

| AxisSpecifier:axis NodeTest:ntest Predicates:pp
{: RESULT = new Step(axis.intValue(),
    parser.findNodeType(axis.intValue(), ntest),
    pp);
:}

| AxisSpecifier:axis NodeTest:ntest
{: RESULT = new Step(axis.intValue(),
    parser.findNodeType(axis.intValue(), ntest),
    null);
:}

| AbbreviatedStep:abbrev
{: RESULT = abbrev; :};

AxisSpecifier ::= AxisName:an DCOLON
    {: RESULT = an; :}

| ATSIGN
{: RESULT = new Integer(Axis.ATTRIBUTE); :};

AxisName ::= ANCESTOR
    {: RESULT = new Integer(Axis.ANCESTOR); :}

| ANCESTORORSELF
{: RESULT = new Integer(Axis.ANCESTORORSELF); :}

| ATTRIBUTE

```

```

{: RESULT = new Integer(Axis.ATTRIBUTE); :}

| CHILD
{: RESULT = new Integer(Axis.CHILD); :}

| DESCENDANT
{: RESULT = new Integer(Axis.DESCENDANT); :}

| DESCENDANTORSELF
{: RESULT = new Integer(Axis.DESCENDANTORSELF); :}

| FOLLOWING
{: RESULT = new
Integer(Axis.FOLLOWING); :}

| FOLLOWINGSIBLING
{: RESULT = new Integer(Axis.FOLLOWINGSIBLING); :}

| NAMESPACE
{: RESULT = new Integer(Axis.NAMESPACE); :}

| PARENT
{: RESULT = new Integer(Axis.PARENT); :}

| PRECEDING
{: RESULT = new Integer(Axis.PRECEDING); :}

| PRECEDINGSIBLING
{: RESULT = new Integer(Axis.PRECEDINGSIBLING); :}

| SELF
{: RESULT = new Integer(Axis.SELF); :};

AbbreviatedStep ::= DOT
{: RESULT = new Step(Axis.SELF, NodeTest.ANODE, null); :}

| DDOT
{: RESULT = new Step(Axis.PARENT, NodeTest.ANODE, null); :};

FilterExpr ::= PrimaryExpr:primary
{: RESULT = primary; :}

| PrimaryExpr:primary Predicates:pp
{: RESULT = new FilterExpr(primary, pp); :};

PrimaryExpr ::= VariableReference:vr
{: RESULT = vr; :}

```



```

| LPAREN Expr:ex RPAREN
{: RESULT = ex; :}

| Literal:string
{:

/*
* If the string appears to have the syntax of a QName, store
* namespace info in the literal expression. This is used for
* element-available and function-available functions, among
* others. Also, the default namespace must be ignored.
*/
String namespace = null;
final int index = string.lastIndexOf(':');

if (index > 0) {
    final String prefix = string.substring(0, index);
    namespace = parser._symbolTable.lookupNamespace(prefix);
}
RESULT = (namespace == null) ? new LiteralExpr(string)
    : new LiteralExpr(string, namespace);
:}

| INT:num
{:
long value = num.longValue();
if (value < Integer.MIN_VALUE || value > Integer.MAX_VALUE) {
RESULT = new RealExpr(value);
}
else {
    if (num.doubleValue() == -0)
        RESULT = new RealExpr(num.doubleValue());
    else if (num.intValue() == 0)
        RESULT = new IntExpr(num.intValue());

    else if (num.doubleValue() == 0.0)
        RESULT = new RealExpr(num.doubleValue());
    else
        RESULT = new IntExpr(num.intValue());
}
:}

| REAL:num
{: RESULT = new RealExpr(num.doubleValue()); :}

| FunctionCall:fc
{: RESULT = fc; :};

```

VariableReference ::= DOLLAR VariableName:varName

```
{:  
  // An empty QName prefix for a variable or parameter reference  
  // should map to the null namespace and not the default URI.  
  SyntaxTreeNode node = parser.lookupName(varName);  
  
  if (node != null) {  
    if (node instanceof Variable) {  
      RESULT = new VariableRef((Variable)node);  
    }  
    else if (node instanceof Param) {  
      RESULT = new ParameterRef((Param)node);  
    }  
    else {  
      RESULT = new UnresolvedRef(varName);  
    }  
  }  
  
  if (node == null) {  
    RESULT = new UnresolvedRef(varName);  
  }  
};
```

FunctionCall ::= FunctionName:fname LPAREN RPAREN

```
{:  
  
  if (parser.getQNameIgnoreDefaultNs("current").equals(fname)) {  
    RESULT = new CurrentCall(fname);  
  }  
  else if (parser.getQNameIgnoreDefaultNs("number").equals(fname)) {  
    RESULT = new NumberCall(fname, parser.EmptyArgs);  
  }  
  else if (parser.getQNameIgnoreDefaultNs("string").equals(fname)) {  
    RESULT = new StringCall(fname, parser.EmptyArgs);  
  }  
  else if (parser.getQNameIgnoreDefaultNs("concat").equals(fname)) {  
    RESULT = new ConcatCall(fname, parser.EmptyArgs);  
  }  
  else if (parser.getQNameIgnoreDefaultNs("true").equals(fname)) {  
    RESULT = new BooleanExpr(true);  
  }  
  else if (parser.getQNameIgnoreDefaultNs("false").equals(fname)) {  
  
    RESULT = new BooleanExpr(false);  
  }  
  else if (parser.getQNameIgnoreDefaultNs("name").equals(fname)) {  
    RESULT = new NameCall(fname);  
  }  
}
```

```

else if (parser.getQNameIgnoreDefaultNs("generate-id").equals(fname)) {
    RESULT = new GenerateIdCall(fname, parser.EmptyArgs);
}
else if (parser.getQNameIgnoreDefaultNs("string-length").equals(fname)) {
    RESULT = new StringLengthCall(fname, parser.EmptyArgs);
}
else if (parser.getQNameIgnoreDefaultNs("position").equals(fname)) {
    RESULT = new PositionCall(fname);
}
else if (parser.getQNameIgnoreDefaultNs("last").equals(fname)) {
    RESULT = new LastCall(fname);
}
else if (parser.getQNameIgnoreDefaultNs("local-name").equals(fname)) {
    RESULT = new LocalNameCall(fname);
}
else if (parser.getQNameIgnoreDefaultNs("namespace-uri").equals(fname)) {
    RESULT = new NamespaceUriCall(fname);
}
else {
    RESULT = new FunctionCall(fname, parser.EmptyArgs);
}
:}

| FunctionName:fname LPAREN NonemptyArgumentList:argl RPAREN
{:
if (parser.getQNameIgnoreDefaultNs("concat").equals(fname)) {
    RESULT = new ConcatCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("number").equals(fname)) {
    RESULT = new NumberCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("document").equals(fname)) {
parser.setMultiDocument(true);
    RESULT = new DocumentCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("string").equals(fname)) {
    RESULT = new StringCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("boolean").equals(fname)) {
    RESULT = new BooleanCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("name").equals(fname)) {
    RESULT = new NameCall(fname, argl);
}
else if (parser.getQNameIgnoreDefaultNs("generate-id").equals(fname)) {
    RESULT = new GenerateIdCall(fname, argl);
}

```

```

}
else if (parser.getQNameIgnoreDefaultNs("not").equals(fname)) {
    RESULT = new NotCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("format-number").equals(fname)) {
    RESULT = new FormatNumberCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("unparsed-entity-uri").equals(fname)) {
    RESULT = new UnparsedEntityUriCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("key").equals(fname)) {
    RESULT = new KeyCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("id").equals(fname)) {
    RESULT = new KeyCall(fname, arg1);
    parser.setHasIdCall(true);
}
else if (parser.getQNameIgnoreDefaultNs("ceiling").equals(fname))
{
    RESULT = new CeilingCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("round").equals(fname)) {
    RESULT = new RoundCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("floor").equals(fname)) {
    RESULT = new FloorCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("contains").equals(fname)) {
    RESULT = new ContainsCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("string-length").equals(fname)) {
    RESULT = new StringLengthCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("starts-with").equals(fname)) {
    RESULT = new StartsWithCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("function-available").equals(fname)) {
    RESULT = new FunctionAvailableCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("element-available").equals(fname))
{
    RESULT = new ElementAvailableCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("local-name").equals(fname)) {
    RESULT = new LocalNameCall(fname, arg1);
}
else if (parser.getQNameIgnoreDefaultNs("lang").equals(fname)) {
    RESULT = new LangCall(fname, arg1);
}

```

```

}
else if (parser.getQNameIgnoreDefaultNs("namespace-uri").equals(fname)) {
    RESULT = new NamespaceUriCall(fname, argl);
}
else if (parser.getQName(Constants.TRANSLET_URI, "xslt", "cast").equals(fname)) {
    RESULT = new CastCall(fname, argl);
}
// Special case for extension function nodeset()
else if (fname.getLocalPart().equals("nodeset") || fname.getLocalPart().equals("node-set")) {
    parser.setCallsNodeset(true); // implies MultiDOM
    RESULT = new FunctionCall(fname, argl);
}
else {
    RESULT = new FunctionCall(fname, argl);
}
:};

```

NonemptyArgumentList

```

::= Argument:arg
{
    Vector temp = new Vector();
    temp.addElement(arg);
    RESULT = temp;
}

| Argument:arg COMMA NonemptyArgumentList:argl
{: argl.insertElementAt(arg, 0); RESULT = argl; :};

```

FunctionName ::= QName:fname

```

{
    RESULT = fname;
}
:};

```

VariableName ::= QName:vname

```

{
    RESULT = vname;
}
:};

```

Argument ::= Expr:ex

```

{: RESULT = ex; :};

```

NodeTest ::= NameTest:nt

```

{: RESULT = nt; :}

| NODE
{: RESULT = new Integer(NodeTest.ANODE); :}

| TEXT

```

```

{: RESULT = new Integer(NodeTest.TEXT); :}

| COMMENT
{: RESULT = new Integer(NodeTest.COMMENT); :}

| PIPARAM LPAREN Literal:l RPAREN
{:
  QName name = parser.getQNameIgnoreDefaultNs("name");
  Expression exp = new EqualityExpr(Operators.EQ,
    new NameCall(name),
    new LiteralExpr(l));
  Vector predicates = new Vector();
  predicates.addElement(new Predicate(exp));
  RESULT = new Step(Axis.CHILD, NodeTest.PI, predicates);
:}

| PI
{: RESULT = new Integer(NodeTest.PI); :};

NameTest ::= STAR
  {: RESULT = null; :}

  | QName:qn
  {: RESULT = qn; :};

QName ::= QNAME:qname
  {: RESULT = parser.getQNameIgnoreDefaultNs(qname); :}

| DIV
  {: RESULT = parser.getQNameIgnoreDefaultNs("div"); :}

| MOD
  {: RESULT = parser.getQNameIgnoreDefaultNs("mod"); :}

| KEY
  {: RESULT = parser.getQNameIgnoreDefaultNs("key"); :}

| ANCESTOR
  {: RESULT = parser.getQNameIgnoreDefaultNs("child"); :}

| ANCESTORORSELF
  {: RESULT = parser.getQNameIgnoreDefaultNs("ancestor-or-self"); :}

| ATTRIBUTE
  {: RESULT
= parser.getQNameIgnoreDefaultNs("attribute"); :}

| CHILD

```

```

{: RESULT = parser.getQNameIgnoreDefaultNs("child"); :}

| DESCENDANT
{: RESULT = parser.getQNameIgnoreDefaultNs("decendant"); :}

| DESCENDANTORSELF
{: RESULT = parser.getQNameIgnoreDefaultNs("decendant-or-self"); :}

| FOLLOWING
{: RESULT = parser.getQNameIgnoreDefaultNs("following"); :}

| FOLLOWINGSIBLING
{: RESULT = parser.getQNameIgnoreDefaultNs("following-sibling"); :}

| NAMESPACE
{: RESULT = parser.getQNameIgnoreDefaultNs("namespace"); :}

| PARENT
{: RESULT = parser.getQNameIgnoreDefaultNs("parent"); :}

| PRECEDING
{: RESULT = parser.getQNameIgnoreDefaultNs("preceding"); :}

| PRECEDINGSIBLING
{: RESULT = parser.getQNameIgnoreDefaultNs("preceding-sibling"); :}

| SELF
{: RESULT = parser.getQNameIgnoreDefaultNs("self"); :}

| ID
{: RESULT = parser.getQNameIgnoreDefaultNs("id"); :};

```

Found in path(s):

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/xpath.cup
```

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```

Found in path(s):

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/Encodings.properties
```

```
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
```

```
jar/org/apache/xml/serializer/XMLEntities.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/output_text.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/res/XSLTInfo.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/output_xml.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/xsltc/compiler/Makefile.inc
*
/opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/output_unknown.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/HTMLEntities.properties
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xml/serializer/output_html.properties
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*/
// is found, then throw a special exception in order to terminate
```

```
Found in path(s):
* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/processor/TransformerFactoryImpl.java
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```



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\* limitations under the License.  
\*/  
// Attribution to: "Voytenko, Dimitry" <DVoytenko@SECTORBASE.COM>

Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
jar/org/apache/xml/serializer/ToHTMLStream.java

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Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
jar/org/apache/xpath/domapi/XPathStylesheetDOM3Exception.java

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  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/functions/package.html
  - \*
  - /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/transformer/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/client/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/processor/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/domapi/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/templates/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/serializer/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/extensions/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/operations/package.html
  - \*
  - /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/compiler/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/patterns/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/lib/sql/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xslt/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/res/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xml/utils/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/objects/package.html
  - \* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/axes/package.html
  - \*
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\*/

/\*\*

\* Execute the proprietary document-location() function, which returns  
\* a node set of documents.

\* @xsl.usage advanced

\*/

Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-  
jar/org/apache/xpath/functions/FuncDoclocation.java

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\*/

/\*

\* \$Id: XSLProcessorVersion.src 468640 2006-10-28 06:53:53Z minchau \$

\*/

package org.apache.xalan.processor;

/\*\*

\* Administrative class to keep track of the version number of  
\* the Xalan release.

\* <P>See also: org/apache/xalan/res/XSLTInfo.properties</P>

\* @deprecated To be replaced by org.apache.xalan.Version.getVersion()

\* @xsl.usage general

\*/

public class XSLProcessorVersion

{

```

/**
 * Print the processor version to the command line.
 *
 * @param argv command line arguments, unused.
 */
public static void main(String argv[])
{
    System.out.println(S_VERSION);
}

/**
 * Constant name of product.
 */
public static final String PRODUCT = "Xalan";

/**
 * Implementation Language.
 */
public static final String LANGUAGE = "Java";

/**
 * Major version number.
 * Version number. This changes only when there is a
 *     significant, externally apparent enhancement from
 *     the previous release. 'n' represents the n'th
 *     version.
 *
 *     Clients should carefully consider
the implications
 *     of new versions as external interfaces and behaviour
 *     may have changed.
 */
public static final int VERSION = @version.VERSION@;

/**
 * Release Number.
 * Release number. This changes when:
 *     - a new set of functionality is to be added, eg,
 *       implementation of a new W3C specification.
 *     - API or behaviour change.
 *     - its designated as a reference release.
 */
public static final int RELEASE = @version.RELEASE@;

/**
 * Maintenance Drop Number.
 * Optional identifier used to designate maintenance
 *     drop applied to a specific release and contains

```

```

*      fixes for defects reported. It maintains compatibility
*      with the release and contains no API changes.
*      When missing, it designates the final and complete
*      development drop for a release.
*/

public static final int MAINTENANCE
= @version.MINOR@;

/**
 * Development Drop Number.
 * Optional identifier designates development drop of
 * a specific release. D01 is the first development drop
 * of a new release.
 *
 * Development drops are works in progress towards a
 * completed, final release. A specific development drop
 * may not completely implement all aspects of a new
 * feature, which may take several development drops to
 * complete. At the point of the final drop for the
 * release, the D suffix will be omitted.
 *
 * Each 'D' drops can contain functional enhancements as
 * well as defect fixes. 'D' drops may not be as stable as
 * the final releases.
 */

public static final int DEVELOPMENT = 0;

/**
 * Version String like <CODE>"<B>Xalan</B> <B>Language</B>
 * v.r[.dd] <B>D</B>nn]"</CODE>.
 * <P>Semantics of the version string are
 identical to the Xerces project.</P>
 */

public static final String S_VERSION = PRODUCT+" "+LANGUAGE+" "
      +VERSION+"."+RELEASE+"."
      +(DEVELOPMENT > 0 ? ("D"+DEVELOPMENT)
      : (""+MAINTENANCE));

}

```

Found in path(s):

```

* /opt/cola/permits/1134796662_1613422294.09/0/xalan-2-7-2-sources-
jar/org/apache/xalan/processor/XSLProcessorVersion.src

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```

```
*/
```

```
/*
```

```
* $Id: xpath.lex 1225752 2011-12-30 04:12:46Z mrglavas $
```

```
*/
```

```
/*
```

```
* @author Jacek Ambroziak
```

```
* @author
```

```
    Santiago Pericas-Geertsen
```

```
* @author Morten Jorgensen
```

```
*
```

```
*/
```

```
package org.apache.xalan.xsltc.compiler;
```

```
import java_cup.runtime.Symbol;
```

```
%%
```

```
%cup
```

```
%unicode
```

```
%class XPathLexer
```

```
%yyeof
```

```
{
```

```
    int last;
```

```
    void initialize() {
```

```
        last = -1;
```

```
    }
```

```
    static boolean isWhitespace(int c) {
```

```
        return (c == ' ' || c == '\t' || c == '\r' || c == '\n' || c == '\f');
```

```
    }
```

```
/**
```

```
 * If symbol is not followed by ':' or '(', then treat it as a
```

```
 * name instead of an axis or function (Jira-1912).
```

```

*/
Symbol disambiguateAxisOrFunction(int ss) throws Exception {
    // Peek in the input buffer without changing the internal state
    int index = yy_buffer_index;

    // Skip whitespace
    while (index < yy_buffer_read && isWhitespace(yy_buffer[index])) {
        index++;
    }

    // If end of buffer, can't disambiguate :(

    if (index >= yy_buffer_read) {
        // Can't disambiguate, so return as symbol
        return new Symbol(ss);
    }

    // Return symbol if next token is ':' or '('
    return (yy_buffer[index] == ':' && yy_buffer[index+1] == ':' ||
            yy_buffer[index] == '(') ?
        newSymbol(ss) : newSymbol(sym.QNAME, yytext());
}

/**
 * If symbol is first token or if it follows any of the operators
 * listed in http://www.w3.org/TR/xpath#exprlex then treat as a
 * name instead of a keyword (Jira-1912).
 */
Symbol disambiguateOperator(int ss) throws Exception {
    switch (last) {
    case -1: // first token
    case sym.ATSIGN:
    case sym.DCOLON:
    case sym.LPAREN:
    case sym.LBRACK:
    case sym.COMMA:
    case sym.AND:
    case sym.OR:
    case
sym.MOD:
    case sym.DIV:
    case sym.MULT:
    case sym.SLASH:
    case sym.DSLASH:
    case sym.VBAR:
    case sym.PLUS:
    case sym.MINUS:
    case sym.EQ:

```

```

case sym.NE:
case sym.LT:
case sym.LE:
case sym.GT:
case sym.GE:
    return newSymbol(sym.QNAME, yytext());
}
return newSymbol(ss);
}

/**
 * If symbol is first token or if it follows any of the operators
 * listed in http://www.w3.org/TR/xpath#exprlex then treat as a
 * wildcard instead of a multiplication operator
 */
Symbol disambiguateStar() throws Exception {
    switch (last) {
        case -1: // first token
        case sym.ATSIGN:
        case sym.DCOLON:
        case sym.LPAREN:
        case sym.LBRACK:
        case sym.COMMA:

case sym.AND:
        case sym.OR:
        case sym.MOD:
        case sym.DIV:
        case sym.MULT:
        case sym.SLASH:
        case sym.DSLASH:
        case sym.VBAR:
        case sym.PLUS:
        case sym.MINUS:
        case sym.EQ:
        case sym.NE:
        case sym.LT:
        case sym.LE:
        case sym.GT:
        case sym.GE:
            return newSymbol(sym.STAR);
        }
        return newSymbol(sym.MULT);
    }

Symbol newSymbol(int ss) {
    last = ss;
    return new Symbol(ss);
}

```



```

}

Symbol newSymbol(int ss, String value) {
    last = ss;
    return new Symbol(ss, value);
}

Symbol newSymbol(int ss, Long value) {
    last = ss;
    return new Symbol(ss, value);
}

Symbol newSymbol(int ss, Double value) {
    last = ss;
    return new
Symbol(ss, value);
}
%}

%eofval{
return newSymbol(sym.EOF);
%eofval}

%yylexthrow{
Exception
%yylexthrow}

Letter={BaseChar}{{Ideographic}

BaseChar=[\u0041-\u005A\u0061-\u007A\u00C0-\u00D6\u00D8-\u00F6\u00F8-\u00FF\u0100-\u0131\u0134-
\u013E\u0141-\u0148\u014A-\u017E\u0180-\u01C3\u01CD-\u01F0\u01F4-\u01F5\u01FA-\u0217\u0250-
\u02A8\u02BB-\u02C1\u0386\u0388-\u038A\u038C\u038E-\u03A1\u03A3-\u03CE\u03D0-
\u03D6\u03DA\u03DC\u03DE\u03E0\u03E2-\u03F3\u0401-\u040C\u040E-\u044F\u0451-\u045C\u045E-
\u0481\u0490-\u04C4\u04C7-\u04C8\u04CB-\u04CC\u04D0-\u04EB\u04EE-\u04F5\u04F8-\u04F9\u0531-
\u0556\u0559\u0561-\u0586\u05D0-\u05EA\u05F0-\u05F2\u0621-\u063A\u0641-\u064A\u0671-\u06B7\u06BA-
\u06BE\u06C0-\u06CE\u06D0-\u06D3\u06D5\u06E5-\u06E6\u0905-\u0939\u093D\u0958-\u0961\u0985-
\u098C\u098F-\u0990\u0993-\u09A8\u09AA-\u09B0\u09B2\u09B6-\u09B9\u09DC-\u09DD\u09DF-
\u09E1\u09F0-\u09F1\u0A05-\u0A0A\u0A0F-\u0A10\u0A13-\u0A28\u0A2A-\u0A30\u0A32-\u0A33\u0A35-
\u0A36\u0A38-\u0A39\u0A59-\u0A5C\u0A5E\u0A72-\u0A74\u0A85-\u0A8B\u0A8D\u0A8F-\u0A91\u0A93-
\u0AA8\u0AAA-\u0AB0\u0AB2-\u0AB3\u0AB5-\u0AB9\u0ABD\u0AE0\u0B05-\u0B0C\u0B0F-\u0B10\u0B13-
\u0B28\u0B2A-\u0B30\u0B32-\u0B33\u0B36-\u0B39\u0B3D\u0B5C-\u0B5D\u0B5F-\u0B61\u0B85-
\u0B8A\u0B8E-\u0B90\u0B92-\u0B95\u0B99-\u0B9A\u0B9C\u0B9E-\u0B9F\u0BA3-\u0BA4\u0BA8-
\u0BAA\u0BAE-\u0BB5\u0BB7-\u0BB9\u0C05-\u0C0C\u0C0E-\u0C10\u0C12-\u0C28\u0C2A-\u0C33\u0C35-
\u0C39\u0C60-\u0C61\u0C85-\u0C8C\u0C8E-\u0C90\u0C92-\u0CA8\u0CAA-\u0CB3\u0CB5-
\u0CB9\u0CDE\u0CE0-\u0CE1\u0D05-\u0D0C\u0D0E-\u0D10\u0D12-\u0D28\u0D2A-\u0D39\u0D60-
\u0D61\u0E01-\u0E2E\u0E30\u0E32-\u0E33\u0E40-\u0E45\u0E81-\u0E82\u0E84\u0E87-
\u0E88\u0E8A\u0E8D\u0E94-\u0E97\u0E99-\u0E9F\u0EA1-\u0EA3\u0EA5\u0EA7\u0EAA-\u0EAB\u0EAD-
\u0EAE\u0EB0\u0EB2-\u0EB3\u0EBD\u0EC0-\u0EC4\u0F40-\u0F47\u0F49-\u0F69\u10A0-\u10C5\u10D0-

```

\u10F6\u1100\u1102-\u1103\u1105-\u1107\u1109\u110B-\u110C\u110E-  
\u1112\u113C\u113E\u1140\u114C\u114E\u1150\u1154-\u1155\u1159\u115F-  
\u1161\u1163\u1165\u1167\u1169\u116D-\u116E\u1172-\u1173\u1175\u119E\u11A8\u11AB\u11AE-  
\u11AF\u11B7-\u11B8\u11BA\u11BC-\u11C2\u11EB\u11F0\u11F9\u1E00-\u1E9B\u1EA0-\u1EF9\u1F00-  
\u1F15\u1F18-\u1F1D\u1F20-\u1F45\u1F48-\u1F4D\u1F50-\u1F57\u1F59\u1F5B\u1F5D\u1F5F-\u1F7D\u1F80-  
\u1FB4\u1FB6-\u1FBC\u1FBE\u1FC2-\u1FC4\u1FC6-\u1FCC\u1FD0-\u1FD3\u1FD6-\u1FDB\u1FE0-  
\u1FEC\u1FF2-\u1FF4\u1FF6-\u1FFC\u2126\u212A-\u212B\u212E\u2180-\u2182\u3041-\u3094\u30A1-  
\u30FA\u3105-\u312C\uAC00-\uD7A3]

Ideographic=[\u4E00-\u9FA5\u3007\u3021-\u3029]

CombiningChar=[\u0300-\u0345\u0360-\u0361\u0483-\u0486\u0591-\u05A1\u05A3-\u05B9\u05BB-  
\u05BD\u05BF\u05C1-\u05C2\u05C4\u064B-\u0652\u0670\u06D6-\u06DC\u06DD-\u06DF\u06E0-\u06E4\u06E7-  
\u06E8\u06EA-\u06ED\u0901-\u0903\u093C\u093E-\u094C\u094D\u0951-\u0954\u0962-\u0963\u0981-  
\u0983\u09BC\u09BE\u09BF\u09C0-\u09C4\u09C7-\u09C8\u09CB-\u09CD\u09D7\u09E2-  
\u09E3\u0A02\u0A3C\u0A3E\u0A3F\u0A40-\u0A42\u0A47-\u0A48\u0A4B-\u0A4D\u0A70-\u0A71\u0A81-  
\u0A83\u0ABC\u0ABE-\u0AC5\u0AC7-\u0AC9\u0ACB-\u0ACD\u0B01-\u0B03\u0B3C\u0B3E-\u0B43\u0B47-  
\u0B48\u0B4B-\u0B4D\u0B56-\u0B57\u0B82-\u0B83\u0BBE-\u0BC2\u0BC6-\u0BC8\u0BCA-  
\u0BCD\u0BD7\u0C01-\u0C03\u0C3E-\u0C44\u0C46-\u0C48\u0C4A-\u0C4D\u0C55-\u0C56\u0C82-  
\u0C83\u0CBE-\u0CC4\u0CC6-\u0CC8\u0CCA-\u0CCD\u0CD5-\u0CD6\u0D02-\u0D03\u0D3E-\u0D43\u0D46-  
\u0D48\u0D4A-\u0D4D\u0D57\u0E31\u0E34-\u0E3A\u0E47-\u0E4E\u0EB1\u0EB4-\u0EB9\u0EBB-  
\u0EBC\u0EC8-\u0ECD\u0F18-\u0F19\u0F35\u0F37\u0F39\u0F3E\u0F3F\u0F71-\u0F84\u0F86-\u0F8B\u0F90-  
\u0F95\u0F97\u0F99-\u0FAD\u0FB1-\u0FB7\u0FB9\u20D0-\u20DC\u20E1\u302A-\u302F\u3099\u309A]

Digit=[\u0030-\u0039\u0660-\u0669\u06F0-\u06F9\u0966-\u096F\u09E6-\u09EF\u0A66-\u0A6F\u0AE6-  
\u0AEF\u0B66-\u0B6F\u0BE7-\u0BEF\u0C66-\u0C6F\u0CE6-\u0CEF\u0D66-\u0D6F\u0E50-\u0E59\u0ED0-  
\u0ED9\u0F20-\u0F29]

Extender=[\u00B7\u02D0\u02D1\u0387\u0640\u0E46\u0EC6\u3005\u3031-\u3035\u309D-\u309E\u30FC-\u30FE]

NCName=({Letter}|"\_"|{NCNameStartChar})({NCNameChar})\*

NCNameChar={Letter}|{Digit}|"."-"\_"|{CombiningChar}|{Extender}|{NCNameStartChar}|  
\u00B7 | [\u0300-\u036F] | [\u203F-\u2040] | [\u0130-\u0136]

NCNameStartChar=[\u0041-\u005A\u0061-\u007A\u00C0-\u00D6\u00D8-\u00F6\u00F8-\u02FF\u0370-  
\u037D\u037F-\u1FFF\u200C-\u200D\u2070-\u218F\u2C00-\u2FEF\u3001-\uD7FF\uF900-\uFDCF\uFDF0-  
\uFFFFD] | \u005F |({HighSurrogate}{LowSurrogate})

HighSurrogate=[\uD800-\uDBFF]

LowSurrogate=[\uDC00-\uDFFF]

%%

"" { return disambiguateStar(); }

```

"/"      { return newSymbol(sym.SLASH); }
"+"      { return newSymbol(sym.PLUS); }
"-"      { return newSymbol(sym.MINUS); }
"div"    { return disambiguateOperator(sym.DIV); }
"mod"    { return disambiguateOperator(sym.MOD); }
"::"     { return newSymbol(sym.DCOLON); }
","      { return newSymbol(sym.COMMA); }
"@@"     { return newSymbol(sym.ATSIGN); }
".."     { return newSymbol(sym.DDOT); }
"|"      { return newSymbol(sym.VBAR); }
"$"      { return newSymbol(sym.DOLLAR); }
"//"      { return newSymbol(sym.DSLASH); }
"="      { return newSymbol(sym.EQ); }
"!="     { return newSymbol(sym.NE); }
"<"     { return newSymbol(sym.LT); }
">"     { return newSymbol(sym.GT); }
"<="    { return newSymbol(sym.LE); }
">="    { return newSymbol(sym.GE); }
"id"     { return disambiguateAxisOrFunction(sym.ID); }
"key"    { return disambiguateAxisOrFunction(sym.KEY); }
"text()" { return newSymbol(sym.TEXT); }
"text"+[ \t\r\n\f]+"()" { return newSymbol(sym.TEXT); }
"node()" { return newSymbol(sym.NODE); }
"node"+[ \t\r\n\f]+"()" { return newSymbol(sym.NODE); }
"comment()" { return newSymbol(sym.COMMENT); }
"comment"+[ \t\r\n\f]+"()" { return newSymbol(sym.COMMENT); }
"processing-instruction"
{ return disambiguateAxisOrFunction(sym.PIPARAM); }
"processing-instruction()" { return newSymbol(sym.PI); }
"processing-instruction"+[ \t\r\n\f]+"()" { return newSymbol(sym.PI); }
"or"     { return disambiguateOperator(sym.OR); }
"and"    { return disambiguateOperator(sym.AND); }
"child"  { return disambiguateAxisOrFunction(sym.CHILD); }
"attribute" { return disambiguateAxisOrFunction(sym.ATTRIBUTE); }
"ancestor" { return disambiguateAxisOrFunction(sym.ANCESTOR); }
"ancestor-or-self" { return disambiguateAxisOrFunction(sym.ANCESTORORSELF); }
"descendant" { return disambiguateAxisOrFunction(sym.DESCENDANT); }
"descendant-or-self" { return disambiguateAxisOrFunction(sym.DESCENDANTORSELF); }
"following" { return disambiguateAxisOrFunction(sym.FOLLOWING); }
"following-sibling" { return disambiguateAxisOrFunction(sym.FOLLOWINGSIBLING); }
}
"namespace" { return disambiguateAxisOrFunction(sym.NAMESPACE); }
"parent"    { return disambiguateAxisOrFunction(sym.PARENT); }
"preceding" { return disambiguateAxisOrFunction(sym.PRECEDING); }
"preceding-sibling" { return disambiguateAxisOrFunction(sym.PRECEDINGSIBLING); }
"self"     { return disambiguateAxisOrFunction(sym.SELF); }
"["       { return newSymbol(sym.LBRACK); }

```

```

"]"          { return newSymbol(sym.RBRACK); }
"("          { return newSymbol(sym.LPAREN); }
")"          { return newSymbol(sym.RPAREN); }
"<PATTERN>"  { initialize(); return new Symbol(sym.PATTERN); }
"<EXPRESSION>" { initialize(); return new Symbol(sym.EXPRESSION); }
\[^\]*\      { return newSymbol(sym.Literal,
    yytext().substring(1, yytext().length() - 1)); }
\[^\]*\      { return newSymbol(sym.Literal,
    yytext().substring(1,
    yytext().length() - 1)); }
{Digit}+     { return newSymbol(sym.INT, new Long(yytext())); }
{Digit}+(\."{Digit}*)? { return newSymbol(sym.REAL, new Double(yytext())); }
"."{Digit}+  { return newSymbol(sym.REAL, new Double(yytext())); }
"."          { return newSymbol(sym.DOT); }
({NCName}":")?{NCName} { return newSymbol(sym.QNAME, yytext()); }
({NCName}":")?""*      { return newSymbol(sym.QNAME, yytext()); }
({NCName}":")?""@*"    { return newSymbol(sym.QNAME, yytext()); }
[\t\r\nf]    { /* ignore white space. */ }
.            { throw new Exception(yytext()); }

```

Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xalan/xsltc/compiler/xpath.lex

No license file was found, but licenses were detected in source scan.

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\*/

// Proprietary, built in functions

/\*\* current function string (Proprietary). \*/

Found in path(s):

\* /opt/cola/permits/1134796662\_1613422294.09/0/xalan-2-7-2-sources-jar/org/apache/xpath/compiler/Keywords.java

# 1.183 chardet 4.0.0

## 1.183.1 Available under license :

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Version 2.1, February 1999

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Any executables

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# 1.184 jakarta-annotation-api 1.3.4

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```
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/annotation/FloatMemberValue.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/annotation/StringMemberValue.java
*
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* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
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* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
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* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/annotation/ByteMemberValue.java
*
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/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/annotation/MemberValue.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/annotation/CharMemberValue.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/annotation/LongMemberValue.java  
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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/NoFieldException.java  
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 \* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/annotation/AnnotationsWriter.java  
 \* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/rmi/Sample.java  
 \* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/LocalVariableTypeAttribute.java  
 \* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-

jar/javassist/util/proxy/DefineClassHelper.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/runtime/DotClass.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/runtime/Desc.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/StringL.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/scopedpool/ScopedClassPoolRepositoryImpl.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/CtNewNestedClass.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/Member.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/proxy/Proxy.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/scopedpool/ScopedClassPool.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/analysis/Executor.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/rmi/RemoteRef.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/analysis/MultiArrayType.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/ClassFile.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/AttributeInfo.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/util/proxy/RuntimeSupport.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/FieldInfo.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/expr/FieldAccess.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/BinExpr.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/SymbolTable.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/Handler.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/BadBytecode.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/Lex.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/web/Webserver.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/rmi/Proxy.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/convert/TransformNew.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-

jar/javassist/bytecode/InstructionPrinter.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/analysis/SubroutineScanner.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/ast/Expr.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/CtNewWrappedConstructor.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/ByteArrayClassPath.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtBehavior.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/ExceptionTable.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/reflect/ClassMetaobject.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ProceedHandler.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/NewArray.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/scopedpool/ScopedClassPoolFactoryImpl.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/SourceFileAttribute.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/stackmap/MapMaker.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/util/proxy/ProxyObjectOutputStream.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/convert/TransformAccessArrayField.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/web/Viewer.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/reflect/CannotCreateException.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/KeywordTable.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/Parser.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/Loader.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/Expr.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/ClassPoolTail.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/expr/ConstructorCall.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/tools/reflect/Metalevel.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/bytecode/analysis/IntQueue.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-  
jar/javassist/compiler/ast/Symbol.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-

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jar/javassist/tools/rmi/ObjectImporter.java
*
/opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/analysis/Frame.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/TokenId.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/tools/rmi/AppletServer.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/CodeAttribute.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/Bytecode.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/InstanceOfExpr.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/NewExpr.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/util/proxy/MethodFilter.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/expr/ExprEditor.java
*
/opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/AccessFlag.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/ClassPath.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/analysis/Analyzer.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtClassType.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/tools/reflect/CannotInvokeException.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/AnnotationsAttribute.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/HotSwapper.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/tools/reflect/Compiler.java
*
/opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/LongVector.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/FieldDecl.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/tools/web/BadHttpRequest.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/CtNewConstructor.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/CastExpr.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/Javac.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/SerialVersionUID.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/MethodInfo.java

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* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/tools/reflect/Metaobject.java
*
/opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/convert/TransformAfter.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/Descriptor.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/CallExpr.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/IntConst.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/AnnotationDefaultAttribute.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/convert/TransformBefore.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/JvstTypeChecker.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/LoaderClassPath.java
*
/opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/ParameterAnnotationsAttribute.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/Variable.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/CtNewMethod.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/ASTree.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/Mnemonic.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/ClassPool.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/ExceptionsAttribute.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/URLClassPath.java
*
/opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/scopedpool/ScopedClassPoolFactory.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/ArrayInit.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/tools/reflect/Loader.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/convert/TransformFieldAccess.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/bytecode/DuplicateMemberException.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/CondExpr.java
* /opt/cola/permits/1135864419_1613618067.93/0/javassist-3-22-0-cr2-sources-4-
jar/javassist/compiler/ast/ASTList.java

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\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/ast/Keyword.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/convert/TransformWriteField.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/ConstPool.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/analysis/Type.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/StackMapTable.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/HotSwapAgent.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/MemberCodeGen.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/ClassMap.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/CompileError.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/tools/rmi/RemoteException.java  
\*  
/opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/bytecode/ConstantAttribute.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/compiler/AccessorMaker.java  
\* /opt/cola/permits/1135864419\_1613618067.93/0/javassist-3-22-0-cr2-sources-4-jar/javassist/util/proxy/SerializedProxy.java

# 1.186 gdbm 1.18.1

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/opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/net/StringEncodings.java

\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/net/QCodec.java

\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-jar/org/apache/commons/codec/net/RFC1522Codec.java

\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-

jar/org/apache/commons/codec/StringEncoder.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/binary/Base64.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/net/BCodec.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/binary/BinaryCodec.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/language/RefinedSoundex.java  
\*  
/opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/DecoderException.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/StringEncoderComparator.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/language/Soundex.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/BinaryEncoder.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
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\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/net/QuotedPrintableCodec.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/language/Metaphone.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/Decoder.java  
\*  
/opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/language/SoundexUtils.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/StringDecoder.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/BinaryDecoder.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/EncoderException.java  
\* /opt/cola/permits/1135953634\_1613641193.46/0/commons-codec-1-3-sources-1-  
jar/org/apache/commons/codec/language/DoubleMetaphone.java

# 1.189 clang 6.0.0

## 1.189.1 Available under license :

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Metadata-Version: 1.0

Name: clang

Version: 6.0.0

Summary: UNKNOWN

Home-page: <https://github.com/ethanhs/clang>

Author: Ethan Smith

Author-email: [ethan@ethanhs.me](mailto:ethan@ethanhs.me)

License: UNKNOWN

Description: UNKNOWN

Platform: UNKNOWN

Found in path(s):

\* /opt/cola/permits/1135983023\_1613644830.93/0/clang-6-0-0-tar-gz/clang-6.0.0/clang.egg-info/PKG-INFO

\* /opt/cola/permits/1135983023\_1613644830.93/0/clang-6-0-0-tar-gz/clang-6.0.0/PKG-INFO

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\* /opt/cola/permits/1135983023\_1613644830.93/0/clang-6-0-0-tar-gz/clang-6.0.0/clang/\_\_init\_\_.py

\* /opt/cola/permits/1135983023\_1613644830.93/0/clang-6-0-0-tar-gz/clang-6.0.0/examples/cindex/cindex-dump.py

\* /opt/cola/permits/1135983023\_1613644830.93/0/clang-6-0-0-tar-gz/clang-6.0.0/examples/cindex/cindex-  
includes.py

\* /opt/cola/permits/1135983023\_1613644830.93/0/clang-6-0-0-tar-gz/clang-6.0.0/clang/enumerations.py

\* /opt/cola/permits/1135983023\_1613644830.93/0/clang-6-0-0-tar-gz/clang-6.0.0/clang/cindex.py

# 1.190 jakarta-ws-rs-api 2.1.5

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```
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Eclipse

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\* org.eclipse.jetty.orbit:org.eclipse.jdt.core

The following artifacts are EPL and ASL2.

\* org.eclipse.jetty.orbit:javac.security.auth.message

The following artifacts are EPL and CDDL 1.0.

\* org.eclipse.jetty.orbit:javac.mail.glassfish

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Oracle

The following artifacts are CDDL + GPLv2 with classpath exception.

<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

\* javax.servlet:javax.servlet-api

\* javax.annotation:javax.annotation-api

\* javax.transaction:javax.transaction-api

\* javax.websocket:javax.websocket-api

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Oracle OpenJDK

If ALPN is used to negotiate HTTP/2 connections, then the following artifacts may be included in the distribution or downloaded when ALPN module is selected.

\* java.sun.security.ssl

These artifacts replace/modify OpenJDK classes. The modifications are hosted at github and both modified and original are under GPL v2 with classpath exceptions.

<http://openjdk.java.net/legal/gplv2+ce.html>

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OW2

The following artifacts are licensed by the OW2 Foundation according to the



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org.ow2.asm:asm-commons

org.ow2.asm:asm

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Apache

The following artifacts are ASL2 licensed.

org.apache.taglibs:taglibs-standard-spec

org.apache.taglibs:taglibs-standard-impl

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MortBay

The following artifacts are ASL2 licensed. Based on selected classes from following Apache Tomcat jars, all ASL2 licensed.

org.mortbay.jasper:apache-jsp

org.apache.tomcat:tomcat-jasper

org.apache.tomcat:tomcat-juli

org.apache.tomcat:tomcat-jsp-api

org.apache.tomcat:tomcat-el-api

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-api

org.apache.tomcat:tomcat-util-scan

org.apache.tomcat:tomcat-util

org.mortbay.jasper:apache-el

org.apache.tomcat:tomcat-jasper-el

org.apache.tomcat:tomcat-el-api

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Mortbay

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<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

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Assorted

The UnixCrypt.java code implements the one way cryptography used by Unix systems for simple password protection. Copyright 1996 Aki Yoshida, modified April 2001 by Iris Van den Broeke, Daniel Deville. Permission to use, copy, modify and distribute UnixCrypt for non-commercial or commercial purposes and without fee is granted provided that the copyright notice appears in all copies.

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## Source Code

The project maintains the following source code repositories:

\* <https://github.com/eclipse-ee4j/jersey>

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\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/tree/LdcInsnNode.java

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\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/signature/SignatureReader.java

\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/tree/FieldNode.java

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/opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/commons/SerialVersionUIDAdder.java

\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/tree/ClassNode.java

\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/commons/Remapper.java

\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/tree/AbstractInsnNode.java

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\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/Type.java

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jar/org/objectweb/asm/tree/TableSwitchInsnNode.java  
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\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/MethodWriter.java  
\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/Opcodes.java  
\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/tree/FieldInsnNode.java  
\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/tree/analysis/Analyzer.java  
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\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/xml/ASMContentHandler.java  
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/opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/xml/SAXAnnotationAdapter.java  
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\* /opt/cola/permits/1136493993\_1613774733.35/0/asm-all-3-2-sources-2-jar/org/objectweb/asm/xml/SAXFieldAdapter.java  
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jar/org/objectweb/asm/tree/analysis/package.html  
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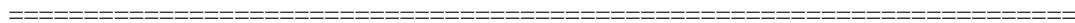
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Contributors

=====

Original Author:

- [Dave Gamble](https://github.com/DaveGamble)

Current Maintainer:

- [Max Bruckner](https://github.com/FSMaxB)
- [Alan Wang](https://github.com/Alanscut)

Contributors:

- \* [Ajay Bhargav](https://github.com/ajaybhargav)
- \* [Alper Akcan](https://github.com/alperakcan)
- \* [Andrew Tang](https://github.com/singku)
- \* [Anton Sergeev](https://github.com/anton-sergeev)
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- \* [Chen Yuan](https://github.com/zjuchenyuan)
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- \* Irwan Djadjadi
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# 1.205 gdbm 1.13

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# 1.206 tdb 1.4.3

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Version 3, 29 June 2007

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## 1.207 ini 1.62.0

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Email local part: Philip.Hazel  
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Upstream-Contact: Yann Collet <[Cyan4973@github.com](mailto:Cyan4973@github.com)>

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## 1.220 libiconv 1.14\_11

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a

derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

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in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

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This option is useful when you wish to copy part of the code of

the

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Version 3, 29 June 2007

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## 1.221 tomcat-el-api 8.5.32

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```
* /opt/cola/permits/1151085999_1617779160.19/0/jackson-mapper-asl-1-0-1-sources-1-  
jar/org/codehaus/jackson/map/MappingJsonFactory.java
```

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# 1.229 tcp-wrappers 7.6.24

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# 1.231 tomcat-coyote 8.5.32

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/bcel/Const.java

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/filters/LocalStrings.properties

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/ciphers/LocalStrings.properties

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\*

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/LocalStrings.properties

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/ajp/LocalStrings.properties

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/LocalStrings.properties

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-

jar/org/apache/tomcat/util/net/jsse/LocalStrings.properties

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jar/org/apache/tomcat/util/bcel/classfile/ConstantLong.java

\*

/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-

jar/org/apache/tomcat/util/bcel/classfile/EnumElementValue.java

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jar/org/apache/tomcat/util/bcel/classfile/JavaClass.java

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jar/org/apache/tomcat/util/bcel/classfile/ClassFormatException.java

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jar/org/apache/tomcat/util/bcel/classfile/Annotations.java

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jar/org/apache/tomcat/util/bcel/classfile/AnnotationElementValue.java

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jar/org/apache/tomcat/util/bcel/classfile/ConstantInteger.java

\*

/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-

jar/org/apache/tomcat/util/bcel/classfile/ElementValuePair.java

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jar/org/apache/tomcat/util/bcel/classfile/Constant.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-

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jar/org/apache/tomcat/util/bcel/classfile/ConstantDouble.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/AnnotationEntry.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/SimpleElementValue.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/ConstantClass.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/ClassParser.java
*
/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/ConstantFloat.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/ArrayElementValue.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/ConstantPool.java
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*/
/**
* Return the initialization parameters for the TrustManager.
* Currently, only the default <code>PKIX</code>
is supported.
*
* @param crlf The path to the CRL file.
* @param trustStore The configured TrustStore.
* @param revocationEnabled Should the JSSE provider perform revocation
* checks? Ignored if { @code crlf } is non-null.
* Configuration of revocation checks are expected
* to be via proprietary JSSE provider methods.
* @return The parameters including the CRLs and TrustStore.
* @throws Exception An error occurred
*/
```

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/jsse/JSSEUtil.java

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\*



/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/Http11Protocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/AsyncStateMachine.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SendfileKeepAliveState.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/AprEndpoint.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SSLUtil.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/ajp/Constants.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/HPackHuffman.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/jsse/JSSEKeyManager.java  
\*  
/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/ajp/AjpNio2Protocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SendfileDataBase.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/Http11NioProtocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/Http2Exception.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/Request.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/RequestGroupInfo.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/HttpMessages.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLConfCmd.java  
\*  
/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/ciphers/Cipher.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/AbstractHttp11JsseProtocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/OutputFilter.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/filters/VoidInputFilter.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/HeaderSink.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/log/SystemLogHandler.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/AprSSLSupport.java

```

* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/upgrade/UpgradeServletInputStream.java
*
/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/SSLSessionManager.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/SSLHostConfigCertificate.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/ajp/AbstractAjpProtocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/openssl/ciphers/OpenSSLCipherConfigurationParser.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/filters/BufferedInputFilter.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/filters/VoidOutputFilter.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/ElementValue.java
*
/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/openssl/ciphers/EncryptionLevel.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/AbstractStream.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/AbstractHttp11Protocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/http/CookieProcessor.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/openssl/OpenSSLConf.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/SocketBufferHandler.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/jsse/JSSSESSLContext.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/http/ServerCookies.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/HpackEncoder.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/SSLImplementation.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/openssl/ciphers/Protocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/upgrade/UpgradeProcessorInternal.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-

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jar/org/apache/coyote/http11/HttpOutputBuffer.java  
\*  
/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/http11/upgrade/UpgradeProcessorExternal.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/UpgradeProtocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/http11/upgrade/InternalHttpUpgradeHandler.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/http11/Http11Nio2Protocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/ajp/AjpProtocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/ServerCookie.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/net/jsse/JSSEImplementation.java  
\*  
/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/http2/ConnectionSettingsLocal.java  
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jar/org/apache/tomcat/util/net/openssl/ciphers/MessageDigest.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/net/DispatchType.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/http2/Http2Error.java  
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jar/org/apache/coyote/AbstractProcessorLight.java  
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jar/org/apache/coyote/InputStream.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/ContainerThreadMarker.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/AsyncContextCallback.java  
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/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
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jar/org/apache/coyote/http11/filters/SavedRequestInputFilter.java  
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jar/org/apache/coyote/http11/Constants.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/ajp/AjpAprProtocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/net/TLSClientHelloExtractor.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/coyote/http11/upgrade/UpgradeServletOutputStream.java

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/StreamStateMachine.java
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/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/ConnectionException.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/HpackException.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/ByteUtil.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/AbstractProtocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/AbstractProcessor.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/ajp/AjpNioProtocol.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/Constants.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http2/Stream.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/net/jsse/JSSESupport.java
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jar/org/apache/coyote/CompressionConfig.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/ActionCode.java
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/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/ErrorState.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/Constants.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/bcel/classfile/ConstantUtf8.java
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/coyote/http11/InputFilter.java
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-

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jar/org/apache/coyote/http2/Http2Parser.java  
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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SendfileState.java  
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/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/filters/GzipOutputFilter.java  
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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/StreamException.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/Nio2Endpoint.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SSLHostConfig.java  
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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/log/CaptureLog.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/filters/IdentityOutputFilter.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/Http11AprProtocol.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SSLSupport.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/HeadersTooLargeException.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/Adapter.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/parser/MediaTypeCache.java  
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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/ciphers/KeyExchange.java  
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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/Rfc6265CookieProcessor.java

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/LegacyCookieProcessor.java

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/FastDateFormat.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/Http2OutputBuffer.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http11/Http11InputBuffer.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SocketProcessorBase.java

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/ActionHook.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/ConnectionSettingsRemote.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/AbstractJsseEndpoint.java

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/UpgradeToken.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/Hpack.java

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/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/ajp/AjpMessage.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/CookieProcessorBase.java

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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/coyote/http2/ConnectionSettingsBase.java

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/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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jar/org/apache/coyote/OutputBuffer.java
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Found in path(s):

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-
jar/org/apache/tomcat/util/http/fileupload/ProgressListener.java
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\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/NioBlockingSelector.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/mime/QuotedPrintableDecoder.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/ParameterParser.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/AbstractEndpoint.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/SocketProperties.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/parser/HttpParser.java  
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/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/modeler/modules/MbeansDescriptorsIntrospectionSource.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLX509Certificate.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/package-info.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/Closeable.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/parser/Cookie.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLSessionContext.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLKeyManager.java  
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/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/parser/Authorization.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileUploadBase.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/UploadContext.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/LimitedInputStream.java



\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileItemHeaders.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/modeler/BaseAttributeFilter.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLContext.java

\*

/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/disk/DiskFileItemFactory.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/log/UserDataHelper.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/mime/MimeUtility.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileItemIterator.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/util/Streams.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/RequestUtil.java

\*

/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/FileItemFactory.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/ApplicationBufferHandler.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/modeler/BaseModelMBean.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/NioChannel.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/Nio2Channel.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/package-info.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/modeler/BaseNotificationBroadcaster.java

\*

/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/servlet/package-info.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/IOUtils.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/net/openssl/OpenSSLProtocols.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/http/fileupload/RequestContext.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-jar/org/apache/tomcat/util/modeler/NotificationInfo.java

\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-

jar/org/apache/tomcat/util/http/fileupload/FileItem.java  
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jar/org/apache/tomcat/util/modeler/RegistryMBean.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/util/mime/ParseException.java  
\*  
/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/servlet/ServletFileUpload.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/modeler/OperationInfo.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/MultipartStream.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/util/FileItemHeadersImpl.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/net/openssl/OpenSSLImplementation.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/modeler/Registry.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/net/SecureNioChannel.java  
\*  
/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/parser/Host.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/ByteArrayOutputStream.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/ThresholdingOutputStream.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/modeler/FeatureInfo.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/modeler/modules/MbeansDescriptorsDigesterSource.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/DeferredFileOutputStream.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/disk/package-info.java  
\*  
/opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/FileUploadException.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/FileItemStream.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/parser/SkipResult.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/modeler/ManagedBean.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/InvalidFileNameException.java  
\* /opt/cola/permits/1151086045\_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/util/mime/package-info.java

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* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/FileUtils.java  
*  
/opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/http/fileupload/FileUpload.java  
* /opt/cola/permits/1151086045_1617779202.43/0/tomcat-coyote-8-5-32-sources-1-  
jar/org/apache/tomcat/util/net/openssl/OpenSSLSessionStats.java
```

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## 1.236 libpkit 0.23.10

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## 1.239 tomcat-jsp-api 8.5.32

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\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/JspEngineInfo.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/SimpleTag.java

\*

/opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagData.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/el/ELParseException.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-

jar/javax/servlet/jsp/JspException.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagLibraryValidator.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/JspContext.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/DynamicAttributes.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TryCatchFinally.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/IterationTag.java  
\*  
/opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/FunctionInfo.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/Tag.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagFileInfo.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/HttpJspPage.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagExtraInfo.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagInfo.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/SimpleTagSupport.java  
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jar/javax/servlet/jsp/tagext/JspFragment.java  
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/opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/el/FunctionMapper.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/el/ImplicitObjectELResolver.java  
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jar/javax/servlet/jsp/JspPage.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagSupport.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/JspTag.java  
\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/PageData.java  
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jar/javax/servlet/jsp/el/Expression.java  
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jar/javax/servlet/jsp/PageContext.java

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jar/javax/servlet/jsp/el/ELException.java

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\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/BodyTag.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/ErrorData.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/SkipPageException.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/JspApplicationContext.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/JspTagException.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagLibraryInfo.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/el/ExpressionEvaluator.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/TagAttributeInfo.java

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/opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/BodyTagSupport.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/el/ScopedAttributeELResolver.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/tagext/ValidationMessage.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-  
jar/javax/servlet/jsp/el/VariableResolver.java

\* /opt/cola/permits/1151085963\_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-

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* /opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
jar/javax/servlet/jsp/tagext/TagVariableInfo.java
*
/opt/cola/permits/1151085963_1617779195.62/0/tomcat-jsp-api-8-5-32-sources-1-
jar/javax/servlet/jsp/tagext/BodyContent.java
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jar/javax/security/auth/message/AuthStatus.java
*
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jar/javax/security/auth/message/config/AuthConfigProvider.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/config/AuthConfig.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/module/ServerAuthModule.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/config/ServerAuthConfig.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/ServerAuth.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/config/RegistrationListener.java
```

```

* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/config/ClientAuthContext.java
*
/opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/config/ServerAuthContext.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/CallerPrincipalCallback.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/MessagePolicy.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/config/AuthConfigFactory.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/AuthException.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/GroupPrincipalCallback.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/config/ClientAuthConfig.java
*
/opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/SecretKeyCallback.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/PasswordValidationCallback.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/ClientAuth.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/module/ClientAuthModule.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/MessageInfo.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/TrustStoreCallback.java
* /opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/CertStoreCallback.java
*
/opt/cola/permits/1151085791_1617779191.06/0/tomcat-jaspic-api-8-5-32-sources-1-
jar/javax/security/auth/message/callback/PrivateKeyCallback.java

```

## 1.241 tomcat-i18n-ja 8.5.32

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## 1.242 tomcat-websocket-api 8.5.32

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Upstream Author(s):  
Steven Knight  
knight at baldmt dot com  
<http://www.baldmt.com/~knight/>

With plenty of help from the SCons Development team:  
Chad Austin  
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## 1.252 binutils 2.33.1

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```
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
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```

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If you've contributed to gas and your name isn't listed here, it is not meant as a slight. I just don't know about it. Email me, nickc@redhat.com and I'll correct the situation.

This file will eventually be deleted: The general info will go into the documentation, and info on specific files will go into an AUTHORS file, as requested by the FSF.

+++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the preprocessing pass, and extensive changes in messages.c, input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration including heavy testing and verification of cross assemblers and file splits and renaming, converted gas to strictly ansi C including full prototypes, added support for m680[34]0 & cpu32, considerable work on i960 including a coff port (including considerable amounts of reverse engineering), a sparc opcode file rewrite, decstation, rs6000, and hp300hpux host ports, updated "know" assertions and made them work, much other reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan. Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith

Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error



checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software



sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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0. This License Agreement applies to any software library which contains a notice placed by the copyright holder or other authorized party saying it may be distributed under the terms of this Library General Public License (also called "this License"). Each licensee is addressed as "you".

A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated

interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the

application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany

it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse

engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot

use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

b) Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

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Each

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#### END OF TERMS AND CONDITIONS

#### Appendix: How to Apply These Terms to Your New Libraries

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>  
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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary.

Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990  
Ty Coon, President of Vice

That's all there is to it!

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Version 3, 29 June 2007

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To protect your rights, we need to prevent others from denying you these rights or asking you to surrender the rights. Therefore, you have certain responsibilities if you distribute copies of the software, or if you modify it: responsibilities to respect the freedom of others.

For example, if you distribute copies of such a program, whether gratis or for a fee, you must pass on to the recipients the same freedoms that you received. You must make sure that they, too, receive or can get the source code. And you must show them these terms so they know their rights.

Developers that use the GNU GPL protect your rights with two steps:

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For the developers' and authors' protection, the GPL clearly explains that there is no warranty for this free software. For both users' and authors' sake, the GPL requires that modified versions be marked as changed, so that their problems will not be attributed erroneously to authors of previous versions.

Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we

have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand ready to extend this provision to those domains in future versions of the GPL, as needed to protect the freedom of users.

Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

## TERMS AND CONDITIONS

### 0. Definitions.

"This License" refers to version 3 of the GNU General Public License.

"Copyright" also means copyright-like laws that apply to other kinds of works, such as semiconductor masks.

"The Program" refers to any copyrightable work licensed under this License. Each licensee is addressed as "you". "Licensees" and "recipients" may be individuals or organizations.

To "modify" a work means to copy from or adapt all or part of the work in a fashion requiring copyright permission, other than the making of an exact copy. The resulting work is called a "modified version" of the earlier work or a work "based on" the earlier work.

A "covered work" means either the unmodified Program or a work based on the Program.

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To "convey" a work means any kind of propagation that enables other parties to make or receive copies. Mere interaction with a user through a computer network, with no transfer of a copy, is

not conveying.

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#### 1. Source Code.

The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The

"System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

The "Corresponding Source" for a work in object code form means all the source code needed to generate, install, and (for an executable work) run the object code and to modify the work, including scripts to control those activities. However, it does not include the work's System Libraries, or general-purpose tools or generally available free programs which are used unmodified in performing those activities but which are not part of the work. For example, Corresponding Source includes interface definition files associated with source files for the work, and the source code for shared libraries and dynamically linked subprograms that the work is specifically designed to require, such as by intimate data communication or control flow between those subprograms and other parts of the work.

The Corresponding Source need not include anything that users can regenerate automatically from other parts of the Corresponding Source.

The Corresponding Source for a work in source code form is that same work.

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You may charge any price or no price for each copy that you convey, and you may offer support or warranty protection for a fee.

#### 5. Conveying Modified Source Versions.

You may convey a work based on the Program, or the modifications to produce it from the Program, in the form of source code under the terms of section 4, provided that you also meet all of these conditions:

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- b) The work must carry prominent notices stating that it is released under this License and any conditions added under section 7. This requirement modifies the requirement in section 4 to "keep intact all notices".
- c) You must license the entire work, as a whole, under this License to anyone who comes into possession of a copy. This License will therefore apply, along with any applicable section 7 additional terms, to the whole of the work, and all its parts, regardless of how they are packaged. This License gives no permission to license the work in any other way, but it does not invalidate such permission if you have separately received it.
- d) If the work has interactive user interfaces, each must display Appropriate Legal Notices; however, if the Program has interactive interfaces that do not display Appropriate Legal Notices, your work need not make them do so.

A compilation of a covered work with other separate and independent works, which are not by their nature extensions of the covered work, and which are not combined with it such as to form a larger program, in or on a volume of a storage or distribution

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#### 6. Conveying Non-Source Forms.

You may convey a covered work in object code form under the terms of sections 4 and 5, provided that you also convey the machine-readable Corresponding Source under the terms of this License, in one of these ways:

- a) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by the Corresponding Source fixed on a durable physical medium customarily used for software interchange.
- b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.
- c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.
- d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the

Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

e) Convey the object code using peer-to-peer transmission, provided you inform other peers where the object code and Corresponding Source of the work are being offered to the general public at no charge under subsection 6d.

A separable portion of the object code, whose source code is excluded from the Corresponding Source as a System Library, need not be included in conveying the object code work.

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## 1.254 libgcrypt 1.8.7

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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.  
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a)

Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will

operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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to most

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## 1.255 cffi 1.14.4

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## 1.256 python-pip 20.1.1

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team.

In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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|---------|--------------|------|-------|---------------------|
| 2.5     | 2.4          | 2006 | PSF   | yes                 |
| 2.5.1   | 2.5          | 2007 | PSF   | yes                 |
| 2.5.2   | 2.5.1        | 2008 | PSF   | yes                 |
| 2.5.3   | 2.5.2        | 2008 | PSF   | yes                 |
| 2.6     | 2.5          | 2008 | PSF   | yes                 |
| 2.6.1   | 2.6          | 2008 | PSF   | yes                 |
| 2.6.2   | 2.6.1        | 2009 | PSF   | yes                 |
| 2.6.3   | 2.6.2        | 2009 | PSF   | yes                 |
| 2.6.4   | 2.6.3        | 2009 | PSF   | yes                 |
| 2.6.5   | 2.6.4        | 2010 | PSF   | yes                 |
| 3.0     | 2.6          | 2008 | PSF   | yes                 |
| 3.0.1   | 3.0          | 2009 | PSF   | yes                 |
| 3.1     | 3.0.1        | 2009 | PSF   | yes                 |
| 3.1.1   | 3.1          | 2009 | PSF   | yes                 |
| 3.1.2   | 3.1.1        | 2010 | PSF   | yes                 |
| 3.1.3   | 3.1.2        | 2010 | PSF   | yes                 |
| 3.1.4   | 3.1.3        | 2011 | PSF   | yes                 |
| 3.2     | 3.1          | 2011 | PSF   | yes                 |
| 3.2.1   | 3.2          | 2011 | PSF   | yes                 |
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| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.2            | 2.1.1        | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          |              |           |            |                     |
| 2.1.2          | 2002         | PSF       | yes        |                     |
| 2.2.1          | 2.2          | 2002      | PSF        | yes                 |
| 2.2.2          | 2.2.1        | 2002      | PSF        | yes                 |
| 2.2.3          | 2.2.2        | 2003      | PSF        | yes                 |
| 2.3            | 2.2.2        | 2002-2003 | PSF        | yes                 |
| 2.3.1          | 2.3          | 2002-2003 | PSF        | yes                 |
| 2.3.2          | 2.3.1        | 2002-2003 | PSF        | yes                 |
| 2.3.3          | 2.3.2        | 2002-2003 | PSF        | yes                 |
| 2.3.4          | 2.3.3        | 2004      | PSF        | yes                 |
| 2.3.5          | 2.3.4        | 2005      | PSF        | yes                 |
| 2.4            | 2.3          | 2004      | PSF        | yes                 |
| 2.4.1          | 2.4          | 2005      | PSF        | yes                 |
| 2.4.2          | 2.4.1        | 2005      | PSF        | yes                 |
| 2.4.3          | 2.4.2        | 2006      | PSF        | yes                 |

|       |       |      |     |     |
|-------|-------|------|-----|-----|
| 2.4.4 | 2.4.3 | 2006 | PSF | yes |
| 2.5   | 2.4   | 2006 | PSF | yes |
| 2.5.1 | 2.5   | 2007 | PSF | yes |
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6   | 2.5   | 2008 | PSF | yes |
| 2.6.1 | 2.6   | 2008 | PSF | yes |
| 2.6.2 | 2.6.1 | 2009 | PSF | yes |
| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
| 2.6.4 | 2.6.3 | 2009 | PSF | yes |
| 2.6.5 | 2.6.4 | 2010 | PSF | yes |
| 2.7   | 2.6   | 2010 | PSF | yes |

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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|----------------|--------------|-----------|-------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI   | yes                 |

|                |           |           |            |         |
|----------------|-----------|-----------|------------|---------|
| 1.3 thru 1.5.2 | 1.2       | 1995-1999 | CNRI       | yes     |
| 1.6            | 1.5.2     | 2000      | CNRI       | no      |
| 2.0            | 1.6       | 2000      | BeOpen.com | no      |
| 1.6.1          | 1.6       | 2001      | CNRI       | yes (2) |
| 2.1            | 2.0+1.6.1 | 2001      | PSF        | no      |
| 2.0.1          | 2.0+1.6.1 | 2001      | PSF        | yes     |
| 2.1.1          | 2.1+2.0.1 | 2001      | PSF        | yes     |
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| 2.6            | 2.5       | 2008      | PSF        | yes     |
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| 2.6.2          | 2.6.1     | 2009      | PSF        | yes     |
| 2.6.3          | 2.6.2     | 2009      | PSF        | yes     |
| 2.6.4          | 2.6.3     | 2009      | PSF        | yes     |
| 2.6.5          | 2.6.4     | 2010      | PSF        | yes     |
| 3.0            | 2.6       | 2008      | PSF        | yes     |
| 3.0.1          | 3.0       | 2009      | PSF        | yes     |
| 3.1            | 3.0.1     | 2009      | PSF        | yes     |
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=====

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- \* Alice Bevan-McGregor
- \* Arfrever Frehtes Taifersar Arahesis
- \* Christophe Combelles
- \* Daniel Stutzbach
- \* Daniel Holth
- \* Hanno Schlichting
- \* Jannis Leidel
- \* Jason R. Coombs
- \* Jim Fulton
- \* Jonathan Lange
- \* Justin Azoff
- \* Lennart Regebro
- \* Marc Abramowitz
- \* Martin von Lwis
- \* Noufal Ibrahim
- \* Pete Hollobon
- \* Philip Jenvey
- \* Reinout van Rees
- \* Robert Myers
- \* Stefan H. Holek
- \* Tarek Ziad
- \* Toshio Kuratomi

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ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php\_curl, or with an Apache+mod\_ssl webserver for SSL client authentication. Just configure this file as the SSLCACertificateFile.#

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#### Contributors

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# Contributions to the urllib3 project

## Creator & Maintainer

\* Andrey Petrov <andrey.petrov@shazow.net>

## Contributors

In chronological order:

- \* victor.vde <<http://code.google.com/u/victor.vde/>>
  - \* HTTPS patch (which inspired HTTPSConnectionPool)
  
- \* erikcederstrand <<http://code.google.com/u/erikcederstrand/>>
  - \* NTLM-authenticated HTTPSConnectionPool
  - \* Basic-authenticated HTTPSConnectionPool (merged into make\_headers)
  
- \* niphlod <niphlod@gmail.com>
  - \* Client-verified SSL certificates for HTTPSConnectionPool
  - \* Response gzip and deflate encoding support
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- \* btoconnor <brian@btoconnor.net>
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- \* georgemarshall <<https://github.com/georgemarshall>>
  - \* Bugfixes, Improvements and Test coverage
  
- \* Thomas Kluyver <thomas@kluyver.me.uk>
  - \* Python 3 support
  
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  - \* Design review, bugfixes, test coverage.
  
- \* studer <theo.studer@gmail.com>
  - \* IPv6 url support and test coverage
  
- \* Shivaram Lingamneni <slingamn@cs.stanford.edu>
  - \* Support for explicitly closing pooled connections

- \* hartator <hartator@gmail.com>
- \* Corrected multipart behavior for params
  
- \* Thomas Weischuh <thomas@t-8ch.de>
- \* Support for TLS SNI
- \* API unification of ssl\_version/cert\_reqs
- \* SSL fingerprint and alternative hostname verification
- \* Bugfixes in testsuite
  
- \* Sune Kirkeby <mig@ibofobi.dk>
- \* Optional SNI-support for Python 2 via PyOpenSSL.
  
- \* Marc Schlaich <marc.schlaich@gmail.com>
- \* Various bugfixes and test improvements.
  
- \* Bryce Boe <bbzbryce@gmail.com>
- \* Correct six.moves conflict
- \* Fixed pickle support of some exceptions
  
- \* Boris Figovsky <boris.figovsky@ravellosystems.com>
- \* Allowed to skip SSL hostname verification
  
- \* Cory Benfield
- <<https://lukasa.co.uk/about/>>
- \* Stream method for Response objects.
- \* Return native strings in header values.
- \* Generate 'Host' header when using proxies.
  
- \* Jason Robinson <jaywink@basshero.org>
- \* Add missing WrappedSocket.fileno method in PyOpenSSL
  
- \* Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
- \* Fixed a race condition
  
- \* Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
- \* Added HTTPS (CONNECT) proxy support
  
- \* Stephen Holsapple <sholsapp@gmail.com>
- \* Added abstraction for granular control of request fields
  
- \* Martin von Gagern <Martin.vGagern@gmx.net>
- \* Support for non-ASCII header parameters
  
- \* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
- \* Support for separate connect and request timeouts
  
- \* Peter Waller <p@pwaller.net>
- \* HTTPResponse.tell() for determining amount received over the wire

- \* Nipunn Koorapati <nipunn1313@gmail.com>
- \* Ignore default ports when comparing hosts for equality
  
- \* Danilo @dbrgn <https://dbrgn.ch/>
- \* Disabled TLS compression by default on Python 3.2+
- \* Disabled TLS compression in pyopenssl contrib module
- \* Configurable cipher suites in pyopenssl contrib module
  
- \* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
- \* Account retries on proxy errors
  
- \* Nicolas Delaby <nicolas.delaby@ezeep.com>
- \* Use the platform-specific CA certificate locations
  
- \* Josh Schneier <https://github.com/jschneier>
- \* HTTPHeaderDict and associated tests and docs
- \* Bugfixes, docs, test coverage
  
- \* Tahia Khan <http://tahia.tk/>
- \* Added Timeout examples in docs
  
- \* Arthur Grunseid <https://grunseid.com>
- \* source\_address support and tests (with https://github.com/bui)
  
- \* Ian Cordasco <graffatcolmingov@gmail.com>
- \* PEP8 Compliance and Linting
- \* Add ability to pass socket options to an HTTP Connection
  
- \* Erik Tollerud <erik.tollerud@gmail.com>
- \* Support for standard library io module.
  
- \* Krishna Prasad <kprasad.iitd@gmail.com>
- \* Google App Engine documentation
  
- \* Aaron Meurer  
<asmeurer@gmail.com>
- \* Added Url.url, which unparses a Url
  
- \* Evgeny Kapun <abacabadabacaba@gmail.com>
- \* Bugfixes
  
- \* Benjamin Meyer <bm\_witness@yahoo.com>
- \* Security Warning Documentation update for proper capture
  
- \* Shivan Sornarajah <github@sornars.com>
- \* Support for using ConnectionPool and PoolManager as context managers.



- \* Alex Gaynor <alex.gaynor@gmail.com>
- \* Updates to the default SSL configuration
  
- \* Tomas Tomecek <ttomecek@redhat.com>
- \* Implemented generator for getting chunks from chunked responses.
  
- \* tlynn <https://github.com/tlynn>
- \* Respect the warning preferences at import.
  
- \* David D. Riddle <ddriddle@illinois.edu>
- \* IPv6 bugfixes in testsuite
  
- \* Thea Flowers <magicalgirl@google.com>
- \* App Engine environment tests.
- \* Documentation re-write.
  
- \* John Krauss <https://github.com/talos>
- \* Clues to debugging problems with `cryptography` dependency in docs
  
- \* Disassem <https://github.com/Disassem>
- \* Fix pool-default headers not applying for url-encoded requests like GET.
  
- \* James Atherfold <jlatherfold@hotmail.com>
- \* Bugfixes relating to cleanup of connections during errors.
  
- \* Christian Pedersen <https://github.com/chripede>
- \* IPv6 HTTPS proxy bugfix
  
- \* Jordan Moldow <https://github.com/jmoldow>
- \* Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- \* Bugfix for ``ConnectionPool.urlopen(release\_conn=False)``.
- \* Creation of ``HTTPConnectionPool.ResponseCls``.
  
- \* Predrag Gruevski <https://github.com/obi1kenobi>
- \* Made cert digest comparison use a constant-time algorithm.
  
- \* Adam Talsma <https://github.com/a-tal>
- \* Bugfix to ca\_cert file paths.
  
- \* Evan Meagher <https://evanmeagher.net>
- \* Bugfix related to `memoryview` usage in PyOpenSSL adapter
  
- \* John Vandenberg <jayvdb@gmail.com>
- \* Python 2.6 fixes; pyflakes and pep8 compliance
  
- \* Andy Caldwell <andy.m.caldwell@googlemail.com>
- \* Bugfix related to reusing connections in indeterminate states.

- \* Ville Skytt <ville.skytta@iki.fi>
- \* Logging efficiency improvements, spelling fixes, Travis config.
  
- \* Shige Takeda <smtakeda@gmail.com>
- \* Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response
  
- \* Jess Shapiro <jesse@jesseshapiro.net>
- \* Various character-encoding fixes/tweaks
- \* Disabling IPv6 DNS when IPv6 connections not supported
  
- \* David Foster <http://dafoster.net/>
- \* Ensure order of request and response headers are preserved.
  
- \* Jeremy Cline <jeremy@jcline.org>
- \* Added connection pool keys by scheme
  
- \* Aviv Palivoda <palaviv@gmail.com>
- \* History list to Retry object.
- \* HTTPResponse contains the last Retry object.
  
- \* Nate Prewitt <nate.prewitt@gmail.com>
- \* Ensure timeouts are not booleans and greater than zero.
- \* Fixed infinite loop in ``stream`` when amt=None.
- \* Added length\_remaining to determine remaining data to be read.
- \* Added enforce\_content\_length to raise exception when incorrect content-length received.
  
- \* Seth Michael Larson <sethmichaellarson@protonmail.com>
- \* Created selectors backport that supports PEP 475.
  
- \* Alexandre Dias <alex.dias@smarkets.com>
- \* Don't retry on timeout if method not in whitelist
  
- \* Moinuddin Quadri <moin18@gmail.com>
- \* Lazily load idna package
  
- \* Tom White <s6yg1ez3@mail2tor.com>
- \* Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.
  
- \* Tim Burke <tim.burke@gmail.com>
- \* Stop buffering entire deflate-encoded responses.
  
- \* Tuukka Mustonen <tuukka.mustonen@gmail.com>
- \* Add counter for status\_forcelist retries.
  
- \* Erik Rose <erik@mozilla.com>
- \* Bugfix to pyopenssl vendoring

- \* Wolfgang Richter <wolfgang.richter@gmail.com>
- \* Bugfix related to loading full certificate chains with PyOpenSSL backend.
  
- \* Mike Miller <github@mikeage.net>
- \* Logging improvements to include the HTTP(S) port when opening a new connection
  
- \* Ioannis Tziakos <mail@itziakos.gr>
- \* Fix ``util.selectors.\_fileobj\_to\_fd`` to accept ``long``.
- \* Update appveyor tox setup to use the 64bit python.
  
- \* Akamai  
(through Jess Shapiro) <jshapiro@akamai.com>
- \* Ongoing maintenance; 2017-2018
  
- \* Dominique Leuenberger <dimstar@opensuse.org>
- \* Minor fixes in the test suite
  
- \* Will Bond <will@wbond.net>
- \* Add Python 2.6 support to ``contrib.securetransport``
  
- \* Aleksei Alekseev <alekseev.yeskela@gmail.com>
- \* using auth info for socks proxy
  
- \* Chris Wilcox <git@crwilcox.com>
- \* Improve contribution guide
- \* Add ``HTTPResponse.geturl`` method to provide ``urllib2.urlopen().geturl()`` behavior
  
- \* Bruce Merry <https://www.brucemerry.org.za>
- \* Fix leaking exceptions when system calls are interrupted with zero timeout
  
- \* Hugo van Kemenade <https://github.com/hugovk>
- \* Drop support for EOL Python 2.6
  
- \* Tim Bell <https://github.com/timb07>
- \* Bugfix for responses with Content-Type: message/\* logging warnings
  
- \* Justin Bramley <https://github.com/jbramleycl>
- \* Add ability to handle multiple Content-Encodings
  
- \* Katsuhiko YOSHIDA <https://github.com/kyoshidajp>
- \* Remove Authorization header  
regardless of case when redirecting to cross-site
  
- \* James Meickle <https://permadeath.com/>
- \* Improve handling of Retry-After header
  
- \* Chris Jerdonek <chris.jerdonek@gmail.com>

- \* Remove a spurious TypeError from the exception chain inside HTTPConnectionPool.\_make\_request(), also for BaseExceptions.
- \* Jorge Lopez Silva <<https://github.com/jalopezsilva>>
- \* Added support for forwarding requests through HTTPS proxies.
- \* Benno Rice <[benno@jeamland.net](mailto:benno@jeamland.net)>
- \* Allow cadata parameter to be passed to underlying ``SSLContext.load\_verify\_locations()``.
- \* Keiichi Kobayashi <[abok.1k@gmail.com](mailto:abok.1k@gmail.com)>
- \* Rename VerifiedHTTPSConnection to HTTPSConnection
- \* Himanshu Garg <[garg\\_himanshu@outlook.com](mailto:garg_himanshu@outlook.com)>
- \* DOC & LICENSE Update
- \* Hod Bin Noon <[bin.noon.hod@gmail.com](mailto:bin.noon.hod@gmail.com)>
- \* Test improvements
- \* Chris Olufson <[tycarac@gmail.com](mailto:tycarac@gmail.com)>
- \* Fix for connection not being released on HTTP redirect and response not preloaded
- \* [Bastiaan Bakker] <<https://github.com/bastiaanb>>
- \* Support for logging session keys  
via environment variable ``SSLKEYLOGFILE`` (Python 3.8+)
- \* [Your name or handle] <[email or website]>
- \* [Brief summary of your changes]

## 1.258 python-pip 20.3.4

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team.

In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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|---------|--------------|------|-------|---------------------|
| 2.5     | 2.4          | 2006 | PSF   | yes                 |
| 2.5.1   | 2.5          | 2007 | PSF   | yes                 |
| 2.5.2   | 2.5.1        | 2008 | PSF   | yes                 |
| 2.5.3   | 2.5.2        | 2008 | PSF   | yes                 |
| 2.6     | 2.5          | 2008 | PSF   | yes                 |
| 2.6.1   | 2.6          | 2008 | PSF   | yes                 |
| 2.6.2   | 2.6.1        | 2009 | PSF   | yes                 |
| 2.6.3   | 2.6.2        | 2009 | PSF   | yes                 |
| 2.6.4   | 2.6.3        | 2009 | PSF   | yes                 |
| 2.6.5   | 2.6.4        | 2010 | PSF   | yes                 |
| 3.0     | 2.6          | 2008 | PSF   | yes                 |
| 3.0.1   | 3.0          | 2009 | PSF   | yes                 |
| 3.1     | 3.0.1        | 2009 | PSF   | yes                 |
| 3.1.1   | 3.1          | 2009 | PSF   | yes                 |
| 3.1.2   | 3.1.1        | 2010 | PSF   | yes                 |
| 3.1.3   | 3.1.2        | 2010 | PSF   | yes                 |
| 3.1.4   | 3.1.3        | 2011 | PSF   | yes                 |
| 3.2     | 3.1          | 2011 | PSF   | yes                 |
| 3.2.1   | 3.2          | 2011 | PSF   | yes                 |
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| 3.3     | 3.2          |      |       |                     |
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| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
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| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          |              |           |            |                     |
| 2.1.2          | 2002         | PSF       | yes        |                     |
| 2.2.1          | 2.2          | 2002      | PSF        | yes                 |
| 2.2.2          | 2.2.1        | 2002      | PSF        | yes                 |
| 2.2.3          | 2.2.2        | 2003      | PSF        | yes                 |
| 2.3            | 2.2.2        | 2002-2003 | PSF        | yes                 |
| 2.3.1          | 2.3          | 2002-2003 | PSF        | yes                 |
| 2.3.2          | 2.3.1        | 2002-2003 | PSF        | yes                 |
| 2.3.3          | 2.3.2        | 2002-2003 | PSF        | yes                 |
| 2.3.4          | 2.3.3        | 2004      | PSF        | yes                 |
| 2.3.5          | 2.3.4        | 2005      | PSF        | yes                 |
| 2.4            | 2.3          | 2004      | PSF        | yes                 |
| 2.4.1          | 2.4          | 2005      | PSF        | yes                 |
| 2.4.2          | 2.4.1        | 2005      | PSF        | yes                 |
| 2.4.3          | 2.4.2        | 2006      | PSF        | yes                 |
| 2.4.4          | 2.4.3        | 2006      | PSF        | yes                 |
| 2.5            | 2.4          | 2006      | PSF        | yes                 |
| 2.5.1          | 2.5          | 2007      | PSF        | yes                 |
| 2.5.2          | 2.5.1        | 2008      | PSF        | yes                 |
| 2.5.3          | 2.5.2        | 2008      | PSF        | yes                 |
| 2.6            | 2.5          | 2008      | PSF        | yes                 |
| 2.6.1          | 2.6          | 2008      | PSF        | yes                 |
| 2.6.2          | 2.6.1        | 2009      | PSF        | yes                 |
| 2.6.3          | 2.6.2        | 2009      | PSF        | yes                 |
| 2.6.4          | 2.6.3        | 2009      | PSF        | yes                 |
| 2.6.5          | 2.6.4        | 2010      | PSF        | yes                 |
| 2.7            | 2.6          | 2010      | PSF        | yes                 |

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
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| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          |              |           |            |                     |
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| 2.3.1          | 2.3          | 2002-2003 | PSF        | yes                 |
| 2.3.2          | 2.3.1        | 2002-2003 | PSF        | yes                 |
| 2.3.3          | 2.3.2        | 2002-2003 | PSF        | yes                 |
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|       |       |      |     |     |
|-------|-------|------|-----|-----|
| 2.3.5 | 2.3.4 | 2005 | PSF | yes |
| 2.4   | 2.3   | 2004 | PSF | yes |
| 2.4.1 | 2.4   | 2005 | PSF | yes |
| 2.4.2 | 2.4.1 | 2005 | PSF | yes |
| 2.4.3 | 2.4.2 | 2006 | PSF | yes |
| 2.4.4 | 2.4.3 | 2006 | PSF | yes |
| 2.5   | 2.4   | 2006 | PSF | yes |
| 2.5.1 | 2.5   | 2007 | PSF | yes |
| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6   | 2.5   | 2008 | PSF | yes |
| 2.6.1 | 2.6   | 2008 | PSF | yes |
| 2.6.2 | 2.6.1 | 2009 | PSF | yes |
| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
| 2.6.4 | 2.6.3 | 2009 | PSF | yes |
| 2.6.5 | 2.6.4 | 2010 | PSF | yes |
| 3.0   | 2.6   | 2008 | PSF | yes |
| 3.0.1 | 3.0   | 2009 | PSF | yes |
| 3.1   | 3.0.1 | 2009 | PSF | yes |
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- \* Arfrever Frehtes Taifersar Arahesis
- \* Christophe Combelles
- \* Daniel Stutzbach
- \* Daniel Holth
- \* Hanno Schlichting
- \* Jannis Leidel
- \* Jason R. Coombs
- \* Jim Fulton
- \* Jonathan Lange
- \* Justin Azoff
- \* Lennart Regebro
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- \* Pete Hollobon
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- \* Reinout van Rees

- \* Robert Myers
- \* Stefan H. Holek
- \* Tarek Ziad
- \* Toshio Kuratomi

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ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php\_curl, or with an Apache+mod\_ssl webserver for SSL client authentication.

Just configure this file as the SSLCACertificateFile.#

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- \* Pete Hollobon
- \* Phillip J. Eby
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## 1.259 urllib3 1.22

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# Contributions to the urllib3 project

## Creator & Maintainer

\* Andrey Petrov <andrey.petrov@shazow.net>

## Contributors

In chronological order:

\* victor.vde <<http://code.google.com/u/victor.vde/>>

\* HTTPS patch (which inspired HTTPSConnectionPool)

\* erikcederstrand <<http://code.google.com/u/erikcederstrand/>>

\* NTLM-authenticated HTTPSConnectionPool

\* Basic-authenticated HTTPSConnectionPool (merged into make\_headers)

\* niphlod <niphlod@gmail.com>

\* Client-verified SSL certificates for HTTPSConnectionPool

\* Response gzip and deflate encoding support

\* Better unicode support for filepost using StringIO buffers

\* btoconnor <brian@btoconnor.net>

\* Non-multipart encoding for POST requests

\* p.dobrogost <<http://code.google.com/u/@WBRSRIBZDhBFXQB6/>>

\* Code review, PEP8 compliance, benchmark fix

- \* kennethreitz <me@kennethreitz.com>
- \* Bugfixes, suggestions, Requests integration
  
- \* georgemarshall <<http://github.com/georgemarshall>>
- \* Bugfixes, Improvements and Test coverage
  
- \*
- Thomas Kluyver <thomas@kluyver.me.uk>
- \* Python 3 support
  
- \* brandon-rhodes <<http://rhodesmill.org/brandon>>
- \* Design review, bugfixes, test coverage.
  
- \* studer <theo.studer@gmail.com>
- \* IPv6 url support and test coverage
  
- \* Shivaram Lingamneni <slingamn@cs.stanford.edu>
- \* Support for explicitly closing pooled connections
  
- \* hartator <hartator@gmail.com>
- \* Corrected multipart behavior for params
  
- \* Thomas Weischuh <thomas@t-8ch.de>
- \* Support for TLS SNI
- \* API unification of ssl\_version/cert\_reqs
- \* SSL fingerprint and alternative hostname verification
- \* Bugfixes in testsuite
  
- \* Sune Kirkeby <mig@ibofobi.dk>
- \* Optional SNI-support for Python 2 via PyOpenSSL.
  
- \* Marc Schlaich <marc.schlaich@gmail.com>
- \* Various bugfixes and test improvements.
  
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- \* Fixed pickle support of some exceptions
  
- \* Boris Figovsky <boris.figovsky@ravello systems.com>
- \* Allowed to skip SSL hostname verification
  
- \* Cory Benfield
- <<http://lukasa.co.uk/about/>>
- \* Stream method for Response objects.
- \* Return native strings in header values.
- \* Generate 'Host' header when using proxies.

- \* Jason Robinson <jaywink@basshero.org>
- \* Add missing WrappedSocket.fileno method in PyOpenSSL
  
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- \* Fixed a race condition
  
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- \* Added HTTPS (CONNECT) proxy support
  
- \* Stephen Holsapple <sholsapp@gmail.com>
- \* Added abstraction for granular control of request fields
  
- \* Martin von Gagern <Martin.vGagern@gmx.net>
- \* Support for non-ASCII header parameters
  
- \* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
- \* Support for separate connect and request timeouts
  
- \* Peter Waller <p@pwaller.net>
- \* HTTPResponse.tell() for determining amount received over the wire
  
- \* Nipunn Koorapati <nipunn1313@gmail.com>
- \* Ignore default ports when comparing hosts for equality
  
- \* Danilo @dbrgn <http://dbrgn.ch/>
- \* Disabled TLS compression by default on Python 3.2+
- \* Disabled TLS compression in pyopenssl contrib module
- \* Configurable cipher suites in pyopenssl contrib module
  
- \* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
- \* Account retries on proxy errors
  
- \* Nicolas Delaby <nicolas.delaby@ezeep.com>
- \* Use the platform-specific CA certificate locations
  
- \* Josh Schneier <https://github.com/jschneier>
- \* HTTPHeaderDict and associated tests and docs
- \* Bugfixes, docs, test coverage
  
- \* Tahia Khan <http://tahia.tk/>
- \* Added Timeout examples in docs
  
- \* Arthur Grunseid <http://grunseid.com>
- \* source\_address support and tests (with https://github.com/bui)
  
- \* Ian Cordasco <graffatcolmingov@gmail.com>
- \* PEP8 Compliance and Linting
- \* Add ability to pass socket options to an HTTP Connection



- \* Erik Tollerud <erik.tollerud@gmail.com>
- \* Support for standard library io module.
  
- \* Krishna Prasad <kprasad.iitd@gmail.com>
- \* Google App Engine documentation
  
- \* Aaron Meurer <asmeurer@gmail.com>
- \* Added Url.url, which unparses a Url
  
- \* Evgeny Kapun <abacabadabacaba@gmail.com>
- \* Bugfixes
  
- \* Benjamin Meyer <bm\_witness@yahoo.com>
- \* Security Warning Documentation update for proper capture
  
- \* Shivan Sornarajah <github@sornars.com>
- \* Support for using ConnectionPool and PoolManager as context managers.
  
- \* Alex Gaynor <alex.gaynor@gmail.com>
- \* Updates to the default SSL configuration
  
- \* Tomas Tomecek <ttomecek@redhat.com>
- \* Implemented generator for getting chunks from chunked responses.
  
- \* tlynn <https://github.com/tlynn>
- \* Respect the warning preferences at import.
  
- \* David D. Riddle <ddriddle@illinois.edu>
- \* IPv6 bugfixes in testsuite
  
- \* Jon Wayne Parrott <jonwayne@google.com>
- \* App Engine environment tests.
  
- \* John Krauss <https://github.com/talos>
- \* Clues to debugging problems with `cryptography` dependency in docs
  
- \* Disassem <https://github.com/Disassem>
- \* Fix pool-default headers not applying for url-encoded requests like GET.
  
- \* James  
Atherfold <jlatherfold@hotmail.com>
- \* Bugfixes relating to cleanup of connections during errors.
  
- \* Christian Pedersen <https://github.com/chripede>
- \* IPv6 HTTPS proxy bugfix
  
- \* Jordan Moldow <https://github.com/jmoldow>

- \* Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- \* Bugfix for ``ConnectionPool.urlopen(release\_conn=False)``.
- \* Creation of ``HTTPConnectionPool.ResponseCls``.
  
- \* Predrag Gruevski <<https://github.com/obi1kenobi>>
  - \* Made cert digest comparison use a constant-time algorithm.
  
- \* Adam Talsma <<https://github.com/a-tal>>
  - \* Bugfix to ca\_cert file paths.
  
- \* Evan Meagher <<https://evanmeagher.net>>
  - \* Bugfix related to `memoryview` usage in PyOpenSSL adapter
  
- \* John Vandenberg <[jayvdb@gmail.com](mailto:jayvdb@gmail.com)>
  - \* Python 2.6 fixes; pyflakes and pep8 compliance
  
- \* Andy Caldwell <[andy.m.caldwell@gmail.com](mailto:andy.m.caldwell@gmail.com)>
  - \* Bugfix related to reusing connections in indeterminate states.
  
- \* Ville Skytt <[ville.skytta@iki.fi](mailto:ville.skytta@iki.fi)>
  - \* Logging efficiency improvements, spelling fixes, Travis config.
  
- \* Shige Takeda <[smtakeda@gmail.com](mailto:smtakeda@gmail.com)>
  - \* Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response
  
- \* Jesse Shapiro <[jesse@jesseshapiro.net](mailto:jesse@jesseshapiro.net)>
  - \* Various character-encoding fixes/tweaks
  - \* Disabling IPv6 DNS when IPv6 connections not supported
  
- \* David Foster <<http://dafoster.net/>>
  - \* Ensure order of request and response headers are preserved.
  
- \* Jeremy Cline <[jeremy@jcline.org](mailto:jeremy@jcline.org)>
  - \* Added connection pool keys by scheme
  
- \* Aviv Palivoda <[palaviv@gmail.com](mailto:palaviv@gmail.com)>
  - \* History list to Retry object.
  - \* HTTPResponse contains the last Retry object.
  
- \* Nate Prewitt <[nate.prewitt@gmail.com](mailto:nate.prewitt@gmail.com)>
  - \* Ensure timeouts are not booleans and greater than zero.
  - \* Fixed infinite loop in ``stream`` when amt=None.
  - \* Added length\_remaining to determine remaining data to be read.
  - \* Added enforce\_content\_length to raise exception when incorrect content-length received.
  
- \* Seth Michael Larson <[sethmichaellarson@protonmail.com](mailto:sethmichaellarson@protonmail.com)>
  - \* Created selectors

backport that supports PEP 475.

\* Alexandre Dias <alex.dias@smarkets.com>

\* Don't retry on timeout if method not in whitelist

\* Moinuddin Quadri <moin18@gmail.com>

\* Lazily load idna package

\* Tom White <s6yg1ez3@mail2tor.com>

\* Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

\* Tim Burke <tim.burke@gmail.com>

\* Stop buffering entire deflate-encoded responses.

\* Tuukka Mustonen <tuukka.mustonen@gmail.com>

\* Add counter for status\_forcelist retries.

\* Erik Rose <erik@mozilla.com>

\* Bugfix to pyopenssl vendoring

\* [Your name or handle] <[email or website]>

\* [Brief summary of your changes]

## 1.260 pbr 3.1.1

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# 1.261 sphinx-rtd-theme 0.4.3

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\* markupsafe.\_speedups

\* ~~~~~

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\* C implementation of escaping for better performance. Used instead of



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## 1.265 python-pip 19.2.3

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- Chris McDonough, 2011/02/16

- Wichert Akkerman, 2012/02/02

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Python was created in the early 1990s by Guido van Rossum at Stichting  
Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands  
as a successor of a language called ABC. Guido remains Python's  
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for  
National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>)  
in Reston, Virginia where he released several versions of the  
software.

In May 2000, Guido and the Python core development team moved to  
BeOpen.com to form the BeOpen PythonLabs team. In October of the same  
year, the PythonLabs team moved to Digital Creations (now Zope  
Corporation, see <http://www.zope.com>). In 2001, the Python Software  
Foundation (PSF, see <http://www.python.org/psf/>) was formed, a  
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All Python releases are Open Source (see <http://www.opensource.org> for  
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| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.2            | 2.1.1        | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          |              |           |            |                     |
| 2.1.2          | 2002         | PSF       | yes        |                     |
| 2.2.1          | 2.2          | 2002      | PSF        | yes                 |
| 2.2.2          | 2.2.1        | 2002      | PSF        | yes                 |
| 2.2.3          | 2.2.2        | 2003      | PSF        | yes                 |
| 2.3            | 2.2.2        | 2002-2003 | PSF        | yes                 |
| 2.3.1          | 2.3          | 2002-2003 | PSF        | yes                 |
| 2.3.2          | 2.3.1        | 2002-2003 | PSF        | yes                 |
| 2.3.3          | 2.3.2        | 2002-2003 | PSF        | yes                 |
| 2.3.4          | 2.3.3        | 2004      | PSF        | yes                 |
| 2.3.5          | 2.3.4        | 2005      | PSF        | yes                 |
| 2.4            | 2.3          | 2004      | PSF        | yes                 |
| 2.4.1          | 2.4          | 2005      | PSF        | yes                 |
| 2.4.2          | 2.4.1        | 2005      | PSF        | yes                 |
| 2.4.3          | 2.4.2        | 2006      | PSF        | yes                 |
| 2.4.4          | 2.4.3        | 2006      | PSF        | yes                 |
| 2.5            | 2.4          | 2006      | PSF        | yes                 |
| 2.5.1          | 2.5          | 2007      | PSF        | yes                 |
| 2.5.2          | 2.5.1        | 2008      | PSF        | yes                 |
| 2.5.3          | 2.5.2        | 2008      | PSF        | yes                 |
| 2.6            | 2.5          | 2008      | PSF        | yes                 |
| 2.6.1          | 2.6          | 2008      | PSF        | yes                 |
| 2.6.2          | 2.6.1        | 2009      | PSF        | yes                 |
| 2.6.3          | 2.6.2        | 2009      | PSF        | yes                 |
| 2.6.4          | 2.6.3        | 2009      | PSF        | yes                 |
| 2.6.5          | 2.6.4        | 2010      | PSF        | yes                 |
| 2.7            | 2.6          | 2010      | PSF        | yes                 |

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.2            | 2.1.1        | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          |              |           |            |                     |
| 2.1.2          | 2002         | PSF       | yes        |                     |
| 2.2.1          | 2.2          | 2002      | PSF        | yes                 |
| 2.2.2          | 2.2.1        | 2002      | PSF        | yes                 |
| 2.2.3          | 2.2.2        | 2003      | PSF        | yes                 |
| 2.3            | 2.2.2        | 2002-2003 | PSF        | yes                 |
| 2.3.1          | 2.3          | 2002-2003 | PSF        | yes                 |
| 2.3.2          | 2.3.1        | 2002-2003 | PSF        | yes                 |
| 2.3.3          | 2.3.2        | 2002-2003 | PSF        | yes                 |
| 2.3.4          | 2.3.3        | 2004      | PSF        | yes                 |

|       |       |      |     |     |
|-------|-------|------|-----|-----|
| 2.3.5 | 2.3.4 | 2005 | PSF | yes |
| 2.4   | 2.3   | 2004 | PSF | yes |
| 2.4.1 | 2.4   | 2005 | PSF | yes |
| 2.4.2 | 2.4.1 | 2005 | PSF | yes |
| 2.4.3 | 2.4.2 | 2006 | PSF | yes |
| 2.4.4 | 2.4.3 | 2006 | PSF | yes |
| 2.5   | 2.4   | 2006 | PSF | yes |
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| 2.5.2 | 2.5.1 | 2008 | PSF | yes |
| 2.5.3 | 2.5.2 | 2008 | PSF | yes |
| 2.6   | 2.5   | 2008 | PSF | yes |
| 2.6.1 | 2.6   | 2008 | PSF | yes |
| 2.6.2 | 2.6.1 | 2009 | PSF | yes |
| 2.6.3 | 2.6.2 | 2009 | PSF | yes |
| 2.6.4 | 2.6.3 | 2009 | PSF | yes |
| 2.6.5 | 2.6.4 | 2010 | PSF | yes |
| 3.0   | 2.6   | 2008 | PSF | yes |
| 3.0.1 | 3.0   | 2009 | PSF | yes |
| 3.1   | 3.0.1 | 2009 | PSF | yes |
| 3.1.1 | 3.1   | 2009 | PSF | yes |
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This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates

file (certdata.txt). This file can be found in the mozilla source tree:  
<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>  
It contains the certificates in PEM format and therefore  
can be directly used with curl / libcurl / php\_curl, or with  
an Apache+mod\_ssl webserver for SSL client authentication.  
Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$  
\$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$  
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## 1.266 jinja2 2.11.2

### 1.266.1 Available under license :

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```
import io
import re

from setuptools import find_packages
from setuptools import setup

with io.open("README.rst", "rt", encoding="utf8") as f:
    readme = f.read()
```

```

with io.open("src/jinja2/__init__.py", "rt", encoding="utf8") as f:
    version = re.search(r'__version__ = "(.*?)"', f.read(), re.M).group(1)

setup(
    name="Jinja2",
    version=version,
    url="https://palletsprojects.com/p/jinja/",
    project_urls={
        "Documentation": "https://jinja.palletsprojects.com/",
        "Code": "https://github.com/pallets/jinja",
        "Issue tracker": "https://github.com/pallets/jinja/issues",
    },
    license="BSD-3-Clause",
    author="Armin Ronacher",
    author_email="armin.ronacher@active-4.com",
    maintainer="Pallets",
    maintainer_email="contact@palletsprojects.com",
    description="A very fast and expressive template engine.",
    long_description=readme,
    classifiers=[
        "Development Status
:: 5 - Production/Stable",
        "Environment :: Web Environment",
        "Intended Audience :: Developers",
        "License :: OSI Approved :: BSD License",
        "Operating System :: OS Independent",
        "Programming Language :: Python",
        "Programming Language :: Python :: 2",
        "Programming Language :: Python :: 2.7",
        "Programming Language :: Python :: 3",
        "Programming Language :: Python :: 3.5",
        "Programming Language :: Python :: 3.6",
        "Programming Language :: Python :: 3.7",
        "Programming Language :: Python :: 3.8",
        "Programming Language :: Python :: Implementation :: CPython",
        "Programming Language :: Python :: Implementation :: PyPy",
        "Topic :: Internet :: WWW/HTTP :: Dynamic Content",
        "Topic :: Software Development :: Libraries :: Python Modules",
        "Topic :: Text Processing :: Markup :: HTML",
    ],
    packages=find_packages("src"),
    package_dir={"": "src"},
    include_package_data=True,
    python_requires=">=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*, !=3.4.*",
    install_requires=["MarkupSafe>=0.23"],
    extras_require={"i18n": ["Babel>=0.8"]},
    entry_points={"babel.extractors": ["jinja2 = jinja2.ext:babel_extract[i18n]"]},

```

)

Found in path(s):

\* /opt/cola/permits/1160880513\_1620412497.03/0/jinja2-2-11-2-1-tar-gz/Jinja2-2.11.2/setup.py

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Metadata-Version: 2.1

Name: Jinja2

Version: 2.11.2

Summary: A very fast and expressive template engine.

Home-page: <https://palletsprojects.com/p/jinja/>

Author: Armin Ronacher

Author-email: [armin.ronacher@active-4.com](mailto:armin.ronacher@active-4.com)

Maintainer: Pallets

Maintainer-email: [contact@palletsprojects.com](mailto:contact@palletsprojects.com)

License: BSD-3-Clause

Project-URL: Documentation, <https://jinja.palletsprojects.com/>

Project-URL: Code, <https://github.com/pallets/jinja>

Project-URL: Issue tracker, <https://github.com/pallets/jinja/issues>

Description: Jinja

=====

Jinja is a fast, expressive, extensible templating engine. Special placeholders in the template allow writing code similar to Python syntax. Then the template is passed data to render the final document.

It includes:

- Template inheritance and inclusion.
- Define and import macros within templates.
- HTML templates can use autoescaping to prevent XSS from untrusted user input.
- A sandboxed environment can safely render untrusted templates.
- AsyncIO support for generating templates and calling async functions.
- I18N support with Babel.
- Templates are compiled to optimized Python code just-in-time and cached, or can be compiled ahead-of-time.
- Exceptions point to the correct line in templates to make debugging easier.
- Extensible filters, tests, functions, and even syntax.

Jinja's philosophy is that while application logic belongs in Python if possible, it shouldn't make the template designer's job difficult by restricting functionality too much.

Installing

-----

Install and update using `pip`_``:

```
.. code-block:: text
```

```
$ pip install -U Jinja2
```

```
.. _pip:
```

<https://pip.pypa.io/en/stable/quickstart/>

In A Nutshell

-----  
.. code-block:: jinja

```
{% extends "base.html" % }
{% block title % }Members{% endblock % }
{% block content % }
<ul>
{% for user in users % }
  <li><a href="{{ user.url }}">{{ user.username }}</a></li>
{% endfor % }
</ul>
{% endblock % }
```

## Links

-----

- Website: <https://palletsprojects.com/p/jinja/>
- Documentation: <https://jinja.palletsprojects.com/>
- Releases: <https://pypi.org/project/Jinja2/>
- Code: <https://github.com/pallets/jinja>
- Issue tracker: <https://github.com/pallets/jinja/issues>
- Test status: [https://dev.azure.com/pallets/jinja/\\_build](https://dev.azure.com/pallets/jinja/_build)
- Official chat: <https://discord.gg/t6rrQZH>

Platform: UNKNOWN

Classifier: Development

Status :: 5 - Production/Stable

Classifier: Environment :: Web Environment

Classifier: Intended Audience :: Developers

Classifier: License :: OSI Approved :: BSD License

Classifier: Operating System :: OS Independent

Classifier: Programming Language :: Python

Classifier: Programming Language :: Python :: 2

Classifier: Programming Language :: Python :: 2.7

Classifier: Programming Language :: Python :: 3

Classifier: Programming Language :: Python :: 3.5

Classifier: Programming Language :: Python :: 3.6

Classifier: Programming Language :: Python :: 3.7

Classifier: Programming Language :: Python :: 3.8

Classifier: Programming Language :: Python :: Implementation :: CPython

Classifier: Programming Language :: Python :: Implementation :: PyPy

Classifier: Topic :: Internet :: WWW/HTTP :: Dynamic Content

Classifier: Topic :: Software Development :: Libraries :: Python Modules

Classifier: Topic :: Text Processing :: Markup :: HTML

Requires-Python: >=2.7, !=3.0.\*, !=3.1.\*, !=3.2.\*, !=3.3.\*, !=3.4.\*

Description-Content-Type:



text/x-rst

Provides-Extra: i18n

Found in path(s):

\* /opt/cola/permits/1160880513\_1620412497.03/0/jinja2-2-11-2-1-tar-gz/Jinja2-2.11.2/PKG-INFO

\* /opt/cola/permits/1160880513\_1620412497.03/0/jinja2-2-11-2-1-tar-gz/Jinja2-2.11.2/src/Jinja2.egg-info/PKG-INFO

# 1.267 pyopenssl 19.1.0

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## 1.275 bash 5.1.4

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## 1.276 openosc 1.0.5

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+++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the preprocessing pass, and extensive changes in messages.c, input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration including heavy testing and verification of cross assemblers and file splits and renaming, converted gas to strictly ansi C including full prototypes, added support for m680[34]0 & cpu32, considerable work on i960 including a coff port (including considerable amounts of reverse engineering), a sparc opcode file rewrite, decstation, rs6000, and hp300hpux host ports, updated "know" assertions and made them work, much other reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan. Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith

Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot

effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run

that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves,

then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the

entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and



therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License.

Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

- a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under

Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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Preamble

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When we speak of free software, we are referring to freedom, not price. Our General Public Licenses are designed to make sure that you have the freedom to distribute copies of free software (and charge for this service if you wish), that you receive source code or can get it if you want it, that you can change the software or use pieces of it in new free programs; and that you know you can do these things.

To protect your rights, we need to make restrictions that forbid anyone to deny you these rights or to ask you to surrender the rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library, or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed

for everyone's  
free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because  
of this blurred distinction, using the ordinary General  
Public License for libraries did not effectively promote software  
sharing, because most developers did not use the libraries. We  
concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference  
between a  
"work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and

distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

- a) The modified work must itself be a software library.
- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under

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3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the

Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials

specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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## 1.283 go-toml 1.9.0

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## 1.284 avahi 0.6

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## 1.285 tevent 0.10.2

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# 1.286 talloc 2.3.1

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## 1.287 go-playground-locales 0.14.0

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## 1.288 leodido-go-urn 1.2.1

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```

Damian Gryski <dgryski@gmail.com>

Jan Mercl <0xjnm@gmail.com>

Jonathan Swinney <jswinney@amazon.com>

Kai Backman <kaib@golang.org>

Klaus Post <klauspost@gmail.com>

Marc-Antoine Ruel <maruel@chromium.org>

Nigel Tao <nigeltao@golang.org>

Rob Pike <r@golang.org>

Rodolfo Carvalho <rhcavalho@gmail.com>

Russ Cox <rsc@golang.org>

Sebastien Binet <seb.binet@gmail.com>

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```

Albert Pretorius <pretoalb@gmail.com>

Arne Beer <arne@twobeer.de>

Billy

Robert O'Neal III <billy.oneal@gmail.com> <bion@microsoft.com>

Chris Kennelly <ckennelly@google.com> <ckennelly@ckennelly.com>

Christopher Seymour <chris.j.seymour@hotmail.com>

David Coeurjolly <david.coeurjolly@liris.cnrs.fr>

Dominic Hamon <dma@stripsock.com>

Eric Fiselier <eric@efcs.ca>

Eugene Zhuk <eugene.zhuk@gmail.com>

Evgeny Safronov <division494@gmail.com>  
Felix Homann <linuxaudio@showlabor.de>  
Ismael Jimenez Martinez <ismael.jimenez.martinez@gmail.com>  
Joao Paulo Magalhaes <joaoppmagalhaes@gmail.com>  
JianXiong Zhou <zhoujianxiong2@gmail.com>  
Jussi Knuuttila <jussi.knuuttila@gmail.com>  
Kaito Udagawa <umireon@gmail.com>  
Kai Wolf <kai.wolf@gmail.com>  
Lei Xu <eddyxu@gmail.com>  
Matt Clarkson <mattyclarkson@gmail.com>  
Maxim Vafin <maxvafin@gmail.com>  
Nick Hutchinson <nshutchinson@gmail.com>  
Oleksandr Sochka <sasha.sochka@gmail.com>  
Pascal Leroy <phl@google.com>  
Paul Redmond <paul.redmond@gmail.com>  
Pierre Phaneuf <pphaneuf@google.com>  
Radoslav Yovchev <radoslav.tm@gmail.com>  
Ray  
Glover <ray.glover@uk.ibm.com>  
Shuo Chen <chenshuo@chenshuo.com>  
Yusuke Suzuki <utatane.tea@gmail.com>  
Tobias Ulvgrd <tobias.ulvgard@dirac.se>  
Zbigniew Skowron <zbychs@gmail.com>  
Dominik Czarnota <dominik.b.czarnota@gmail.com>

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# 1.295 nginx 1.12.1

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be optional: if the application does not supply it, the square  
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## Copyright FAQ

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1. I don't understand the resale restriction... What gives?

Bitstream is giving away these fonts, but wishes to ensure its competitors can't just drop the fonts as is into a font sale system and sell them as is. It seems fair that if Bitstream can't make money from the Bitstream Vera fonts, their competitors should not be able to do so either. You can sell the fonts as part of any software package, however.

2. I want to package these fonts separately for distribution and sale as part of a larger software package or system. Can I do so?

Yes. A RPM or Debian package is a "larger software package" to begin with, and you aren't selling them independently by themselves. See 1. above.

3. Are derivative works allowed?

Yes!

4. Can I change or add to the font(s)?

Yes, but you must change the name(s) of the font(s).

5. Under what terms are derivative works allowed?

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6. If I have improvements for Bitstream Vera, is it possible they might get adopted in future versions?

Yes. The contract between the Gnome Foundation and Bitstream has provisions for working with Bitstream to ensure quality additions to the Bitstream Vera font family. Please contact us if you have such additions. Note, that in general, we will want such additions for the entire family, not just a single font, and that you'll have to keep both Gnome and Jim Lyles, Vera's designer, happy! To make sense to add

glyphs to the font, they must be stylistically in keeping with Vera's design. Vera cannot become a "ransom note" font. Jim Lyles will be providing a document describing the design elements used in Vera, as a guide and aid for people interested in contributing to Vera.

7. I want to sell a software package that uses these fonts: Can I do so?

Sure. Bundle the fonts with your software and sell your software with the fonts. That is the intent of the copyright.

8. If applications have built the names "Bitstream Vera" into them, can I override this somehow to use fonts of my choosing?

This depends on exact details of the software. Most open source systems and software (e.g., Gnome, KDE, etc.) are now converting to use fontconfig (see [www.fontconfig.org](http://www.fontconfig.org)) to handle font configuration, selection and substitution; it has provisions for overriding font names and substituting alternatives. An example is provided by the supplied local.conf file, which chooses the family Bitstream Vera for "sans", "serif" and "monospace". Other software (e.g., the XFree86 core server) has other mechanisms for font substitution.

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```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) 19yy <name of author>
```

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or  
(at your option) any later version.

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```
Gnomovision version 69, Copyright (C) 19yy name of author  
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.  
This is free software, and you are welcome to redistribute it  
under certain conditions; type `show c' for details.
```

The hypothetical commands `show w' and `show c' should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than `show w' and `show c'; they could even be mouse-clicks or menu items--whatever suits your program.

You should also get your employer (if you work as a programmer) or your

school, if any, to sign a "copyright disclaimer" for the program, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the program  
'Gnomovision' (which makes passes at compilers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989  
Ty Coon, President of Vice

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## 1.297 coverage 3.6

### 1.297.1 Available under license :

No license file was found, but licenses were detected in source scan.

```
/* Copyright (c) 2010
 * @author Laurence Wheway
 * Dual licensed under the MIT (http://www.opensource.org/licenses/mit-license.php)
 * and GPL (http://www.opensource.org/licenses/gpl-license.php) licenses.
 *
 * @version 1.2.0
 */
```

Found in path(s):

```
*/opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/coverage/htmlfiles/jquery.isonscreen.js
```

No license file was found, but licenses were detected in source scan.

```
# Copyright 2001 Gareth Rees. All rights reserved.
# Copyright 2004-2012 Ned Batchelder. All rights reserved.
# Redistribution and use in source and binary forms, with or without
# modification, are permitted provided that the following conditions are
# 1. Redistributions of source code must retain the above copyright
# notice, this list of conditions and the following disclaimer.
# 2. Redistributions in binary form must reproduce the above copyright
# notice, this list of conditions and the following disclaimer in the
# documentation and/or other materials provided with the
```

Found in path(s):

```
*/opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/coverage/__init__.py
```

No license file was found, but licenses were detected in source scan.

License :: OSI Approved :: BSD License

Found in path(s):

\* /opt/cola/permits/1230279813\_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/setup.py

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Metadata-Version: 1.1

Name: coverage

Version: 3.6

Summary: Code coverage measurement for Python

Home-page: <http://nedbatchelder.com/code/coverage>

Author: Ned Batchelder and others

Author-email: [ned@nedbatchelder.com](mailto:ned@nedbatchelder.com)

License: BSD

Description: Coverage.py measures code coverage, typically during test execution. It uses the code analysis tools and tracing hooks provided in the Python standard library to determine which lines are executable, and which have been executed.

Coverage.py runs on Pythons 2.3 through 3.3, and PyPy 1.9.

Documentation is at [nedbatchelder.com](http://nedbatchelder.com) <<http://nedbatchelder.com/code/coverage>>`. Code repository and issue

tracker are at [bitbucket.org](http://bitbucket.org) <<http://bitbucket.org/ned/coveragepy>>`.

New in 3.6: ``--fail-under``, and >20 bugs closed.

New in 3.5: Branch coverage exclusions, keyboard shortcuts in HTML report.

New in 3.4: Better control over source to measure, and unexecuted files can be reported.

New in 3.3: `.coveragerc` files.

New in 3.2: Branch coverage!

Keywords: code coverage testing

Platform: UNKNOWN

Classifier: Environment :: Console

Classifier: Intended Audience :: Developers

Classifier: License :: OSI Approved :: BSD License

Classifier: Operating System :: OS Independent

Classifier: Programming Language :: Python :: 2

Classifier: Programming Language :: Python :: 3

Classifier: Topic :: Software Development :: Quality Assurance

Classifier: Topic :: Software Development :: Testing

Classifier: Development Status :: 5 - Production/Stable

Found in path(s):

\* /opt/cola/permits/1230279813\_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/PKG-INFO  
\* /opt/cola/permits/1230279813\_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/coverage.egg-info/PKG-INFO

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/\*

\* jQuery Hotkeys Plugin  
\* Copyright 2010, John Resig  
\* Dual licensed under the MIT or GPL Version 2 licenses.  
\*  
\* Based upon the plugin by Tzury Bar Yochay:  
\* <http://github.com/tzuryby/hotkeys>  
\*  
\* Original idea by:  
\* Binny V A, [http://www.openjs.com/scripts/events/keyboard\\_shortcuts/](http://www.openjs.com/scripts/events/keyboard_shortcuts/)  
\*/

Found in path(s):

\* /opt/cola/permits/1230279813\_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/coverage/htmlfiles/jquery.hotkeys.js

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/\*\*

\* QUnit - A JavaScript Unit Testing Framework  
\*  
\* <http://docs.jquery.com/QUnit>  
\*  
\* Copyright (c) 2011 John Resig, Jörn Zaefferer  
\* Dual licensed under the MIT (MIT-LICENSE.txt)  
\* or GPL (GPL-LICENSE.txt) licenses.  
\*/

/\*\*

\* jsDump  
\* Copyright (c) 2008 Ariel Flesler - [aflesler\(at\)gmail\(dot\)com](mailto:aflesler(at)gmail(dot)com) | <http://flesler.blogspot.com>  
\* Licensed under BSD (<http://www.opensource.org/licenses/bsd-license.php>)  
\* Date: 5/15/2008  
\* @projectDescription Advanced and extensible data dumping for Javascript.  
\* @version 1.0.0  
\* @author Ariel Flesler  
\* @link {<http://flesler.blogspot.com/2008/05/jsdump-pretty-dump-of-any-javascript.html>}  
\*/

/\*

\* Javascript Diff Algorithm  
\* By John Resig (<http://ejohn.org/>)  
\* Modified by Chu Alan "sprite"  
\*  
\* Released under the MIT license.  
\*  
\* More Info:

```
* http://ejohn.org/projects/javascript-diff-algorithm/
*
* Usage: QUnit.diff(expected, actual)
*
* QUnit.diff("the quick brown
fox jumped over", "the quick fox jumps over") == "the quick <del>brown </del> fox <del>jumped
</del><ins>jumps </ins> over"
*/
```

Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/test/qunit/qunit.js
```

No license file was found, but licenses were detected in source scan.

```
/**
```

```
* QUnit - A JavaScript Unit Testing Framework
*
* http://docs.jquery.com/QUnit
*
* Copyright (c) 2011 John Resig, Jörn Zaefferer
* Dual licensed under the MIT (MIT-LICENSE.txt)
* or GPL (GPL-LICENSE.txt) licenses.
*/
```

Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/test/qunit/qunit.css
```

No license file was found, but licenses were detected in source scan.

```
/*
```

```
* jQuery Templates Plugin 1.0.0pre
* http://github.com/jquery/jquery-tmpl
* Requires jQuery 1.4.2
*
* Copyright Software Freedom Conservancy, Inc.
* Dual licensed under the MIT or GPL Version 2 licenses.
* http://jquery.org/license
*/
```

Found in path(s):

```
* /opt/cola/permits/1230279813_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/test/qunit/jquery.tmpl.min.js
```

No license file was found, but licenses were detected in source scan.

```
/*!
```

```
* jQuery JavaScript Library v1.4.3
* http://jquery.com/
*
* Copyright 2010, John Resig
* Dual licensed under the MIT or GPL Version 2 licenses.
* http://jquery.org/license
```

\*  
\* Includes Sizzle.js  
\* <http://sizzlejs.com/>  
\* Copyright 2010, The Dojo Foundation  
\* Released under the MIT, BSD, and GPL Licenses.  
\*  
\* Date: Thu Oct 14 23:10:06 2010 -0400  
\*/

Found in path(s):

\*/opt/cola/permits/1230279813\_1637354508.37/0/coverage-3-6-tar-gz/coverage-3.6/coverage/htmlfiles/jquery-1.4.3.min.js

# 1.298 libdwarf 20161124

## 1.298.1 Available under license :

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Version 2.1, February 1999

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/\*

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\*/

/\* dsc\_type: if 0, then dsc\_low is a single discriminant value  
and dsc\_high is zero..

If 1, then dsc\_low, dsc\_high are a discriminant range

All the messy complexity here is so we can have both  
a set of values read as uleb and as sleb.

We make our own copy of the block for the same reason.

\*/

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libdwarf.h

dwarf.h

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17 March 2014: Updated to

remove reference to dwarf.v2.mm.

## 1.299 websocket-client 0.59.0

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Includes `lib/servlet-api-2.4.jar` from Apache Tomcat

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# 1.307 python-requests 2.27.1

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## 1.308 icu 70

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---

layout: default  
title: Legalities  
parent: Release & Milestone Tasks  
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---

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`-nosvn -novc -noeclipse | xargs grep -i -L Copyright`

(See the `find_textfiles`

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See [svn changeset r39632](<https://github.com/unicode-org/icu/commit/0001f6c5e92f6f3a8d66c7dbc47cc24df7633a71>) for

an example; there should be only two files to update.

```
## *-makefile-*-
```

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```

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```

```
## Commands to generate dependency files
```

```
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
```

```
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)
```

```
## Flags for position independent code
```

```
SHAREDLIBCFLAGS = -fPIC
```

```
SHAREDLIBCXXFLAGS = -fPIC
```

```
SHAREDLIBCPPFLAGS = -DPIC
```

```
## Additional flags when building libraries and with threads
```

```
THREADSCPPFLAGS = -D_REENTRANT
```

```
LIBCPPFLAGS =
```

```
## Compiler switch to embed a runtime search path
```

```
LD_RPATH=
```

```
LD_RPATH_PRE= -Wl,-rpath,
```

```
## Compiler switch to embed a library name
```

```
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))
```

```
## Shared library options
```

```
LD_SOOPTIONS= -Wl,-Bsymbolic
```

```

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation
rules
%. $(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%. $(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s^\($*\)\.o[ :]*^1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s^\($*\)\.o[ :]*^1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@'

## Versioned libraries rules

%. $(SO).$(SO_TARGET_VERSION_MAJOR): %. $(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%. $(SO): %. $(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}. $(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =

```

STATIC\_PREFIX =

## End BSD-specific setup

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## 1.310 google-renameio 1.0.1

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## 1.311 go-jmespath 0.4.0

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# 1.312 dom 1.0

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Found in path(s):

```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
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```

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```

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```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ElementEditAS.java
```

```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASNotationDeclaration.java
```

```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASObject.java
```

```
*
```

```
/opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/CharacterDataEditAS.java
```

```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/DOMImplementationAS.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASModel.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/DocumentAS.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASAttributeDeclaration.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASElementDeclaration.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASEntityDeclaration.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASNamedObjectMap.java
*
/opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASDataType.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASContentModel.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/DOMASWriter.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/DocumentEditAS.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/ASObjectList.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/DOMASBuilder.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/DOMASException.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom3/as/NodeEditAS.java
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```

```

*/
/**
 * Checks if this content model has had its min/maxOccurs values reduced for
 * purposes of speeding up UPA. If
so, this content model should not be used
 * for any purpose other than checking unique particle attribution
 *
 * @return a boolean that says whether this content has been compacted for UPA
 */

```

Found in path(s):

```

*/opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xs/models/XSCMValidator.java

```

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```

```

/**
 * DOM Level 3 LS CR - Experimental.
 * Create a new LSParser. The newly constructed parser may
 *
then be configured by means of its DOMConfiguration
 * object, and used to parse documents by means of its parse
 * method.
 * @param mode The mode argument is either
 * MODE_SYNCHRONOUS or MODE_ASYNCHRONOUS, if
 * mode is MODE_SYNCHRONOUS then the
 * LSParser that is created will operate in synchronous
 * mode, if it's MODE_ASYNCHRONOUS then the
 * LSParser that is created will operate in asynchronous
 * mode.
 * @param schemaType An absolute URI representing the type of the schema
 * language used during the load of a Document using the
 * newly created LSParser. Note that no lexical checking

```

- \* is done on the absolute URI. In order to create a
- \* `LSParser` for any kind of schema types (i.e. the
- \* `LSParser` will be free to use any schema

found), use the value

- \* `null`.
- \* `<p><b>Note:</b> For W3C XML Schema [XML Schema Part 1</a>\]`
- \* , applications must use the value
- \* `"http://www.w3.org/2001/XMLSchema"`. For XML DTD [[XML 1.0</a>\],](http://www.w3.org/TR/2000/REC-xml-20001006/)
- \* applications must use the value
- \* `"http://www.w3.org/TR/REC-xml"`. Other Schema languages
- \* are outside the scope of the W3C and therefore should recommend an
- \* absolute URI in order to use this method.
- \* `@return` The newly created `LSParser` object. This
- \* `LSParser` is either synchronous or asynchronous
- \* depending on the value of the `mode` argument.
- \* `<p><b>Note:</b> By default, the newly created LSParser`
- \* does not contain a `DOMErrorHandler`, i.e. the value of
- \* the "[parameter-error-handler](http://www.w3.org/TR/2003/WD-DOM-Level-3-Core-20030609/core.html#parameter-error-handler)"
- \* `error-handler` configuration parameter is `null`. However, implementations
- \* may provide a default error handler at creation time. In that case,
- \* the initial value of the `"error-handler"` configuration
- \* parameter on the new created `LSParser` contains a
- \* reference to the default error handler.
- \* `@exception DOMException`
- \* `NOT_SUPPORTED_ERR`: Raised if the requested mode or schema type is
- \* not supported.

\*/

Found in path(s):

- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom/CoreDOMImplementationImpl.java

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- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/traversers/XSDocumentInfo.java
- \*
- /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/traversers/XSDElementTraverser.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/xni/parser/XMLErrorHandler.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/html/dom/HTMLFrameElementImpl.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/util/XMLEntityDescriptionImpl.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/util/ShadowedSymbolTable.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/util/ObjectListImpl.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/dom/WMLImgElementImpl.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/WMLOptgroupElement.java
- \*
- /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/xni/parser/XMLInputSource.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/WMLImgElement.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom/DeferredCDATASectionImpl.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/traversers/XSDAbstractIDConstraintTraverser.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/dom/WMLTimerElementImpl.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/xs/XSParticle.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/dom/WMLWmlElementImpl.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/dv/xs/AnySimpleDV.java
- \*
- /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/xs/StringList.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/WMLTimerElement.java
- \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-

jar/org/apache/xerces/impl/xpath/regex/CaseInsensitiveMap.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/xs/opti/SchemaDOMImplementation.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/xs/opti/DefaultXMLDocumentHandler.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/dom/DeferredElementImpl.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/wml/WMLAnchorElement.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xml/serialize/HTMLSerializer.java  
\*  
/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/html/dom/HTMLHtmlElementImpl.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/stax/DefaultNamespaceContext.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/xinclude/MultipleScopeNamespaceSupport.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/xs/opti/SchemaParsingConfig.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/dtd/XML11NSDTDValidator.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/xpointer/XPointerMessageFormatter.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/xs/XSException.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/wml/dom/WMLMetaElementImpl.java  
\*  
/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/html/dom/HTMLQuoteElementImpl.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/util/EncodingMap.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/xinclude/XIncludeTextReader.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/dtd/models/CMStateSet.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/dv/xs/Base64BinaryDV.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/jaxp/validation/DraconianErrorHandler.java

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jar/org/apache/xerces/parsers/SoftReferenceSymbolTableConfiguration.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/dtd/BalancedDTDGrammar.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/xpointer/ShortHandPointer.java  
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/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/xni/parser/XMLParserConfiguration.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/parsers/BasicParserConfiguration.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/html/dom/HTMLPreElementImpl.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
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* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/wml/WMLSelectElement.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/xni/XMLString.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/jaxp/validation/XMLSchemaValidatorComponentManager.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/html/dom/HTMLTableRowElementImpl.java
*
/opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/html/dom/HTMLLOListElementImpl.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xs/opti/SchemaDOM.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/XMLDTDSscannerImpl.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/html/dom/HTMLInputElementImpl.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
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* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/jaxp/validation/DOMDocumentHandler.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/xni/parser/XMLPullParserConfiguration.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/parsers/SecureProcessingConfiguration.java
*
/opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/xni/parser/XMLDocumentFilter.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xs/util/XSObjectListImpl.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/dom/NamedNodeMapImpl.java
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/xs/XSFacet.java

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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/dom/WMLGoElementImpl.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/traversers/XSDAbstractTraverser.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xml/serialize/Printer.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/dtd/XML11DTDProcessor.java

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/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/dtd/XMLDTDDescription.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/dom/WMLRefreshElementImpl.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xpath/regex/ParserForXMLSchema.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom/DOMNormalizer.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/dv/xs/IntegerDV.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/jaxp/DocumentBuilderFactoryImpl.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/html/dom/HTMLIsIndexElementImpl.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/dtd/models/SimpleContentModel.java

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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/xs/XSSimpleTypeDefinition.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/stax/EmptyLocation.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xml/serialize/SerializerFactory.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/util/HTTPInputSource.java

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/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/stax/events/CharactersImpl.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/dom/DeferredDOMImplementationImpl.java

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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
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jar/org/apache/xerces/impl/dv/xs/SchemaDateTimeException.java



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 \* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
 jar/org/apache/wml/dom/WMLFieldsetElementImpl.java

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/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/wml/dom/WMLDoElementImpl.java  
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\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/xni/parser/XMLConfigurationException.java  
\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/util/AttributesProxy.java  
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/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-jar/org/apache/xerces/impl/xs/SchemaNamespaceSupport.java

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jar/org/apache/xerces/dom/DOMImplementationListImpl.java

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/\*\*

\* used to check the 3 constraints against each complex type

\* (should be each model group):

\* Unique Particle

Attribution, Particle Derivation (Restriction),

\* Element Declarations Consistent.

\*/

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jar/org/apache/xerces/impl/dv/xs/AbstractDateTimeDV.java

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jar/org/apache/xerces/impl/xs/traversers/XSDUniqueOrKeyTraverser.java

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jar/org/apache/xerces/impl/dv/xs/DurationDV.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/dv/xs/MonthDayDV.java

\*

/opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/dv/xs/TimeDV.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/xs/traversers/XSAttributeChecker.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/dv/xs/YearMonthDV.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/xs/XSModelGroupImpl.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/dv/xs/XSSimpleTypeDecl.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/xs/traversers/XSDHandler.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/dv/xs/DayDV.java

\* /opt/cola/permits/1257218116\_1642790294.82/0/xercesimpl-2-12-0-sources-  
jar/org/apache/xerces/impl/xs/XMLSchemaValidator.java

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```

Found in path(s):

```
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XMLSchemaMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xpath/regex/message.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/DOMMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XPointerMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/DatatypeMessages.properties
*
/opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xpath/regex/message_fr.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/SAXMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XIncludeMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XMLMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/XMLSerializerMessages.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/xpath/regex/message_ja.properties
* /opt/cola/permits/1257218116_1642790294.82/0/xercesimpl-2-12-0-sources-
jar/org/apache/xerces/impl/msg/JAXPValidationMessages.properties
```

## 1.313 constantine 0.7

### 1.313.1 Available under license :

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```
<project xmlns="http://maven.apache.org/POM/4.0.0" xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"
xsi:schemaLocation="http://maven.apache.org/POM/4.0.0 http://maven.apache.org/maven-v4_0_0.xsd">
<modelVersion>4.0.0</modelVersion>
<groupId>org.jruby.extras</groupId>
<artifactId>constantine</artifactId>
<packaging>jar</packaging>
<version>0.7</version>
<name>constantine</name>
<description>A set of platform constants (e.g. errno values)</description>
<url>http://constantine.kenai.com</url>
<licenses>
<license>
```

```

    <name>The MIT License</name>
    <url>http://www.opensource.org/licenses/mit-license.php</url>
    <distribution>repo</distribution>
  </license>
</licenses>
<scm>
  <connection>scm:hg:https://kenai.com/hg/constantine~mercurial</connection>
  <url>https://kenai.com/hg/constantine~mercurial</url>
</scm>
<distributionManagement>
  <repository>
    <id>codehaus-jruby-repository</id>
    <name>JRuby Central Repository</name>
    <url>dav:https://dav.codehaus.org/repository/jruby</url>
  </repository>
  <snapshotRepository>
    <id>codehaus-jruby-snapshot-repository</id>
    <name>JRuby Central Development Repository</name>
    <url>dav:https://dav.codehaus.org/snapshots.repository/jruby</url>
  </snapshotRepository>
</distributionManagement>
<repositories>
  <repository>
    <id>maven2-repository.dev.java.net</id>
    <name>Java.net Repository for Maven</name>
    <url>http://download.java.net/maven/2/</url>
  </repository>
  <repository>
    <id>codehaus</id>
    <name>Codehaus Repository</name>
    <releases>
      <enabled>true</enabled>
    </releases>
    <snapshots>
      <enabled>>false</enabled>
    </snapshots>
    <url>http://repository.codehaus.org</url>
  </repository>
</repositories>

<developers>
  <developer>
    <id>wmeissner</id>
    <name>Wayne Meissner</name>
    <email>wmeissner@gmail.com</email>
  </developer>
</developers>

```

```

<dependencies>
  <dependency>
    <groupId>junit</groupId>
    <artifactId>junit</artifactId>
    <version>4.5</version>
    <scope>test</scope>
  </dependency>
</dependencies>
<build>
  <directory>build</directory>
  <outputDirectory>build/classes</outputDirectory>
  <testOutputDirectory>build/test/classes</testOutputDirectory>
  <sourceDirectory>src</sourceDirectory>
  <testSourceDirectory>test</testSourceDirectory>
  <resources>
  </resources>
  <plugins>
    <plugin>
      <groupId>org.apache.maven.plugins</groupId>
      <artifactId>maven-compiler-plugin</artifactId>
      <version>2.0.2</version>
      <configuration>
        <source>1.5</source>
        <target>1.5</target>
      </configuration>
    </plugin>
  </plugins>
  <extensions>
    <extension>
      <groupId>org.jvnet.wagon-svn</groupId>
      <artifactId>wagon-svn</artifactId>

      <version>1.8</version>
    </extension>
    <extension>
      <groupId>org.apache.maven.wagon</groupId>
      <artifactId>wagon-webdav</artifactId>
    </extension>
  </extensions>
</build>
<reporting>
  <outputDirectory>build/report</outputDirectory>
</reporting>
</project>

```

Found in path(s):

\* /opt/cola/permits/1257263874\_1672980538.3560572/0/constantine-0-7-jar-zip/constantine-0-7-jar/META-INF/maven/org.jruby.extras/constantine/pom.xml



# 1.314 jaffl 0.5.1

## 1.314.1 Available under license :

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```
<project xmlns="http://maven.apache.org/POM/4.0.0" xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"
xsi:schemaLocation="http://maven.apache.org/POM/4.0.0 http://maven.apache.org/maven-v4_0_0.xsd">
  <modelVersion>4.0.0</modelVersion>
  <groupId>org.jruby.extras</groupId>
  <artifactId>jaffl</artifactId>
  <packaging>jar</packaging>
  <version>0.5.12</version>
  <name>jaffl</name>
  <description>An abstracted interface to invoking native functions from java</description>
  <url>http://github.com/wmeissner/jaffl</url>
  <licenses>
    <license>
      <name>The Apache Software License, Version 2.0</name>
      <url>http://www.apache.org/licenses/LICENSE-2.0.txt</url>
      <distribution>repo</distribution>
    </license>
  </licenses>
  <scm>
    <connection>scm:git:http://github.com/wmeissner/jaffl.git</connection>
    <url>http://github.com/wmeissner/jaffl</url>
  </scm>
  <distributionManagement>
    <repository>
      <id>codehaus-jruby-repository</id>
      <name>JRuby Central Repository</name>
      <url>dav:https://dav.codehaus.org/repository/jruby</url>
    </repository>
    <snapshotRepository>
      <id>codehaus-jruby-snapshot-repository</id>
      <name>JRuby Central Development Repository</name>
      <url>dav:https://dav.codehaus.org/snapshots.repository/jruby</url>
    </snapshotRepository>
  </distributionManagement>
  <repositories>
    <repository>
      <id>maven2-repository.dev.java.net</id>
      <name>Java.net Repository for Maven</name>
      <url>http://download.java.net/maven/2/</url>
    </repository>
    <repository>
      <id>codehaus</id>
```

```

<name>Codehaus Repository</name>
<releases>
  <enabled>true</enabled>
</releases>
<snapshots>
  <enabled>true</enabled>
</snapshots>
<url>http://repository.codehaus.org</url>
</repository>
</repositories>
<pluginRepositories>
  <pluginRepository>
    <id>maven2-repository.dev.java.net</id>
    <name>Java.net Repository for Maven</name>
    <url>http://download.java.net/maven/2/</url>
  </pluginRepository>
</pluginRepositories>
<developers>
  <developer>
    <id>wmeissner</id>
    <name>Wayne Meissner</name>
    <email>wmeissner@gmail.com</email>
  </developer>
</developers>
<properties>
  <maven.test.failure.ignore>true</maven.test.failure.ignore>
</properties>
<dependencies>
  <dependency>
    <groupId>junit</groupId>
    <artifactId>junit</artifactId>
    <version>4.5</version>
    <scope>test</scope>
  </dependency>
  <dependency>
    <groupId>org.jruby.extras</groupId>
    <artifactId>jffi</artifactId>
    <version>1.0.6</version>
    <scope>compile</scope>
  </dependency>
  <dependency>
    <groupId>org.jruby.extras</groupId>
    <artifactId>jffi</artifactId>
    <version>1.0.6</version>
    <scope>runtime</scope>
    <classifier>native</classifier>
  </dependency>
</dependencies>

```

```

    <groupId>asm</groupId>
    <artifactId>asm</artifactId>
    <version>3.2</version>
    <scope>compile</scope>
</dependency>
<dependency>
    <groupId>asm</groupId>
    <artifactId>asm-commons</artifactId>
    <version>3.2</version>
    <scope>compile</scope>
</dependency>
<dependency>
    <groupId>org.jruby.extras</groupId>
    <artifactId>jnr-x86asm</artifactId>
    <version>1.0.0</version>
    <scope>compile</scope>
</dependency>
</dependencies>
<build>
    <sourceDirectory>src</sourceDirectory>
    <testSourceDirectory>test</testSourceDirectory>
    <plugins>
    <plugin>
        <groupId>org.apache.maven.plugins</groupId>
        <artifactId>maven-compiler-plugin</artifactId>
        <version>2.0.2</version>
        <configuration>
            <source>1.5</source>
            <target>1.5</target>
        </configuration>
    </plugin>
    <plugin>
        <artifactId>maven-antrun-plugin</artifactId>
        <version>1.1</version>
        <executions>
            <execution>
                <phase>test-compile</phase>
                <configuration>
                    <tasks>
                        <exec dir="{basedir}" executable="make" failonerror="true">
                            <arg line="-f libtest/GNUMakefile"/>
                            <arg line="BUILD_DIR={build.directory}"/>
                        </exec>
                    </tasks>
                </configuration>
            </execution>
        </executions>
        <goals>
            <goal>run</goal>
        </goals>
    </plugin>
    </plugins>
</build>

```

```

    </execution>
  </executions>
</plugin>
<plugin>
  <groupId>org.apache.maven.plugins</groupId>
  <artifactId>maven-surefire-plugin</artifactId>
  <version>2.4.2</version>
  <configuration>
    <systemProperties>
      <property>
        <name>jna.library.path</name>
        <value>${build.directory}</value>
      </property>
    </systemProperties>
  </configuration>
</plugin>
</plugins>
<extensions>
  <extension>
    <groupId>org.jvnet.wagon-svn</groupId>
    <artifactId>wagon-svn</artifactId>
    <version>1.8</version>
  </extension>
  <extension>
    <groupId>org.apache.maven.wagon</groupId>
    <artifactId>wagon-webdav</artifactId>
  </extension>
</extensions>
</build>
<reporting>
  <outputDirectory>build/report</outputDirectory>
</reporting>
</project>

```

Found in path(s):

```
* /opt/cola/permits/1257263866_1675746964.5826902/0/jaffl-0-5-12-jar/META-INF/maven/org.jruby.extras/jaffl/pom.xml
```

## 1.315 jnr-netdb 0.4

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Version 3, 29 June 2007

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```
* /opt/cola/permits/1257784248_1643014186.5/0/stateless4j-2-5-0-sources-  
jar/com/github/oxo42/stateless4j/delegates/FuncBoolean.java  
* /opt/cola/permits/1257784248_1643014186.5/0/stateless4j-2-5-0-sources-  
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<article id="copyright_test" last-revision="DEBUG MODE Date: 2000/12/20 12:00:00 $"
xmlns:xi="http://www.w3.org/2001/XInclude">
  <title>Copyright Test</title>
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Aleksey Gurtovoy (agurtovoy@meta-comm.com)

Andrei Alexandrescu (andrewalex - at - hotmail.com) (See Boost list message of August 12, 2004 11:06:58 AM  
EST)

Andrew Lumsdaine ()

Anthony Williams (anthony -at- justsoftwaresolutions.co.uk)

Beman Dawes (bdawes@acm.org)

Brad King (brad.king -at- kitware.com) (See Boost list message of Wed, 21 Jul 2004 11:15:46 -0400)

Brian Osman (osman -at- vvisions.com) (See CVS log)

Bruce Barr (schmoost -at- yahoo.com) (See Boost list of Mon, 16 Aug 2004 15:06:43 -0500)

Bruno da Silva de Oliveira (bruno - at - esss.com.br)

Christain Engstrom (christian.engstrom -at- glindra.org) (See Boost list message of Mon, 30 Aug 2004 14:31:49  
+0200)

Cromwell D Enage (sponage -at- yahoo.com) (See Boost list message of August 12, 2004 11:49:13 AM EST)

Dan Gohman (djg

-at- cray.com) (See Boost list message of Sat, 21 Aug 2004 10:54:59 +0100)

Dan Nuffer (dan -at- nuffer.name)

Daniel Frey (d.frey -at- gmx.de, daniel.frey -at- aixigo.de)

Daniel Nuffer (dan -at- nuffer.name)

Darin Adler (darin -at- bentspoon.com) (Email to Andreas Huber, see change log)

Daryle Walker (darylew - at - hotmail.com)

Dave Abrahams (dave@boost-consulting.com)

Dave Moore (dmoore -at- viefinancial.com) (See Boost list message of 18 Dec 2003 15:35:50 -0500)

David Abrahams (dave@boost-consulting.com)

Dietmar Kuehl (dietmar\_kuehl -at- yahoo.com) (Email to Andreas Huber, see change log)

Douglas Gregor (gregod -at- cs.rpi.edu, dgregor -at- cs.indiana.edu, doug.gregor -at- gmail.com)  
Dr John Maddock (john - at - johnmaddock.co.uk)  
Edward D. Brey (brey -at- ductape.net) (Email to Andreas Huber, see change log)  
Eric Ford (un5o6n902 -at- sneakemail.com) (See Boost list message of Sun, 15 Aug 2004 10:29:13 +0100)  
Eric Friedman (ebf@users.sourceforge.net)  
Eric Niebler (eric@boost-consulting.com)  
Fernando  
Cacciola (fernando\_cacciola@ciudad.com.ar)  
Fernando Luis Cacciola Carballal (fernando\_cacciola@ciudad.com.ar)  
Francois Faure (Francois.Faure -at- imag.fr) (See CVS log)  
Gary Powell (powellg - at - amazon.com) (See Boost list message of 10 Feb 2004 14:22:46 -0800)  
Gennadiy Rozental (rogeeff -at- mail.com) (Email to Andreas Huber, see change log)  
Gottfried Ganssaue (Gottfried.Ganssaue -at- HAUFE.DE) (See Boost List message of Mon, 16 Aug 2004 10:09:19 +0200)  
Gottfried Ganauge (Gottfried.Ganssaue -at- HAUFE.DE) (Alternative spelling of Gottfried Ganssaue)  
Greg Colvin (gregory.colvin -at- oracle.com) (See Boost list message of Sat, 14 Aug 2004 10:57:00 +0100)  
Gregory Colvin (gregory.colvin -at- oracle.com) (See Boost list message of Sat, 14 Aug 2004 10:57:00 +0100)  
Gunter Winkler (gunter.winkler -at- unibw-muenchen.de) (See Boost List message of Mon, 16 Aug 2004 10:24:17 +0200)  
Hartmut Kaiser (hartmut.kaiser -at- gmail.com)  
Herve Bronnimann (hbr -at- poly.edu)  
Herv  
Brnnimann (hbr -at- poly.edu)  
Housemarque Oy (Ilari Kuittinen ilari.kuittinen -at- housemarque.fi)  
Howard Hinnant (hinnant -at- twcnny.rr.com) (See Boost list message of July 25, 2004 3:44:49 PM EST)  
Hubert Holin (hubert\_holin -at- users.sourceforge.net)  
Indiana University ()  
Itay Maman (imaman -at- users.sourceforge.net)  
Jaakko Jrv (jajarvi -at- osl.iu.edu)  
Jaap Suter (j.suter -at- student.utwente.nl) (See Boost list message of Thu, 16 Sep 2004 09:32:43 -0700)  
Jeff Garland (jeff - at - crystalclearsoftware.com) (see Boost list post of July 25, 2004 19:31:09 -0700)  
Jens Maurer (Jens.Maurer@gmx.net)  
Jeremy G Siek (jsiek@osl.iu.edu)  
Jeremy Siek (jsiek@osl.iu.edu)  
Joel de Guzman (joel -at- boost-consulting.com) (See Boost list message of July 25, 2004 8:32:00 PM EST)  
John Bandela (jbandela-at-ufl.edu)  
John Maddock (john - at - johnmaddock.co.uk)  
John R Bandela (jbandela-at-ufl.edu)  
Jonathan Turkanis (turkanis -at- coderage dot com)  
Juergen Hunold (hunold -at- ive.uni-hannover.de) (See Boost  
List Message of Fri, 13 Aug 2004 19:39:55 +0200)  
Kevlin Henney (kevin -at- curbralan.com) (See Boost list message of Wed, 15 Sep 2004 18:15:17 +0200)  
Kresimir Fresl (fresl -at- master.grad.hr) (See Boost List message of August 16, 2004 8:23:35 AM EST)  
Lars Gullik Bijnnes (larsbj -at- lyx.org) (See Boost list message of Tue, 17 Aug 2004 15:49:02 +0100)  
Lie-Quan Lee (liequan - at - slac.stanford.edu, llee - at - cs.indiana.edu)  
Maarten Keijzer (mkeijzer -at- cs.vu.nl) (See Boost list message of Wed, 18 Aug 2004 21:43:18 +0100)  
Mac Murrett (mmurrett -at- mac.com)  
Marc Wintermantel (wintermantel -at- imes.mavt.ethz.ch, wintermantel -at- even-ag.ch) (See CVS log)

Michael Glassford (glassfordm - at - hotmail.com)  
Michael Stevens (Michael.Stevens - at - epost.de)  
Multi Media Ltd. (pdimov@mmltd.net)  
Nicolai M Josuttis (solutions -at- josuttis.com) (See Boost list message of Mon, 30 Aug 2004 10:52:00 +0100)  
Nikolay Mladenov (nickm -at- sitius.com) (See Boost list message of Tue, 17 Aug 2004 15:45:33 +0100)  
Paul Mensonides (pmenso57 -at- comcast.net) (See Boost list message of July 21, 2004 1:12:21 AM EST)  
Pavol Droba (droba -at- topmail.sk)  
Peter Dimov (pdimov@mmltd.net)  
R W Grosse-Kunstleve (RWGrosse-Kunstleve@lbl.gov)  
Ralf W. Grosse-Kunstleve (RWGrosse-Kunstleve@lbl.gov)  
Rational Discovery LLC (Greg Landrum Landrum -at- RationalDiscovery.com) (See Boost list post of Tue, 17 Aug 2004 10:35:36 +0100)  
Rene Rivera (grafik/redshift-software.com, rriviera/acm.org)  
Robert Ramey (ramey@www.rrsd.com)  
Roland Richter (roland -at- fill.jku.at) (See Boost list post of Mon, 16 Aug 2004 22:16:55 +0200)  
Roland Schwarz (roland.schwarz -at- chello.at)  
Ronald Garcia (garcia -at- cs.indiana.edu) (Email to Andreas Huber, see change log)  
Samuel Krempf (krempf -at- crans.ens-cachan.fr) (See Boost list message of Mon, 27 Sep 2004 13:18:36 +0200)  
Stefan Seefeld (seefeld -at- sympatico.ca)  
Stephen Cleary (scleary -at- jerviswebb.com) (See Boost list message of Tue, 28 Sep 2004 13:11:46 +0100)  
Steve  
Cleary (Variant of Stephen Cleary)  
Sylvain Pion (Sylvain.Pion - at - sophia.inria.fr)  
The Trustees of Indiana University ()  
Thomas Witt (witt - at - ive.uni-hannover.de, witt - at - acm.org, witt - at - styleadvisor.com)  
Thorsten Jrgen Ottosen (nesotto - at - cs.auc.dk)  
Thorsten Ottosen (nesotto - at - cs.auc.dk)  
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Vladimir Prus (ghost@cs.msu.su)  
William E. Kempf () (email to Beman Dawes, 9/14/2006 4:18 PM)  
Joerg Walter (jhr.walter - at - t-online.de : email to ublas mailing list Mon, 17 Sep 2007 10:17:08 +0200)  
Mathias Koch (mkoch - at - idesis.de 7 : email to boost-owner@lists.boost.org Sep 2007 13:20:09 +0200)

--- end ---

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////

[#mpl]

# MPL Support, <boost/mp11/mpl.hpp>

:toc:  
:toc-title:  
:idprefix:

The header `<boost/mp11/mpl.hpp>`, when included, defines the necessary support infrastructure for `mp_list` and `std::tuple` to be valid link:../../../../libs/mpl[MPL] sequences.

NOTE: `mpl.hpp` is not included by `<boost/mp11.hpp>`.  
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This is an attempt to acknowledge early contributions to the garbage collector. Later contributions should instead be mentioned in README.changes.

## HISTORY -

Early versions of this collector were developed as a part of research projects supported in part by the National Science Foundation and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for the Russell programming language implementation. The first version of the garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for



C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:

More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was

originally contributed by Mark Weiser.

The Encore Multimax modifications were supplied by

Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM.

Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood.

Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code.

Al Dosser (dosser@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk) subsequently provided updates and information on variation between ULTRIX systems. Parag Patel (parag@netcom.com) supplied the A/UX code.

Jesper Peterson(jep@mtiame.mtia.oz.au), Michel Schinz, and

Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port.

Thomas Funke (thf@zelator.in-berlin.de(?)) and

Brian D.Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports.

Douglas Steel (doug@wg.icl.co.uk) provided ICL DRS6000 code.

Bill Janssen (janssen@parc.xerox.com) supplied

the SunOS dynamic loader

specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and

Sony News specific code. Al Dosser provided Alpha/OSF/1 code. He and

Dave Detlefs(detlefs@src.dec.com) also provided several generic bug fixes.

Alistair G. Crooks(agg@uts.amdahl.com) supplied the NetBSD and 386BSD ports.

Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port.

Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to

a Motorola 88K processor running CX/UX (Harris NightHawk).

Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to

nonIBM development environments (a nontrivial task).

Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port.

David Chase, then at Olivetti Research, suggested several improvements.

Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the code to save and print call stacks for leak detection on a SPARC.

Jesse Hull and John Ellis supplied the C++ interface code.

Zhong Shao performed much of the

experimentation that led to the

current typed allocation facility. (His dynamic type inference code hasn't made it into the released version of the collector, yet.)

# 1.324 elfutils 0.170-1

## 1.324.1 Available under license :

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Version 3, 29 June 2007

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```
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- LZ4 source repository : <http://code.google.com/p/lz4/>

## 1.337 stunnel 5.57

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The original code is FirewallControl.pas, released April 16, 2007.

The initial developer of the original code is Rainer Budde (<http://www.speed-soft.de>).

SimpleFC - NSIS Firewall Plugin is written, published and maintained by Rainer Budde ([rainer@speed-soft.de](mailto:rainer@speed-soft.de)).

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## 1.338 zlib 1.2.12

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\* zlib.h -- interface of the 'zlib' general purpose compression library  
version 1.2.12, March 11th, 2022

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Jean-loup Gailly     Mark Adler  
jloup@gzip.org     madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950> (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

\*/

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## 1.341 fpconst 0.7.2

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-----  
fpconst: A Python module for handling IEEE 754 floating point special  
values  
-----

Author: Gregory R. Warnes <Gregory.R.Warnes@Pfizer.com>  
Date: 2005-02-24  
Version: 0.7.2  
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-----

This module provides 'fpconst' a reference implementation of Python  
Enhancement Protocol (PEP) 754, "IEEE 754 Floating Point Special  
Values". See the file pep-0754.txt or  
<http://www.python.org/peps/pep-0754.html> for the text of PEP 754.

## INSTALLATION

This is a standard Python source package. Consequently, simply unpack  
the source distribution, change into the directory, and run

```
$ Python setup.py install
```

## DOCUMENTATION

See the text of PEP  
754 at <http://www.python.org/peps/pep-0754.html>  
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""

```
from distutils.core import setup
```

```
url="http://www.analytics.washington.edu/statcomp/projects/rzope/fpconst/"
```

```
import
```

```
fpconst
```

```
setup(name="fpconst",  
      version=fpconst.__version__,  
      description="Utilities for handling IEEE 754 floating point special values",  
      author="Gregory Warnes",  
      author_email="Gregory.R.Warnes@Pfizer.com",  
      url = url,  
      long_description=fpconst.__doc__,  
      py_modules=['fpconst']  
    )
```

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\* /opt/cola/permits/1304941961\_1649692597.31/0/fpconst-zip/fpconst/setup.py

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Metadata-Version: 1.0

Name: fpconst

Version: 0.7.2

Summary: Utilities for handling IEEE 754 floating point special values

Home-page: <http://www.analytics.washington.edu/statcomp/projects/rzope/fpconst/>

Author: Gregory Warnes

Author-email: [Gregory.R.Warnes@Pfizer.com](mailto:Gregory.R.Warnes@Pfizer.com)

License: UNKNOWN

Description: Utilities for handling IEEE 754 floating point special values

This python module implements constants and functions for working with IEEE754 double-precision special values. It provides constants for Not-a-Number (NaN), Positive Infinity (PosInf), and Negative Infinity (NegInf), as well as functions to test for these values.

The code is implemented in pure python by taking advantage of the 'struct' standard module. Care has been taken to generate proper results on both big-endian and little-endian machines. Some efficiency could be gained by translating the core routines into C.

See <http://babbage.cs.qc.edu/courses/cs341/IEEE-754references.html> for reference material on the IEEE 754 floating point standard.

Further information on this package is available at <http://www.analytics.washington.edu/statcomp/projects/rzope/fpconst/>.

-----  
Author: Gregory R. Warnes <[Gregory.R.Warnes@Pfizer.com](mailto:Gregory.R.Warnes@Pfizer.com)>

Date: 2005-02-24

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Platform: UNKNOWN

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cnumpy.core.multiarray  
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recarray  
qKUbRq(KMcnumpy  
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tq}q(hhUi8KKRq(KU<NNNJJktbK(qh  
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hKqhhUO8KKRq(KU|NNNJJk?tbKqhhK qhhKquK8KKtb]q((cdatetime  
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QG@wG{JIKG@wG{tr(hURrG@wa\G@wQG@wIG@wQJ4fG@wQtr(hURrG@wzHG@xG33333G@wRG@x;  
QJPYG@x;Qtr (hURr  
G@x&=p  
G@x`G@wQG@xJMG@xtr(hURrG@x\$)G@x1p  
=G@wG@w\JKG@w\tr  
(hURrG@wG@wG@wsQG@wJd=G@wtr(hURrG@wzHG@w\G@wmp  
=G@w  
=qJ?G@w  
=qtr(hURrG@w=p  
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JD8G@w=p  
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SG@x~fffftr-(hU  
Rr.G@xffffG@yl\(\G@xG@yiQJ  
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=pJIG@z

=ptrY(hU

RrZG@z(\G@zG@z)G@zJdF7G@ztr[(hU

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=G@z\JAG@z\tr](hU

Rr^G@zDG@z\G@z33333G@zJ=p

JxOG@zJ=p

tr\_(hU

Rr`G@zo

=pG@zG@zG@z4)J[G@z4)\tra(hU

RrbG@zCzHG@zG@z9QG@z)\JG@z)\trc(hU

RrdG@|

=pG@|G@|Yp

=G@|QJG@|Qtre(hU

RrfG@|zGG@~J=p

G@|RG@~zGJyG@~zGtrg(hU

RrhG@}zGG@}\(G@}v\G@}\)J\$G@}\)tri(hU

RrjG@}

=pG@~G@}\(G@~iJ0G@~itrk(hU

RrlG@~zG{G@~\G@~C33333G@~QJKkG@~Qtrm(hU

RrnG@~>fffffG@~S

=pG@}

=pG@}33333JdG@}33333tro(hU

RrpG@}QG@~\G@}`\G@}QJ%dG@}Qtrq(hU

RrrG@}\)G@~"\G@}p

=G@}=p

JX\_G@}=p

trs(hURrtG@}\(G@}zGG@}\(G@}8J|RG@}8tru(hURrvG@}8G@}zHG@}&zGG@}^\(JOG@}^\(trw(hURrxG@

}zHG@}G@}\)G@}JJG@}|try(hURrzG@}QG@}\(G@}GzG@}33333J\*LG@}33333tr{(hURr|G@}33333G@}

QG@}|QG@}QJLJG@}Qtr}(hURr~G@}eG@~

=pG@}IG@}JHyG@}tr(hU RrG@}G@}

=pG@}|\(G@}zGJ`sJG@}zGtr(hU

RrG@}zGG@}QG@}d

=qG@}J\*G@}tr(hU

RrG@}fffffG@~QG@}=p

G@~zGJ@BG@~zGtr(hURrG@~33333G@~33333G@~G@~Jh8nG@~tr(hURrG@~G{G@=G@~G{G@~G{JG

@~G{tr(hURrG@~G@G{G@~)G@~fffffJMG@~ffffftr(hURrG@~G@:(G@~G@,

=qJXTG@,

=qtr(hURrG@&fffffG@&fffffG@~fffffG@~J1NG@~tr(hURrG@

=qG@G@~GzG@fffffJG@ffffftr(hURrG@QG@G@zGG@(\JDG@(\tr(hURrG@G@G@G@J)pG@tr(hURrG@U

RG@|zGG@~LG@~LJoG@~Ltr(hURrG@~zGG@~\G@}zGG@~J`vG@~tr(hURrG@~

=pG@~

=pG@~\$G@~JfffffJ\$}`G@~Jffffftr(hURrG@~C

=pG@~fffffG@~G@~L)JUG@~L)tr(hURrG@~\_zHG@~=p

G@}G@~JUG@~tr(hURrG@~0G@~vG{G@}G@~MJ<JG@~Mtr(hURrG@~vfffffG@~

=pG@~N=p

G@~pJX>G@~ptr(hURrG@~o(G@~fffffG@~HQG@~\JCG@~\tr(hURrG@~zHG@~G@~&QG@~\*=p

J+GG@~\*=p  
tr(hURrG@~  
=pG@~G@~G@~A\<G@~A\<tr(hURrG@~NQG@~ffffG@~<G@~>G{JIG@~>G{tr(hURrG@~=G@~e\<G@~  
~zGG@~zGJ?G@~zGtr(hU  
RrG@~K  
=pG@~XG@ }QG@ }  
=pJT#GG@ }  
=ptr(hURrG@~G@~<G@ } \<G@~!RJdvHG@~!Rtr(hURrG@~\*=p  
G@~A\<G@ }p  
=G@~J4OG@~tr(hURrG@~(\<G@~+  
=pG@|QG@|JRzG@|tr(hURrG@|QG@|T)\G@|G@ }JzGJxdG@ }JzGtr(hURrG@ }`G@ }xG@|GzG@|ffffJBG@|f  
fffftr(hURrG@ }G{G@ }G@|Ep  
=G@|33333JTjG@|33333tr(hURrG@|G@|=p  
G@|KzHG@|yGzJL<G@|yGztr(hURrG@|QG@|QG@|ip  
=G@|zGJG@|zGtr(hURrG@|G@ }AGzG@|G@ }@zGJL@G@ }@zGtr(hURrG@ }1RG@ }IGzG@|G@|\)J/G@|\)tr(  
hURrG@|G@ }QG@|\<G@|zHJ'G@|zHtr(hURrG@ }  
G@ } \<G@|\<G@ }9p  
=JuG@ }9p  
=tr(hURrG@ }PG@~?33333G@ }EG@~4\<JZxG@~4\<tr(hURrG@~(G@~xG@ } \<G@~s  
=pJ\$hG@~s  
=ptr(hURrG@~{  
=pG@~RG@~#33333G@~9GzJHG@~9Gztr(hU RrG@~W33333G@~G@~33333G@~XJRG@~Xtr(hU  
RrG@~FG{G@~G@~  
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=pG@~J|KG@~tr(hURrG@~G@~ffffG@ }G@~  
p  
=JvRG@~  
p  
=tr(hURrG@~  
=qG@~LG@ }  
=qG@ }J0GG@ }tr(hURrG@~G33333G@8  
=qG@~4  
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=J,aG@~p  
=tr(hURrG@~G{G@ffffG@~pzGG@~p  
=JSG@~p  
=tr(hURrG@  
G@,G@~G@~QJHG@~Qtr(hURrG@~G@ G@~QG@~QJ?G@~Qtr(hURrG@  
=pG@G@~(\<G@XJ@G@Xtr(hURrG@G@(\<G@~zGG@~JG@~tr(hURrG@~))(\<G@~PG@ } )G@~J\_G@~tr(hUR  
rG@ }G@ }G@ }#  
=pG@ }2\<JmG@ }2\<tr(hURrG@ }AG@ }G@ }4\<G@ }wzHJ4QG@ }wzHtr(hURrG@ }QG@ }G@ }LzGG@ }`(\<J>G  
@ }`(\<tr(hURrG@ }@G@ }G@ }ffffG@ }pzGJ4>G@ }pzGtr(hU RrG@ }zffffG@ }G{G@|G@|=p  
J"JG@|=p  
tr(hURrG@|G{G@|=p  
G@|pQG@|  
=qJWG@|

=qtr(hU  
RrG@|fffffG@|zGG@|\G@|J=G@|tr(hURrG@|G@ }RzGG@|QG@ }G{JVG@ }G{tr(hURrG@ }  
G@ }"zGG@|QG@|QJ=G@|Qtr(hURrG@|G@ }bfffffG@|\)G@ }\_  
=pJ@^G@ }\_  
=ptr(hURrG@ }GQG@ }G@ }|(G@ }J>G@ }tr(hURrG@ }|p  
=G@ }G{G@ };  
=pG@ }|(JVG@ }|(tr (hURr  
G@ }  
=pG@~C  
=pG@ }=p  
G@ }JWG@ }tr(hURrG@ }G@ }33333G@ }<G@ }iRjh>;G@ }iRtr  
(hURrG@ }GzG@ }G@|G@ }G{JI<G@ }G{tr(hURrG@|pG@|G@ {QG@|QJ G@|Qtr(hURrG@|&|(G@|ZQG@ {  
=qG@|33333J,zG@|33333tr(hURrG@ {QG@|FQG@ {G@|zHJG@|zHtr(hURrG@ {\(G@|33333G@ {jG{G@ {jG{J  
0udG@ {jG{tr(hURrG@ {PQG@ {G@ {PG@ {33333J'G@ {33333tr(hURrG@ {QG@|G@ {zGG@|JrG@|tr(hURrG@  
|  
=pG@|=p  
G@|d  
=qG@|z=p  
JcG@|z=p  
tr(hURrG@|QG@ }G@|aG@|kQJpQG@|kQtr(hU Rr  
G@|G@|fffffG@|!G@|O|(J\$KG@|O|(tr!(hURr"G@|IQG@|tG@|1|(G@|IJ4G@|ltr#(hU  
Rr\$G@|!(G@|>G{G@ {GzG@ {zGJTOaG@ {zGtr%(hURr&G@ {zHG@|  
\(G@ {pG@|JTzG@|tr'(hURr(G@ {\(G@|QG@ {  
=pG@ {  
=pJ/<G@ {  
=ptr(hURr\*G@ {fffffG@ {33333G@ {~=p  
G@ {JYVG@ {tr+(hURr,G@ {G@|G@ {zGG@ {zHJOOG@ {zHtr-(hURr.G@ {  
=qG@ {G@ {G@ {zGJ<54G@ {zGtr/(hURr0G@ {G@|QG@ {\(G@|JyXG@|tr1(hURr2G@|y|(G@|QG@|HzGG@|  
=qJ<VG@|  
=qtr3(hURr4G@|33333G@|=p  
G@|GzG@|GzJ>G@|Gztr5(hURr6G@|G@ }G@|yRG@ }JGG@ }tr7(hURr8G@|G@ }zHG@|p  
=G@|RJ9G@|Rtr9(hURr:G@|RG@ }  
=pG@|fffffG@|zGJFG@|zGtr;(hURr<G@ }G@ } G@|pG@|QJ<G@|Qtr=(hURr>G@|G@|fffffG@|=p  
G@|\(J3G@|\(tr?(hURr@G@|\(G@|zGG@|ARG@|zGJ4G@|zGtrA(hURrBG@ }G@ }G@ }G@ }J5cG@ }trC(hURr  
DG@ }=p  
G@ }G@ }YGzG@ }pQJ9G@ }pQtrE(hURrFG@ }tG@ }p  
=G@ }YRG@ }x(Jp)G@ }x(trG(hU RrHG@ }zHG@ }G@ }p  
=G@ }C(JT.G@ }C(trI(hU  
RrJG@ }1p  
=G@ }1  
=qG@ }|(G@ }|(Ju-G@ }|(trK(hURrLG@ } \)G@ }VfffffG@|\(G@ }zGJ\*:G@ }zGtrM(hURrNG@ }G@ }@G@|  
=pG@ }6=p  
JQ)G@ }6=p  
trO(hU  
RrPG@ }G33333G@ }LQG@|\(G@ }\$  
=qJ0\*G@ }\$  
=qtrQ(hURrRG@ }G|(G@ }  
=pG@ }BfffffG@ }QJ{MG@ }QtrS(hURrTG@ }G@ }=p

G@ }yG@ }Jt0G@ }trU(hURrVG@ }t(\G@ }ffffG@ }XzGG@ }(\JdVG@ }(\trW(hURrXG@ }G@~33333G@ }Yp  
=G@ }zffffJ@G@ }zfffftrY(hURrZG@~QG@~G@~  
QG@~z'HJG@~zHtr[(hURr\G@~G@~PG@ }(\G@ }GzJhVG@ }Gztr](hURr^G@ }(\G@ }zHG@ }G@ }zGJ`8G@  
}zGtr\_(hURr`G@~G@~RG@ }(\G@ }  
=pJP<G@ }  
=ptr(hURrbG@ }G@~G33333G@ }(\G@~G{J>G@~G{trc(hURrdG@~QG@~&ffffG@ }GzG@ }(\J,G@ }(\tre(h  
URrfG@ }ffffG@~G@ }vzGG@ }vzGJp7G@ }vzGtrg(hURrhG@ }  
=pG@ })\G@ }QG@ }PJ7G@ }Ptri(hURrjG@ }JffffG@ }qGzG@ }zHG@ }zGJ.G@ }zGtrk(hURrlG@ }#QG@ }QG  
@ }  
=qG@ }zHJ6G@ }zHtrm(hURrnG@ }aRG@ }p  
=G@ }zGG@ }qRJE<G@ }qRtro(hURrpG@ }=p  
G@ }QG@ }QG@ }4QJD.G@ }4Qtrq(hURrrG@ }"zGG@ }BQG@ }zHG@ },\,)JT,G@ },\,)trs(hU  
RrtG@ }"ffffG@ }{zHG@|zGG@ }TJZ;G@ }Ttru(hU  
RrvG@ }0  
=qG@ }W  
=pG@|QG@|QJ?8G@|Qtrw(hURrxG@|GzG@ }0G@|G@ }+  
=pJd,G@ }+  
=ptry(hURrzG@ }zHG@ }8(\G@|G@|zGJ;G@|zGtr{(hURr\G@ }(\G@|  
=qG@ }(\G@|J>G@|tr}(hURr~G@|G@ }=p  
G@|QG@ }(\JXdG@ }(\tr(hURrG@ }(\G@ }QG@ }l)\G@ }o(\JxGG@ }o(\tr(hURrG@ }zGG@ }33333G@ }(\G@ }eQJ  
e8G@ }eQtr(hURrG@ }XzGG@ }33333G@ }+QG@ }iJ]G@ }itr(hURrG@ }G@ }(\G@ }G@ }(\J:G@ }(\tr(hURrG@~  
QG@~6(\G@ }G@ }QJh6MG@ }Qtr(hURrG@ }ffffG@ }33333G@ }xG@ }GzJ  
?G@ }Gztr(hURrG@ }33333G@~O33333G@ }QG@~8QJQG@~8Qtr(hURrG@~PG@~G@~@G@~q(\JOG@~q(\(  
tr(hURrG@~HG@~p  
=G@~0G@)JnG@)tr(hURrG@H)\G@zGG@)\G@ }(\J,G@ }(\tr(hURrG@PG@QG@G{G@FffffJ:IG@Ffffftr(hUR  
rG@ }(\G@ }(\G@ }p  
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@ }(\G@2G@G@p  
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RrG@p  
=G@  
=qG@+  
=pG@  
=pJTkG@  
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J5^G@=p  
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G@#(\G@ }gRG@ }G@ }g  
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=ptr(hURrG@  
=pG@RG@[

=pG@{\(JxG@{\(tr(hURrG@  
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fG@Yp  
=G@G@@\)J/G@@\)tr(hURrG@8zGG@ @G@33333G@zGJ`G@zGtr(hURrG@G@5QG@\(\G@JUG@tr(hURr  
G@'  
=pG@(\G@)\)G@(\J(TG@(\tr(hURrG@  
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JTG@=p  
tr(hURrG@\(\G@:QG@G@  
G{Jy<G@ G{tr(hURrG@A  
=pG@CQG@G@J)AG@tr(hURrG@G@\$zGG@=p  
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\(\G@RG@G@)\J#0G@(\tr(hURrG@G@ G@pG@pJw0G@ptr(hURrG@pG@ffffG@hG@J7G@tr(hURrG@G@?  
=pG@  
=qG@ (\JIAG@ \tr(hURrG@:QG@n=p  
G@(QG@n=p  
J>G@n=p  
tr(hU RrG@FffffG@v\(\G@  
=pG@  
=pJIG@  
=ptr(hU  
RrG@G{G@5\(\G@zGG@JYG@tr(hU  
RrG@<QG@>G@=p  
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=pG@(\G@QG@(\J7G@(\tr(hURrG@(\G@"G@G@J1G@tr(hURrG@  
G@!  
=pG@G@QJ\G@Qtr (hURr  
G@G{G@ffffG@G@J%G@tr(hURrG@p  
=G@+G@ffffG@

zGJlz#G@  
zGtr  
(hURrG@zGG@  
=pG@  
=qG@ffffJ1G@fffftr(hURrG@p  
=G@  
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=ptr(hURrG@G{G@33333G@GzG@ffffJ4v(G@fffftr(hURrG@G@\$G@QG@J@o-G@tr(hU  
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G@`(\G@jffffJ!G@jfffftr)(hU Rr\*G@t(\G@G@b(\G@(\JS@G@(\tr+(hU Rr,G@(\G@+G@G{G@JtUTG@tr-  
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G@F  
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=p  
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G@}hG@}p  
=G@}5(\G@}=p  
JdG@}=p  
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G@{  
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G@|\(G@ }<\)JaEG@ }<\)trA(hU RrBG@ }RzGG@ }G@|  
=qG@ }  
=pJd\G@ }  
=ptrC(hU  
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=J@EG@ {p  
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=G@~p  
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JC5G@~+33333tr(hURrG@ } )G@~p  
G@ }w  
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G@ }=(tr(hURrG@ }ARG@ }(\G@|ffffG@|J%G@|tr(hURrG@ }9p  
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=tr(hURrG@`G@zGG@  
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RrG@YG@x  
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=qG@  
=pJ,G@  
=ptr(hURrG@  
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p  
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=G@}\(Jz2G@}\(tr(hURrG@}zHG@}GzG@}  
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G@}\G@}p(\G@|GzG@|  
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PDB2PQR

Main Authors (alphabetical):

Nathan A. Baker (baker@biochem.wustl.edu)  
Paul Czodrowski  
Todd Dolinsky (todd@ccb.wustl.edu)  
Jan H. Jensen  
Jens E. Nielsen

Additional contributing authors listed in the code documentation.

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P6

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Rr.G@M@G@MG@LG@MJ>IG@+Qtr/(hU  
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Rr2G@KG@KG@K G@KJzG@)fffftr3(hU  
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=ptr5(hU  
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=qJXOG@(zHtr7(hU  
Rr8G@K G@K0  
=qG@IG@JJIG@(aGztr9(hU  
  
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=qtr;(hU  
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Rr@G@J G@JG@GG@H JG@&zGtrA(hU  
RrBG@H G@H G@AG@B@J@fG@ 33333trC(hU  
RrDG@C@G@EG@@P  
=qG@A@J6G@trE(hU  
RrFG@C@G@EG@CG@D@J@##G@"=p  
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RrJG@AG@B@G@A G@AJ@IG@ =p  
=trK(hU  
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=ptrM(hU  
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RrPG@>G@@G@=@G@@JEG@zGtrQ(hU  
RrRG@A G@DG@@ G@CJG@"zGtrS(hU  
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G@B JG@ trY(hURrZG@AG@BG@A`G@BJ@G@ zG{tr[(hURr\G@B G@C`G@B  
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=p  
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=trg(hU  
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=p  
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=qJ G@"ffffftr(hU RrG@CG@CG@C@G@Cp  
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@D  
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=qJG@#Qtr(hURrG@DG@DG@D@G@D@J G@"p  
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tr# (hURr\$ G@A@G@AG@A G@AJ`AG@ =p  
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Rr0 G@C`G@CG@C G@C@JcG@!(\tr1 (hURr2 G@CG@CG@C G@C`JECG@!G{tr3 (hURr4  
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=ptr(hURrG@:@G@;G@:@G@:QJZ2G@tr(hURrG@;G@;G@:G@;@JEBG@\(\tr(hURrG@;G@<@G@;G@;Ju  
G@  
=ptr(hURrG@<@G@=@G@<G@<JuG@\(\tr(hURrG@=G@=@G@<G@=JP^`G@  
=p  
tr(hURrG@=@G@>G@=G@>@J`G@=p  
=tr(hURrG@>G@>G@=G@=J0lrG@Qtr(hURrG@=@G@>@G@=@G@=@JDG@GzHtr(hU  
RrG@=@G@>@G@=G@=JXG@\(\tr(hU  
RrG@=G@=G@<@G@<JfSG@\(\tr(hURrG@<G@<G@;G@;J[G@tr(hURrG@;G@;G@:G@:JG@  
=ptr(hU  
RrG@:G@;G@:@G@;^QJpKG@zG{tr(hURrG@;G@<G@;@G@;J7G@Qtr(hURrG@;G@<G@;@G@<^QJ;G

@\tr(hURrG@=G@=G@<@G@<JgG@zGtr(hURrG@<G@<G@;G@;JOSG@Qtr(hURrG@;G@<G@;G@<J6G  
@333333tr(hURrG@<G@<G@;G@<^QJ@1G@)\tr(hURrG@<G@<G@;G@<JR7G@333333tr(hURrG@<G@  
<@G@:G@;@Jp|OG@zG{tr(hURrG@;@G@;@G@:G@:QJ`G@Qtr(hURrG@;G@;G@:G@:JeG@\tr(hURrG  
@:G@:G@9QG@:JQG@=p  
=tr(hURrG@:G@:G@:G@:JEG@\tr(hU RrG@:G@:G@9G@:QJ{G@\tr(hU  
RrG@:G@:G@9@G@9JG@tr(hU RrG@:G@:G@9@G@9JpXG@tr(hU  
RrG@:G@;G@9G@:@JGNG@Qtr(hU RrG@:@G@;G@:G@:Jp{G@tr(hU RrG@:G@;G@:G@:J@QG@=p  
=tr(hU  
RrG@:@G@:@G@9^QG@:@JPIG@Qtr(hU  
RrG@:@G@:G@8G@9@J0G@Qtr(hU RrG@8@G@9G@8G@8@JG@\tr(hU RrG@8G@9G@7G@8J|G@tr(hU  
RrG@8@G@9G@8@G@8J|G@  
=p  
tr(hU RrG@8^QG@9G@8@G@9@J  
]G@Qtr(hU RrG@9@G@9G@8G@8QJ <G@(\tr(hU RrG@8G@9@G@7QG@8JPG@tr(hU  
RrG@8@G@9G@8@G@9Jo<G@\tr(hU RrG@9G@9G@8G@8JGG@  
=p  
tr(hU RrG@9G@9@G@8G@9J)G@GzHtr(hU RrG@9G@9@G@8@G@8J=G@  
=p  
tr(hU RrG@8G@9G@8@G@8J  
3G@  
=p  
tr(hU RrG@8@G@8QG@7G@7QJ\$G@333333tr(hU RrG@8G@8G@7G@7^QJG@Qtr(hU  
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Rr  
G@7G@8G@7G@7J:`G@  
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RrG@7G@8G@7^QG@7QJ\_G@\tr  
(hU  
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Rr G@<G@<@G@;@G@;JKG@\tr!(hU  
Rr"G@;G@?@G@;@G@>@JVG@fffftr#(hU  
Rr\$G@>G@?G@=G@>@JG@fffftr%(hU  
Rr&G@>@G@>G@=QG@>J wG@(\tr'(hU  
Rr(G@=G@>G@=G@=JuyG@Qtr)(hU  
Rr\*G@>G@@ G@=G@?JyG@  
=ptr+(hU  
Rr,G@?G@@ G@?G@?J PG@Qtr-(hU

Rr.G@?G@?G@>G@>JJG@Gztr/(hURr0G@>G@?G@>@G@?J9G@tr1(hURr2G@?@G@@G@?G@@`J  
GzG@tr3(hURr4G@@G@@G@?G@?QJo`G@Qtr5(hURr6G@?G@@ G@>G@@  
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Rr>G@>@G@>G@>G@>J  
3\*G@Gztr?(hURr@G@>G@@G@>G@?^QJMG@zG{trA(hURrBG@?G@@G@>G@?JaNG@  
=ptrC(hURrDG@?G@@`G@?G@@JUG@zGtrE(hURrFG@@G@A G@?G@AJ\_G@  
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GzHtrK(hURrLG@@G@@G@@@G@@JpGCG@ zGtrM(hURrNG@@`G@@G@@  
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G@?@G@?J0<G@Qtr[(hURr\\G@?G@@G@?G@?J&7G@\\(tr)(hURr^G@?G@@G@?G@?JAG@Qtr\_(hURr`G  
@?G@@@G@?@G@@ JUG@p  
=qtra(hURrbG@@G@@ G@?G@@ J"G@p  
=qtrc(hURrdG@@G@@ G@?G@?QJG@zGtre(hU RrfG@?G@@G@=G@>JcG@=p  
=trg(hU  
RrhG@>@G@>G@;G@<@JQG@\\(tri(hU  
RrjG@<@G@=G@;G@=Jp2G@\\(trk(hURrlG@=@G@=G@=G@=QJG@ffffftrm(hURrnG@=G@=G@=G@=J  
CG@tro(hURrpG@=G@=G@=G@=^QJ 'EG@  
=ptrq(hURrrG@=G@=G@=QG@=J  
OOG@\\(trs(hURrtG@=@G@=G@<@G@<JHgG@tru(hURrvG@<G@<G@;@G@;J  
G@trw(hURrxG@;@G@<G@;G@<J)cG@GzHtry(hURrzG@;@G@;@G@:G@;@J{G@\\(tr{(hURr|G@;G@<G  
@;@G@<JnWG@tr)(hURr~G@<G@=G@<G@=QJWG@ffffftr(hURrG@=@G@=@G@<G@<J:G@tr(hURrG  
@<G@>@G@<G@=JG@tr(hURrG@=G@>@G@=@G@=@JPWG@Qtr(hURrG@=G@>G@=G@=QJbG@Qtr  
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=qG@?G@@JMG@  
u\\(tr(hURrG@@G@AG@@@G@@`J G@Qtr(hURrG@@G@@G@?@G@@@  
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=qG@@`G@@@  
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G@@@  
=qJp,G@ GzHtr(hURrG@@G@AG@@G@AJpyG@!(tr(hURrG@A`G@AG@@G@@@  
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=ptr(hURrG@@G@A G@@G@A  
=qJ@HG@  
=ptr(hURrG@A G@A`G@@G@AJ@JG@  
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G@"tr(hURrG@B`G@BG@B G@B  
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B\tr(hURrG@@G@@G@@ G@@`JtG@  
Qtr(hURrG@@G@@G@@?G@@@J\G@tr(hURrG@@G@@G@@?G@@Jp'rG@ B\tr(hURrG@@  
G@@G@@?G@@J [G@ B\tr(hURrG@@G@A G@@G@@J]5G@ aGztr(hURrG@AG@AG@@`G@@JpFG@  
aGztr (hURr  
G@@G@@G@@`G@@  
=qJ0\*G@ W  
=ptr(hURrG@@G@AG@@G@@J`G@ aGztr  
(hURrG@@G@@G@@@G@@JW:G@ aGztr(hURrG@@@  
=qG@@  
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RrG@@ G@@@G@@?G@@?JG@zGtr(hURrG@>G@?G@>G@?J  
yG@  
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G@\tr(hURrG@=@G@>G@<G@<@J G@  
=ptr(hURrG@<G@>G@;G@=QJG@tr(hURr  
G@?@G@@G@<G@=JM|G@Qtr!(hURr"G@=G@?G@=G@?JG@QRtr#(hURr\$G@?G@?G@?G@?@JYG@\  
tr%(hURr&G@?G@?@G@=G@>@J6G@tr'(hURr(G@>G@>G@=G@>J

3G@(\tr)(hURr\*G@>G@?@G@>G@?J0(CG@QRtr+(hURr,G@?G@?@G@=G@>@JpHG@tr-  
(hURr.G@?G@ @G@>G@ @JG@ #  
=ptr/(hURr0G@ @G@ @G@ @ G@ @p  
=qJHG@ zGtr1(hURr2G@ @ G@ @ `G@?@G@ @ `zHJfG@tr3(hU Rr4G@ @ G@ @ @G@>G@?@J`LG@(\tr5(hU  
Rr6G@?G@ @G@?G@?JPPG@QRtr7(hURr8G@?G@?G@=G@>@JOG@tr9(hURr:G@>G@>G@=G@=  
=qJ:G@  
=p  
tr;(hU  
Rr<G@=G@>G@=@G@>J@2G@(\tr=(hURr>G@>G@>G@=G@=JJG@Gztr?(hURr@G@=G@=G@<G@=  
QJpnbG@QtrA(hURrBG@=G@>G@=@G@>QJ@CG@(\trC(hURrDG@>G@ @ @G@>G@ @  
=qJ.G@p  
=qtrE(hURrFG@?G@ @ G@?G@?(J5G@ffffftrG(hURrHG@?G@?@G@>G@>JgAG@  
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@?G@>@G@>J`G@  
=ptrO(hURrPG@>@G@>G@=G@=  
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VG@<^QG@<G@;QG@;^QJ`G@GztrW(hURrXG@;QG@<G@:G@;QJG@QtrY(hURrZG@;G@;G@;G@;^QJ  
DG@Gztr[(hURr\G@;@G@;G@;@G@;J`LG@tr](hURr^G@;G@;QG@:G@:QJ0G@  
=ptr\_(hU Rr`G@9QG@;G@9G@;JG@Qtra(hU  
RrbG@;QG@;^QG@:^QG@:JMG@  
=p  
trc(hU  
RrdG@:^QG@;0  
=qG@:^QG@;J2G@Qtre(hURrfG@;@G@;^QG@:QG@;)(J@fTG@trg(hURrhG@;G@<G@:QG@;)(J  
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=p  
trm(hURrnG@:@G@;@G@:G@;QJmG@  
=ptro(hURrpG@:QG@;@G@9G@:JG@(\trq(hURrrG@:@G@:G@:G@:J0F>G@trs(hURrtG@:@G@:@G@8  
QG@9QJ"oG@zGtru(hURrvG@9QG@:QG@8G@9(\JpG@(\trw(hURrxG@9@G@:@G@8QG@:@JG@try(hU  
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=ptr}(hURr~G@:@G@:QG@:@G@:J7G@  
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=p  
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=ptr(hURrG@9QG@:G@9G@:)(J  
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RrG@<G@>@G@<G@=  
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IG@Qtr(hURrG@?G@ @  
=qG@>QG@?QJG@QRtr(hURrG@?QG@AG@?QG@ @QJ .G@ =p  
tr(hURrG@ @

=qG@@G@@`G@@  
=qJp0)G@ ffffftr(hURrG@@G@@  
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RrG@@  
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=qG@A  
=qG@B  
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=qG@B  
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=qG@AJPUG@!Qtr(hU RrG@A  
=qG@AG@A  
=qG@AJ1G@!Qtr(hU  
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=qG@AzHJP8G@!  
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=qG@B  
=qG@A  
=qG@BJP1G@!p  
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=qG@BG@AG@B0  
=qJ|G@!  
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=qG@A  
=qG@A0  
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=qJ ;G@ 33333tr(hU RrG@AG@A0  
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=tr (hU

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WG@  
  
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=pG@2O(G@1@G@1(JXG@!(tr) (hURr\* G@0  
=qG@1G@0QG@0JXQG@ tr+ (hURr, G@1G@1G@0(G@1(JRG@!(tr- (hURr. G@10  
=qG@10  
=qG@0p  
=qG@0  
=qJ8G@ 33333tr/ (hURr0 G@0  
=qG@1p  
=qG@0^QG@1JNG@!tr1 (hURr2 G@-B(G@.G@,G@,QJ0G@  
=ptr3 (hURr4 G@,GzG@-(G@,G@,QJG@  
=ptr5 (hURr6 G@-QG@.QG@,GzG@.QJHG@Qtr7 (hURr8 G@.aGzG@.(G@-(G@.aGzJUG@(\tr9 (hURr:  
G@.G@0G@.G@.(JG@(\tr; (hU  
Rr< G@/QG@/QG@-(G@.J  
G@tr= (hURr> G@.(G@.G@,GzG@,GzJG@Gztr? (hURr@ G@-QG@-aGzG@,G@,QJ5G@QtrA (hURrB G@-  
QG@-B(G@+GzG@,JHG@zG{trC (hURrD G@,(G@.G@,G@,JG@trE (hURrF G@+(G@-  
B(G@+B(G@,(Jh+4G@(\trG (hURrH G@,G@.G@+(G@,QJG@QtrI (hURrJ G@,B(G@.G@,B(G@.J|G@trK  
(hURrL G@-(G@.G@,G@-aGzJx/vG@(\trM (hURrN G@,zGG@-QG@,aGzG@-QJfG@  
=ptrO (hURrP G@,(G@-GzG@,QG@-QJ0yG@  
=ptrQ (hURrR G@-aGzG@.G@-G@-(JWG@(\trS (hURrT G@-(G@.G@-QG@-(JG@(\trU (hURrV G@-G@0  
=qG@,GzG@0^QJhGG@ aGztrW (hURrX G@2#  
=pG@2G@0(G@1(JG@!(trY (hURrZ G@0  
=qG@1^QG@0(G@0^QJG@ aGztr[ (hURr\ G@0  
=qG@0G{G@/GzG@0(JpG@ \tr] (hU Rr^ G@0(G@1  
=pG@0(G@10  
=qJ  
AG@!333333tr\_ (hU  
Rr` G@0  
=qG@1G@0(G@0(J<G@ \tra (hURrb G@0@G@2G@0@G@2JG@"trc (hURrd G@1QG@2G@1(G@10  
=qJhG@!333333tre (hURrf G@1p  
=qG@2@G@1G@1QJ`G@!Qtrg (hURrh G@1(G@1(G@0G@0)(J`VG@ tri (hURrj G@1(G@2G@1QG@2  
=qJG@"333333trk (hURrl G@3p  
=qG@3(G@2  
=qG@3JhgG@#trm (hURrm G@3(G@3QG@2p  
=qG@3@J0G@#=#p  
=tro (hURrp G@3O)(G@4  
=qG@3(G@4J hG@\$trq (hURrr G@4QG@4  
=qG@3(G@4JHG@\$trs (hURrt

G@4\G@4(G@3G@3  
=qJ  
G@#  
=qtru (hURrv G@3G@3\G@3\G@3\J%G@#\trw (hURrx G@3\G@5G@3\G@5  
=qJYG@%33333try (hURrz G@5\G@6G@4QG@5JkyG@%\tr{ (hURr| G@5G@6G@5p  
=qG@5QJXG@%Qtr} (hURr~ G@4  
=qG@5G@4G@5QJG@%Qtr (hURr G@5QG@5  
=qG@4G@4QJG@\$Qtr (hURr G@4G@4\G@3G@40  
=qJ@G@\$333333tr (hURr G@4\G@5c  
=pG@4G@5QJ4G@%Qtr (hURr G@4\G@4QG@3\G@4J0G@\$)\tr (hURr G@4\G@5\G@40  
=qG@4Jh\IG\$\tr (hU Rr G@4G@4\G@2  
=qG@3QJAG@#Qtr (hURr G@3\G@4G@2\G@3  
=qJxG@#333333tr (hU  
Rr G@3  
=qG@4p  
=qG@3G@3QJH@G@#Qtr (hURr G@30  
=qG@3QG@2G@3JuG@#tr (hURr G@3  
=qG@4\G@3  
=qG@4\JG\$\tr (hURr G@3G@3G@2G@3JG@#tr (hURr G@30  
=qG@3p  
=qG@20  
=qG@20\JG@"Ltr (hURr G@2@G@3  
=qG@2@G@2QJG@"Gztr (hURr G@3\G@3^QG@2G@2\J vG@"tr (hURr  
G@2QG@2QG@2@G@2\JxFG@"tr (hURr G@3\G@3  
=qG@2\G@3J pG@#tr (hURr G@3GzHG@3p  
=qG@2  
=qG@3^QJG@#aGztr (hURr G@3^QG@3p  
=qG@2QG@2@J G@"=p  
=tr (hURr G@1\G@2G@10  
=qG@2JG@"\tr (hURr G@20\G@4p  
=qG@2@G@3@JIG@#=#p  
=tr (hURr G@3^QG@4G@3@G@4^QJG@\$aGztr (hURr G@4QG@6\G@4  
=qG@5JXG@%tr (hURr G@50\G@5QG@4G@5@JG@%=p  
=tr  
(hURr G@4  
=qG@5QG@4p  
=qG@4\JoG@\$tr (hU Rr G@4QG@4  
=qG@4G@4@JG@\$=p  
=tr (hURr G@3  
=qG@3QG@2QG@2QJ!G@"Qtr (hU  
Rr G@2QG@3\G@20  
=qG@3\JXG@#\tr (hURr G@2G@4G@2p  
=qG@4p  
=qJeG@\$p  
=qtr (hURr G@4QG@5^QG@3  
=qG@3  
=qJ} G@#333333tr (hURr G@3G@40\G@2QG@3QJrG@#Qtr (hURr G@3G@4QG@3G@4\Jp"G\$\tr (hURr  
G@4QG@4

=qG@3  
=qG@3  
=qJHG@#33333tr (hURr G@3zHG@4QG@3^QG@4QJiG@\$Qtr (hURr G@4^QG@5G@40  
=qG@5QJEG@%Qtr (hURr G@6(G@7(G@6G@7JG@'tr (hURr G@7!GzG@7G@5!GzG@5zHJ>G@%zHtr  
(hURr G@5  
=qG@7G@5fffffG@6QJ[(G@&Gztr (hURr G@6zGG@6G@5G@6+QJ>G@&.zGtr (hURr  
G@5QG@7s33333G@5G@6zHJ NG@&=p  
tr (hURr G@6G@6QG@5W  
=pG@6RjX,G@&\(tr (hURr G@6  
=pG@6)G@5fffffG@5  
=pJ(G@%tr (hURr G@5(G@5fffffG@4!GzG@4=p  
=J(G@\$=p  
=tr (hURr G@3(G@4@G@2G@3J@uG@#tr (hURr G@4G@6G@4G@4QJXwG@\$Gztr (hURr G@4G@5  
=p  
G@3fffffG@4  
=pJ  
G@\$tr (hU Rr G@4  
=qG@5W  
=pG@4(G@4=p  
JFG@\$=p  
tr (hU  
Rr G@4fffffG@633333G@4zHG@6  
=p  
J?G@&  
=p  
tr (hURr G@6G{G@7G@5GzHG@5J`G@%tr (hURr G@5kQG@7QG@5&fffffG@6kQJG@&kQtr (hURr G@6  
=pG@6fffffG@4(G@5p  
=qJhoG@%p  
=qtr (hURr  
G@5333333G@55(G@3G@4fffffJhguG@\$ffffftr (hURr G@5RG@8zGG@5zGG@6=p  
JXWG@&tr (hURr G@9G@9G@7G@9QJG@)Qtr (hURr G@8zGG@9GzG@8G@9  
=p  
JyG@)  
=p  
tr (hURr G@8W  
=pG@9G@8G@8@J'G@(=p  
=tr (hURr G@8TzGG@8G@7(G@8zHJG@(  
=p  
tr (hURr G@85(G@8(G@7RG@8QJBG@(Qtr (hURr G@9+QG@:G@8zGG@8  
=qJG@(33333tr (hURr G@9333333G@:J=p  
G@8G@:33333J8G@\*333333tr (hURr G@:33333G@;QG@8QG@9}p  
=J  
G@)zG{tr (hURr G@9h)G@:G@9333333G@9zGJxG@)  
=qtr (hURr G@:W  
=pG@:33333G@9(G@:  
=pJG@\*tr (hURr G@9QG@:@G@8G{G@8(JSG@(\(tr (hURr G@8=p  
=G@9G@7(G@9J)G@)\(tr (hURr!G@9QG@9(G@8  
=pG@8(J@G@(\(tr!(hURr!G@9YG@9s33333G@733333G@8RJ@G@(\(tr!(hU Rr!G@8#



=pG@8G@7QG@7G{J  
G@'G{tr!(hU  
Rr!G@85\(\G@8G@633333G@7JzG@'tr!(hURr!G@7\(\G@7}p  
=G@6\(\G@6JnG@&\(tr !(hURr  
!G@6  
=pG@7zGG@6G@7J=p  
JG@'Ltr!(hURr!G@7^QG@9G@7  
=p  
G@7.zGJ0+G@'.zGtr  
!(hURr!G@7B\(\G@8G@6G@8JG@(\tr!(hURr!G@8:G{G@8TzGG@7@G@7JG@'=p  
tr!(hURr!G@7\(\G@7  
=pG@7QG@7zHJVg@'=p  
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@6\(\G@7:G{J'G@'8Qtr!(hURr!G@7J=p  
G@7LG@6QG@7333333JG@'333333tr!(hURr!G@7333333G@7J=p  
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!G@4\(\G@4\(\G@3LG@3zHJ'yG@#zHtr!!(hURr"!G@3G@4=p  
=G@3}p  
=G@333333J[G@#\(\tr#!(hURr!G@4!GzG@5  
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=pJG@\$Gztr%!(hURr&!G@5zGG@5\(\G@4u\(\G@4)JxG@\$)tr!(hURr(!G@4G@5G@4YG@4  
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=qtr!(hURr\*!G@4zGG@4zGG@4TzGG@4G{J(yG@\$Qtr+!(hURr,!G@4\(\G@533333G@4s33333G@5)J5G@  
%)tr-(hURr.!G@5fffffG@5fffffG@4\(\G@5QRJ8G@%QRtr!(hURr0!G@5G@5RG@333333G@4  
=p  
J7G@\$  
=p  
tr1!(hURr2!G@3QG@4  
=qG@3\(\G@4O\(\JG@\$Ltr3!(hU  
Rr4!G@5kQG@5G{G@4\(\G@4xQJG@\$u\(\tr5!(hURr6!G@4  
=p  
G@4s33333G@3QG@3GzJ  
G@#Gztr7!(hURr8!G@4G@4G@3YG@4p  
=qJ8G@\$p  
=qtr9!(hURr:!G@4h)G@4G@4G@4TzGJG@\$QRtr;!(hURr<!G@4G@5fffffG@4\(\G@4O  
=qJG@\$333333tr=!(hURr>!G@4G@5G@3G{G@5QJ6G@%zGtr?!(hURr@!G@5G@7G@5G@6}p  
=JG@&zG{trA!(hURrB!G@6zG{G@7G@5\(\G@6B\(\J G@&B\(\trC!(hURrD!G@6G@8G@6s33333G@7p  
=JG@'G{trE!(hURrF!G@7W  
=pG@7QG@7\(\G@7JxG@'\(\trG!(hURrH!G@7zGG@8G@6G@7W  
=pJG@'W  
=ptrI!(hURrJ!G@7G@7)G@6  
=qG@7=p  
J@G@'=p  
trK!(hURrL!G@7)G@9G@7333333G@7@JG@'=p  
=trM!(hURrN!G@7  
=pG@8:G{G@7#  
=pG@7fffffJ]G@'ffffftrO!(hURrP!G@7\(\G@8.zGG@7G@7  
=pJT=G@'

=ptrQ!(hURrR!G@7G@7QG@7)(G@70  
=qJRG@'333333trS!(hURrT!G@6)(G@6)(G@5QG@6zHJ(G@&  
=p  
trU!(hU RrV!G@6  
=pG@7G@5zGG@633333JG@&333333trW!(hU  
RrX!G@633333G@7RG@4  
=pG@5#  
=pJgG@%#  
=ptrY!(hURrZ!G@5zHG@6G@5G@6=p  
JgG@&=p  
tr[!(hURr!G@7LG@8)(G@7LG@8)(\JP  
OG@(\(\tr]!(hU  
Rr^!G@8!GzG@9)(G@7  
=pG@8JG@(\(\tr\_!(hURr`!G@8GzG@9G@7)G@7(JG@\"(tra!(hURrb!G@7G{G@98QG@7)(G@9J

aG@)trc!(hURrd!G@5zHG@6zHG@4kQG@4=p  
JpkG@\$tre!(hURrf!G@5:G{G@5kQG@3G@3)(JHG@#(\trg!(hURrh!G@333333G@4)(G@3)p  
=G@3G{JpGG@#G{tri!(hURrj!G@4  
=pG@4G@3)(G@3=p  
JG@#=p  
trk!(hURrl!G@3c  
=pG@3QG@2G{G@3  
=pJG@#trm!(hURrn!G@3QG@3LG@1QG@2xQJG@\"u(\tro!(hURrp!G@2zG{G@2G@1G@2  
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=qtru!(hURrv!G@3EQG@3kQG@2)(G@2=p  
JHG@\"trw!(hURrx!G@3)(G@3zHG@233333G@3)(JG@#(\try!(hURrz!G@3fffffG@3QG@3B)(G@3RJG@#Rtr  
{!(hURr!G@3  
=pG@3fffffG@3G@3J@deG@#tr}!(hURr~!G@3  
=p  
G@3)G@3G@3!GzJXN6G@#Qtr!(hURr!G@3TzGG@3QG@2G{G@3@J[G@#=p  
=tr!(hURr!G@3B)(G@333333G@2=p  
G@2fffffJ  
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=p  
tr!(hU  
Rr!G@3  
=p  
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Rr!G@3G@3TzGG@2)(G@3  
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=qG@2333333G@2p  
=qJG@\"p  
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=ptr!(hURr!G@2G@2s33333G@1p  
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=pG@2:G{G@1)(G@2QJp~G@\"Qtr!(hURr!G@2#  
=pG@2#

=pG@133333G@1QJ3eG@!Qtr!(hURr!G@1  
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tr!(hU Rr!G@2G@3zGG@2.zGG@2@JG@"=p  
=tr!(hU Rr!G@2=p  
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u\tr!(hU Rr!G@0#  
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G@GzHtr!(hU Rr!G@.G@/G@.ffffG@/QJxG@  
=p  
tr!(hU Rr!G@/kQG@/RG@.zHG@/QJG@  
=p  
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Rr!G@.\G@^G@-\G@.JG@Qtr!(hU  
Rr!G@-ffffG@.QG@-\G@-\(J:tG@\tr!(hU  
Rr!G@.33333G@0@G@-G{G@/\J(G@\tr!(hU  
Rr!G@.G@0&ffffG@-G{G@0#  
=pJG@ #  
=ptr!(hU  
Rr!G@/#  
=pG@0YG@/G@0333333JWqG@ 333333tr!(hU  
Rr!G@0G@0333333G@/B\G@0J ^G@ tr!(hU  
  
Rr!G@0G@0G@/ffffG@0RJ(G@ Rtr!(hU  
Rr!G@0QG@1p  
=G@0G@1p  
=JG@!p  
=tr!(hU  
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Rr!G@133333G@2aGzG@133333G@1p  
=JG@!G{tr!(hU  
Rr!G@2  
=pG@2333333G@1QG@2\JHnG@"Qtr!(hU  
Rr!G@2W  
=pG@2h)G@0\G@0p  
=J(G@ G{tr!(hU

Rr!G@1J=p  
G@2:G{G@1J=p  
G@2JMG@"tr!(hU  
Rr!G@1  
=qG@2fffffG@1GzG@2LJZG@"Ltr!(hU  
Rr!G@25\G@3RG@2  
=pG@3QJG@#Qtr!(hU  
Rr!G@3QG@3kQG@1QG@2#  
=pJHuG@"#  
=ptr!(hU  
Rr!G@2\G@3  
=pG@1G@233333JG@"\tr!(hU  
Rr!G@2p  
=qG@3@G@2\G@30  
=qJG@#333333tr!(hU  
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Rr!G@1aGzG@2G@1\G@1JG@!tr!(hU  
Rr!G@1G{G@2fffffG@1p  
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GG@3  
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Rr!G@2G@3@G@2G@2\J\IG@"33333tr!(hURr"G@2)G@3+QG@1\G@2JmG@"\tr"(hU  
Rr"G@3zGG@3c  
=pG@2\G@3^QJozG@#aGztr"(hURr"G@3  
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@3EQG@3J=p  
G@2fffffG@2QJ}G@"\tr "(hURr  
"G@3G@4G@2\G@4J8?G@\$tr"(hURr"G@3RG@4333333G@3G@3zHJG@#=#p  
tr  
"(hURr"G@3\G@3G@3B\G@3zGJmG@#zGtr"(hURr"G@3\G@333333G@3RG@3  
=pJ  
G@#  
=ptr"(hURr"G@3  
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\G@4zHJG@\$=p  
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=p  
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"G@6\G@8zHG@6+QG@7\J5G@!\tr!"(hURr""G@7zG{G@7G@6#  
=pG@6zHJ}G@&zHtr#"(hURr"\$G@6u\G@6\G@6G@6=p  
J0nG@&=p  
tr%"(hU

Rr&"G@6J=p  
G@6p  
=G@6:G{G@6=p  
J\G@&=p  
tr"(hURr("G@6QG@6G@5ffffG@5zHJoG@%zHtr)"(hURr\*"G@5QG@5QG@5@G@5}p  
=JhG@%zG{tr+"(hU  
Rr,"G@5}p  
=G@5G@4G@5JkG@%tr-"(hURr."G@4G{G@4zGG@4  
=pG@4c  
=pJzgG@\$aGztr/"(hURr0"G@4ffffG@5G@40  
=qG@4QJ^G@\$Qtr1"(hURr2"G@4  
=pG@5TzGG@48QG@5(J1G@%Qtr3"(hURr4"G@4zGG@5zGG@4xQG@5QJG@%Qtr5"(hURr6"G@5ffffG  
@5xQG@4QG@4QJxG@\$zGtr7"(hURr8"G@5(G@5=p  
G@4G@5JG@%tr9"(hURr:"G@4ffffG@5s33333G@4ffffG@5(\JHG@%\(\tr;"(hURr<"G@5YG@6LG@5#  
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G@&p  
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=pG@733333G@6p  
=G@7  
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=ptrO"(hU  
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=pG@5  
=pG@4G@5JG@%  
=p  
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=p  
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=pG@6  
=qG@8  
=pJG@(trk"(hURrl"G@8(\)G@8G{G@8(\G@8QJXHG@(Qtrm"(hURrn"G@8W  
=pG@8(G@8W  
=pG@8h)J0G@(\fffftro"(hURrp"G@8QRG@9QG@8333333G@9YJG@)\(\trq"(hURrr"G@9  
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ffG@9J=p  
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=pG@7^QG@8zHJG@(

=p  
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=G@8G{J8G@(G{tr{"(hURr|"G@8)G@9  
=p  
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Rr~"G@8G{G@9=p  
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=pG@8G@9\J4G@)\tr"(hURr"G@8\G@8p  
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=JG@(p  
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=JNG@)p  
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=G@9}p

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=pG@8zGG@8:G{G@8zHJG@ (=p  
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=pG@8@J;G@ (=p  
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G@'u\ (tr#(hURr#G@7^QG@8^QG@7QRG@8QRJG@(QRtr#(hURr#G@8s33333G@9=p  
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#G@8p  
=G@8p  
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#(hURr#G@7QG@8p  
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=pJ G@\$tr##(hU  
Rr\$#G@4QG@4G@3aGzG@3=p  
JG@#=p  
tr%#(hURr&#G@3=p  
=G@4(\G@2(\G@4JXG@\$tr'#(hURr(#G@4=p  
=G@4GzG@3G@4=p  
JG@\$=p  
tr)(hURr\*#G@4kQG@4  
=pG@3G{G@4&ffffJG@ \$#  
=ptr+#(hURr,#G@1^QG@1G@0GzG@1QJG@!Qtr-  
#(hURr.#G@1+QG@1G@0G@1(\J &G@!Qtr/#(hURr0#G@0QG@1}p  
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G@0J@G@  
(tr1#(hURr2#G@0QG@1G{G@033333G@1EQJbG@!GzHtr3#(hURr4#G@1fffffG@1zGG@0(\G@1#  
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=ptr5#(hURr6#G@0G@1J=p  
G@/\(G@0J0G@ =p  
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=qtr?#(hURr@#G@0(\G@1zGG@0G@1JhvlG@!:=p  
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=JXG@"p  
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=pG@2J=p  
G@1u(\G@1zHJ{G@!:=p  
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G@-G{J0G@(\trU#(hURrV#G@-fffffG@.W  
=pG@-(\G@-QJG@QtrW#(hURrX#G@-G@.aGzG@-8QG@-  
=pJG@  
=ptrY#(hURrZ#G@-G@.B(\G@,GzG@,  
=qJHG@(\tr[#(hURr'#G@,)G@.p  
=qG@.G@.fffffJpG@ffffftr]#(hURr^#G@-(\G@-fffffG@,QG@,QJ8:G@Qtr\_#(hURr`#G@,QG@-  
\G@+G@,zGJ0qG@zGtra#(hURrb#G@,(\G@.333333G@.p  
=G@.  
=p  
JG@



=p  
trc#{hURrd#G@-33333G@/QG@-QG@.(J  
\_G@Gztre#{hURrf#G@.G@.  
=pG@-G@.QJPQG@Qtrg#{hURrh#G@.8QG@.  
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=p  
trq#{hURrr#G@-p  
G@.\{G@-p  
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=qtr#{hURr#G@/ffffG@/ffffG@.QRG@/\{JXgG@  
=p  
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=pG@,\{J=G@\{tr#{hU Rr#G@,p  
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=G@,\}J G@  
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=pG@.\{G@-#  
=pG@-J`=G@tr#{hU Rr#G@-aGzG@..zGG@-  
=p  
G@.  
=p  
JpG@  
=p  
tr#{hU Rr#G@-G@-G@,\{G@-(\}J@;pG@(\}tr#{hU Rr#G@-p  
=G@-GzG@-  
=p

G@-p  
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=qJ(XpG@p  
=qtr#(hU Rr#G@,G@-#  
=pG@,GzHG@-JG@tr#(hU  
Rr#G@-.zGG@-333333G@,G@-QJG@  
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=ptr#(hU  
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Rr#G@/#  
=pG@/\(G@-QG@.GzJ=G@Gztr#(hU

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=qG@/  
=pG@0RJ7G@  
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fffftr#(hURr#G@1zGG@1QRG@033333G@18QJHuG@!8Qtr#(hURr#G@0  
=qG@1G@/QG@0J4G@ tr#(hURr#G@0(G@0333333G@/  
=p  
G@/zGJgG@zGtr#(hURr#G@/zG{G@/zHG@.=p  
=G@.QRJh]SG@QRtr#(hURr#G@.  
=pG@0  
=p  
G@.\(G@/GzHJyG@GzHtr#(hU  
Rr#G@/G@0RG@.\(G@/.zGJI~G@333333tr#(hURr#G@/G@0h)\G@^(G@0LJP:MG@  
Ltr#(hURr#G@0:G{G@0=p  
=G@/QG@/ffffJ(WG@Gztr#(hURr\$G@00  
=qG@0333333G@/  
=p  
G@/LJ(YG@GzHtr\$(hURr\$G@/G@/G@.QG@.=p  
JrG@\tr\$(hURr\$G@.G@/ffffG@.G@^(JqG@  
=p  
tr\$(hURr\$G@/G@0p  
=qG@/G@0YJG@ \(\tr\$(hURr\$G@0  
=pG@0LG@/G@0(JH,|G@ Qtr \$(hURr  
\$G@0zHG@0#  
=pG@/kQG@/  
=qJIG@\tr\$(hURr\$G@/33333G@/G@.=p  
G@.RJG@tr  
\$(hURr\$G@/333333G@/QG@.ffffG@/p  
=qJJG@p  
=qtr\$(hURr\$G@/zGG@^(G@.RG@/J/\*NG@tr\$(hURr\$G@/G@0G@.QG@.\(J  
LG@\tr\$(hURr\$G@.ffffG@.zGG@.333333G@.QRJ~G@QRtr\$(hURr\$G@.\(G@.aGzG@-G@-  
=qJG@\tr\$(hURr\$G@.\(G@.\)G@-(G@-B(J@G@GzHtr\$(hURr\$G@-LG@.aGzG@-  
=p  
G@-ffffJG@Gztr\$(hU Rr\$G@-GzG@-ffffG@-W  
=pG@-JG@Qtr\$(hU  
Rr\$G@-G@.ffffG@-u(G@.\(J-G@\tr\$(hURr  
\$G@.G@.G{G@.\)G@.G{J%G@\tr!\$(hURr"\$G@/QG@/G@.QG@.aGzJ"bQG@\tr#\$(hU  
Rr\$G@.GzHG@.LG@-LG@-zGJHYG@\tr%\$(hURr&\$G@-QG@.333333G@-8QG@-  
33333JG@Qtr\$(hURr(\$G@-33333G@.aGzG@-QRG@.\)JHWyG@(\tr)\$(hURr\*\$G@-G@-QG@-G@-#  
=pJ8  
RG@Qtr+\$(hURr,\$G@-\(G@-  
=pG@.333333G@.ffffJbG@fffftr-\$(hURr.\$G@.zGG@-  
QG@+\(G@.GzHJXYG@GzHtr/\$(hURr0\$G@,QRG@-G@.=p  
=G@.G{JDG@\tr1\$(hURr2\$G@,GzG@,  
=qG@,G@,QJHpG@Qtr3\$(hURr4\$G@,  
=pG@-QG@,\(G@,J0.G@tr5\$(hURr6\$G@,QG@,\(G@,QG@,QJ+G@Qtr7\$(hURr8\$G@,\)G@,LG@+zGG@,#

=pJ}TG@Qtr9\$(hURr:\$G@,G@,QG@+ffffG@,)\J cmG@  
=ptr;\$hURr<\$G@,QG@-  
=pG@,33333G@-JbG@tr=\$(hURr>\$G@-G@-(\G@-.zGG@-J[PG@tr?(hURr@\$G@.\(G@.\(G@-(G@-  
JG@trA\$(hURrB\$G@-zGG@.G@,  
=qG@-33333J(G@QtrC\$(hURrD\$G@-(\)G@-kQG@,GzG@-J% }G@QtrE\$(hU RrF\$G@-p  
=G@-  
=pG@-G@-(\JMuG@(\trG\$(hU  
RrH\$G@-(\)G@-  
=pG@,G{G@-p  
=qJ l\_G@p  
=qtrI\$(hU  
RrJ\$G@-G@-G@,QG@-B(\JaG@GzHtrK\$(hURrL\$G@-aGzG@-  
=pG@,G{G@-8QJeG@333333trM\$(hURrN\$G@-.zGG@-fffffG@,QG@,(\J?G@GztrO\$(hURrP\$G@,kQG@-  
QG@,kQG@-p  
=JP0G@=p  
=trQ\$(hURrR\$G@-QG@-QG@,(\)G@,333333J\_G@333333trS\$(hURrT\$G@,kQG@,RG@,G@,  
=p  
JG@  
=p  
trU\$(hURrV\$G@+(\(G@,LG@+G@+(\J>uG@(\trW\$(hURrX\$G@,G@,QG@+ffffG@,W  
=pJc|G@(\trY\$(hURrZ\$G@,zG{G@,zG{G@+QG@+JwG@tr[\$(hURr\ \$G@+(\(G@-  
G@+LG@,B(\JLG@GzHtr)\$hURr^\$G@,zG{G@-aGzG@,QRG@-(\)J(G@(\)tr\_\$(hURr` \$G@-  
G@.333333G@,G@-(\JG@Gztra\$(hURrb\$G@-(\G@.#  
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=pJHG@  
=ptrc\$(hURrd\$G@,aGzG@-G@,G@,QJG@Qtre\$(hURrf\$G@,RG@-RG@,33333G@-  
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(\G@,GzG@,ffffJ0xG@Gztrk\$(hURrl\$G@,QG@-.zGG@,p  
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G@,zHJ|G@tru\$(hU  
Rrv\$G@,RG@-GzHG@,zG{G@-zGJqG@zGtrw\$(hURrx\$G@-8QG@-p  
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=pJ0G@(\try\$(hURrz\$G@-G@.G@-p  
=qG@.=p  
JG@(\tr{ \$(hURr|\$G@.#  
=pG@.LG@-(\G@-33333JG@Qtr}\$(hURr~\$G@-33333G@-QG@-kQG@-p  
J8CzG@(\tr\$(hURr\$G@-  
=pG@.QG@-LG@.JUG@tr\$(hURr\$G@-QG@.\(G@+G@-zG{J:bG@zG{tr\$(hURr\$G@-(\G@.\)G@-(\)G@.  
=p  
J0fG@  
=p  
tr\$(hURr\$G@-G{G@.  
=p  
G@,(\G@-JNvG@tr\$(hURr\$G@-#

=pG@.G@-QG@-QJTG@Qtr\$(hURr\$G@-QG@..zGG@-p  
G@.QJQjG@  
=p  
tr\$(hURr\$G@.QG@.QRG@-G@-LJ  
oG@GzHtr\$(hURr\$G@-zG{G@-QG@,GzG@-QJDG@Qtr\$(hURr\$G@-8QG@-G@-  
=p  
G@-p  
=Jp EG@=p  
=tr\$(hURr\$G@-(\)G@-333333G@,G@-QJ@k6G@Qtr\$(hURr\$G@,  
=qG@-kQG@,QG@-(\JmG@  
=p  
tr\$(hU  
Rr\$G@-QG@-W  
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=qJ8lyG@p  
=qtr\$(hU  
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=qG@-G@,W  
=pG@-p  
=qJG@p  
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=p  
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=pG@.J T}G@tr\$(hURr\$G@.#  
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=G@-#  
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hOG@Qtr\$(hURr\$G@-  
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G@-W  
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G@-p

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=pG@)  
=pG@\*zG{J)G@zG{tr\$(hURr\$G@\*ffffffG@\*G@)p  
=qG@\*=p  
=JpOG@=p  
=tr\$(hURr\$G@\*B(\G@\*aGzG@)(\G@\*GzHJSG@GzHtr\$(hURr\$G@\*(\G@+=p  
=G@\*.zGG@+QJG@  
=p  
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G@2  
=pG@1QG@1J7G@!(\tr\$(hURr\$G@1  
=pG@2ffffffG@1p  
=G@2=p  
=J@aG@"=p  
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=pJoG@!  
=ptr%(hURr%G@1p  
=G@2

=p  
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=pG@1&ffffJG@!#  
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=p  
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JG@  
tr%(hU  
Rr%G@0  
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=pG@3  
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=p  
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=p  
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JG@\$tr%(hU  
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=G@5\((G@4GzG@5  
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J :G@&Ltr%(hU  
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=G@80  
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=pG@Q0Jo(G@Q0trk+(hURrl+G@Q  
G@Q'  
=pG@PQG@PQJDG@PQtrm+(hURrn+G@P(\G@P  
=qG@PGzHG@PQJTAG@PQtro+(hURrp+G@P(\G@PzGG@PQG@Pp  
=JrG@Pp  
=trq+(hURrr+G@PRG@Q33333G@P=p  
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Rrv+G@Q>zGG@QG@Q#33333G@QQJ'G@QQtrw+(hU Rrx+G@QQG@Q(\G@QIG@QRJG@QRtry+(hU  
Rrz+G@QffffG@R^QG@QG@R333333JhG@R333333tr{+(hU Rr|+G@RWzHG@RdzGG@Q=p  
G@R!GzJ=G@R!Gztr}+(hU Rr~+G@RQG@RnQG@QG{G@R JT;G@R tr+(hU Rr+G@R3  
=pG@R\G@QG@R(QJG@R(Qtr+(hU  
Rr+G@R6ffffG@RzGG@R33333G@RJ)pG@Rtr+(hU Rr+G@RnzGG@RG{G@R]p  
=G@RG{JpG@RG{tr+(hU Rr+G@RffffG@RQG@RR)(G@RffffJXG@Rfffftr+(hU Rr+G@Rs33333G@R  
=pG@RS33333G@Rx\JXG@Rx)tr+(hU Rr+G@RffffG@R  
=pG@R333333G@RqGzJ4G@RqGztr+(hU Rr+G@RQG@RQG@RzGG@R  
=qJbG@R  
=qtr+(hU Rr+G@RG@S  
=pG@RGzG@RJG@Rtr+(hU Rr+G@R33333G@R(\G@R%QG@R@JujG@R@tr+(hU Rr+G@Rs  
=pG@R  
=pG@RnzGG@RJG@Rtr+(hU Rr+G@SQG@SqRG@SffffG@Sg  
=pJYG@Sg  
=ptr+(hU Rr+G@SJG{G@S^zGG@RzGG@S=p  
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=qtr+(hU Rr+G@SG  
=pG@SaGzG@S+QG@S>QJt&G@S>Qtr+(hU  
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Rr+G@RG@RG@RL(\G@RQJG@RQtr+(hU  
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=G@RJ=p  
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Rr+G@S  
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G@S\J8G@S\tr+(hU  
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Rr+G@S\(\G@TyG@SG@T]p  
=J0G@T]p  
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J

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JMG@S=p  
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=pG@SQG@Sp  
=JXG@Sp  
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=pJ-nG@V  
=ptr+(hURr+G@VaRG@WEQG@V`G@VQJ(G@VQtr+(hURr+G@W  
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=pG@VzGG@Vz=p  
G@V  
=pJx4G@V  
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G@U  
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=pG@V!RG@V`  
=qJG@V`  
=qtr\_(hURr`,G@VJ=p  
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=pJfG@U  
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JG@UZ=p  
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=J41G@W}p  
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=JG@Wp  
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@WRG@WQG@W0G@W:=p  
J4GGG@W:=p  
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=G@W33333G@WB\(\G@WiJ@G@Witr,(hU  
Rr,G@WjG{G@W  
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Rr,G@ZQG@[5\(\G@ZzG{G@Z\(\J  
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fG@Z\(\tr,(hURr,G@ZG@[wzHG@ZG@[^\(\JG@[^\(\tr,(hURr,G@[QG@[G@[qGzG@[GzJ4RG@[Gztr,(hURr,G  
@[



=pG@\G@[33333G@[QJ]\G@[Qtr,(hURr,G@\|(G@\pG@\

=qG@\b\J\7G@\b\tr,(hURr,G@\GzG@\G@%\|(G@8)JG@8)tr,(hURr,G@\3

=pG@\p

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G@^(\G@^G@^G{JG@^G{tr,(hURr,G@^33333G@\_33333G@^G@^J^G@^)tr,(hURr,G@\_?)\G@\_

=pG@^(\G@\_zGJG@\_zGtr,(hURr,G@\_tzGG@\_uQG@^(\G@\_)(JG@\_)(tr,(hURr,G@\_G@\_G@)\(G@^(\JcG@^(\tr,(hURr,G@]ffffG@^mp

=G@]

=pG@^QJj G@^Qtr,(hU

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=pG@^

JDG@^ tr,(hURr,G@^RG@\_KQG@^(\G@\_E)(J=G@\_E)(tr,(hURr-G@\_,\(G@\_@

=qG@^=p

G@^=p

JG@^=p

tr-(hURr-G@^zHG@\_\*=p

G@^G@^c33333JG@^c33333tr-(hURr-G@^IG@\_)(G@^zGG@^JG@^tr-(hURr-G@^ffffG@\_G@^QG@^JXXXG@^tr-(hURr-G@\_)(G@\_E)(G@^C

=pG@^(\JIG@^)(tr-(hURr

-G@^QG@\_G@]zGG@]J<G@]tr-(hURr-G@^

=pG@^(\G@]

=qG@^x)J\*G@^x)tr

-(hURr-G@^

=pG@^(\G@^G@^#

=pJG@^#

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=qJ0(G@^P

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=pG@`zGG@`JG@`tr-(hURr-G@`\G@`G{G@`LG@`JG@`tr-(hU Rr-G@`\G@`G@`%\G@`J(JG@`J)(tr-(hURr-G@`\G@`G@`RG@`33333JG@`33333tr-(hURr-G@`=p

G@`ffffG@`iRG@`zGJG@`zGtr-(hURr-G@`33333G@`zHG@`zGG@`=p

J8G@`=p

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=G@a)G@a33333JxG@a33333tr7-(hURr8-G@b

\(G@b.ffffG@ar=p

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=pG@a(G@`p

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RrL-G@`  
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=pJAG@^  
=ptrU-(hURrV-G@^p  
=G@\_G@^G@^zGJG@^zGtrW-(hURrX-G@^p  
=G@`QG@^@G@\_zGJG@\_zGtrY-(hURrZ-G@`g  
=pG@`G@`J(\G@`QJBG@`Qtr[-(hURr\G@`G@{G@`G@{G@`8QG@`b=p  
JcG@`b=p  
tr]- (hURr^G@`P)G@`  
=pG@`9RG@`JG@`tr\_-(hURr`-G@`zGG@`QG@`33333G@`J\$G@`tra-(hURrb-  
G@`\_zHG@`QG@`QG@`zGJG@`zGtrc-(hURrd-G@`<(\G@`\(\G@`1GzG@`\(\J{G@`\(\tre-(hURrf-G@`p  
=G@aHG@`G@aJTG@atrg-(hURrh-G@aogHG@atG@a-QG@aO)(JG@aO)(tri-(hU Rrj-  
G@a~zGG@b7(\G@azG{G@bQJTG@bQtrk-(hU Rrl-G@b  
=pG@b:G{G@a33333G@aQJG@aQtrm-(hU Rrn-G@`RG@a2=p  
G@`QG@`QJG@`Qtro-(hU Rrp-G@`QG@`G@`@G@`x  
=qJ  
G@`x  
=qtrq-(hU  
Rrr-G@azHG@aAGzG@`ffffG@aQJ\*G@aQtrs-(hU Rrt-G@a<G@aIG@`G@`zHJG@`zHtru-(hU Rrv-  
G@`zHG@alG@`G@a33333J]-G@a33333trw-(hU  
Rrx-G@aZ(\G@a`G@aG@a&ffffJeG@a&ffffftry-(hU Rrz-G@a=p  
G@a\_(\G@affffG@aYRJJG@aYRtr{-(hU Rrj-G@a\_zHG@aG{G@a333333G@aMQJIZG@aMQtr}-(hU Rr~-  
G@aaRG@a33333G@a:(G@ap  
=JCG@ap  
=tr-(hU Rr-G@a  
=qG@aQG@alG@a  
=qJ/G@a  
=qtr-(hU Rr-G@aG@aGzG@aj=p  
G@aRjyG@aRtr-(hU Rr-G@azGG@bG@aRG@bJIG@btr-(hU Rr-G@bW(\G@b33333G@bTG@b)Ja=G@b)tr-  
(hU Rr-G@bZG{G@c'  
=pG@bZ=p  
G@c%\(\JG@c%\(\tr-(hU Rr-G@cO  
=pG@c`G@bG@c  
=qJzG@c  
=qtr-(hU Rr-G@c8  
=qG@cP  
=qG@c

=p  
G@cPjfG@cPtr-(hU Rr-G@c.zGG@cS33333G@cG@c/  
=pJ4OG@c/  
=ptr-(hU  
Rr-G@cT(\G@cQG@c\(\G@cG{J\*G@cG{tr-(hU  
Rr-G@cG@cG{G@c|zGG@cffffJG@cfffftr-(hU  
Rr-G@c\G@c\(\G@cQG@cp  
=JdyG@cp  
=tr-(hU  
Rr-G@cG@c\(\G@c0G@czHJfG@czHtr-(hU  
Rr-G@c  
=pG@d2\(\G@cffffG@d.ffffJ&G@d.fffftr-(hU  
Rr-G@dozHG@dQG@d\_  
=pG@dQJG@dQtr-(hU  
Rr-G@eFffffG@ecQG@d\(\G@dQJYG@dQtr-(hU  
  
Rr-G@dG@d\(\G@d33333G@dGzJkG@dGztr-(hU  
Rr-G@e/zHG@e\(\G@c&QG@dG\(\JG@dG\tr-(hU  
Rr-G@d`QG@d\G@d9G@dJ`G@dtr-(hU  
Rr-G@d\(\G@e2=p  
G@dpG@d\(\JIKG@d\tr-(hU  
Rr-G@dGzG@eE\(\G@dG@e2\(\JEG@e2\tr-(hU  
Rr-G@ezGG@eGzG@e%\(\G@eJfG@etr-(hU  
Rr-G@epG@ezGG@eaG@eJ(\G@etr-(hU  
Rr-G@ezHG@e\(\G@e@G@eMp  
=JG@eMp  
=tr-(hU  
Rr-G@eK33333G@eG@e>QG@eQJG@eQtr-(hU  
Rr-G@gRG@g33333G@fQG@gEQJhIG@gEQtr-(hU  
Rr-G@g9RG@gfQG@gfzHG@g=\(J\*G@g=\tr-(hU  
Rr-G@g  
=pG@g<G@fQG@f)J,G@f)tr-(hU  
Rr-G@g)GzG@g+  
=pG@f\(\G@gffffJG@gfffftr-(hU  
Rr-G@g.gffffG@gRG{G@gffffG@g"G{J&G@g"G{tr-(hU  
Rr-G@gE\(\G@g  
=pG@g\(\G@g`JG@g`tr-(hU  
Rr-G@g\(\G@g  
=pG@gffffG@gffffJLG@gfffftr-(hURr-G@g33333G@g33333G@fG@gnzGJG@gnzGtr-(hURr-  
G@gQG@gzGG@fzHG@g{  
=pJ"G@g{  
=ptr-(hURr-G@g)GzG@gQG@gzHG@gE\(\J=G@gE\tr-(hURr-G@gG@hG@g(  
=qG@gGzJHG@gGztr-(hURr-G@gQG@h\(\G@gD\(\G@gIJGG@gItr-(hURr-G@gUp  
=G@g\G@d  
=qG@e  
=pJUG@e  
=ptr-(hU Rr-G@edG@e  
=pG@dQG@d

=pJL>G@d  
=ptr-(hURr-G@d)G@dffffG@b(\G@c8QJ\$/G@c8Qtr-(hU  
Rr-G@d33333G@e\_(G@c8QG@e>QJG@e>Qtr-(hURr-G@f%QG@f2=p  
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=qG@c(\G@dzGJG@dzGtr-(hURr-G@d33333G@effffG@dC33333G@d~ffffJtG@d~fffftr-(hURr-G@dp  
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=qG@e33333G@eGzJtG@eGztr-(hURr-G@e  
=pG@eGzG@e@QG@eRJG@eRtr-(hURr-G@f=p  
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=pJ2sG@f  
=ptr-(hURr-G@fm(\G@g%p  
=G@fdG@g GzJ<G@g Gztr-(hURr-G@gjG{G@gvffffG@fvffffG@f  
=pJLG@f  
=ptr-(hURr-G@fQG@gzGG@f6ffffG@f[QJG@f[Qtr-(hURr-G@f\$G@fG@fzHG@fyRJG@fyRtr-(hURr-  
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Rr-G@h2G{G@huQG@hzGG@hFQJ  
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(hU  
Rr-G@gzGG@h  
=pG@gz=p  
G@g(J-G@g)\tr-(hURr-G@g  
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=pG@hG@f(G@gJi.G@gtr-(hURr-G@gP  
=qG@gj(\G@f\$33333G@f(JDG@f)\tr-(hURr.G@f(G@gzGG@fG@f  
=pJG@f  
=ptr.(hURr.G@g-\(G@gz)(G@f(G@gfQJG@gfQtr.(hURr.G@g  
=pG@h=QG@gzGG@h=QJhG@h=Qtr.(hURr.G@h`)G@h(\G@hYGzG@hJG@htr.(hURr.G@hQG@iQG@h=p  
G@hffffJG@hfffftr .(hURr  
.G@hffffG@i^QG@hG@h=p  
J4G@h=p  
tr.(hURr.G@iG{G@i1RG@h(\G@h(JG)G@h)\tr  
. (hURr.G@hG@iG@hG@h(J%G@h)\tr.(hURr.G@h  
=qG@iQG@hG@hZG{JLG@hZG{tr.(hURr.G@hmQG@hzGG@hzGG@h)\(JG@h)\tr.(hURr.G@gffffG@h  
G@fzGG@fJG@ftr.(hURr.G@fG@f33333G@eG)(G@f4zGJtAiG@f4zGtr.(hURr.G@fzGG@fQG@eYG@ehJi>  
G@ehtr.(hU Rr.G@eiG@fpG@e G@fIJG@fltr.(hU  
Rr.G@f2)(G@fG@eQG@f@  
=qJH((G@f@  
=qtr.(hURr.G@fG@f;33333G@e@G@ezGJG@ezGtr.(hURr .G@f0  
=qG@fmp  
=G@ep  
=G@fX)JWG@fX)tr!. (hURr".G@f7  
=pG@fg  
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=G@dzGJG@dzGtr'.(hURr.(G@d6QG@dG@cQG@d+QJTG@d+Qtr).(hURr\*.G@bRG@c(\G@b@G@ctzGJ.G  
@ctzGtr+.(hURr,.G@azGG@aG@\_)G@ab=p

J.G@ab=p  
tr-  
. (hURr..G@azHG@affffG@`QG@`33333JTEG@`33333tr/.(hURr0.G@a\_zHG@abG{G@`3QG@`@QJDOG@`  
@Qtr1.(hURr2.G@`QG@`ffffG@\_G@`@QJ#G@`@Qtr3.(hURr4.G@dG@`GzG@`!G@`qGzJlqWG@`qGztr5.(  
hURr6.G@`k  
=pG@`ffffG@`@G@`(\JhG@`(\tr7.(hURr8.G@`.ffffG@aG@`,`G@`QJUG@`Qtr9.(hURr:.G@azHG@aG{G@`(\  
G@`J&G@`tr;.(hURr<.G@`QG@`G@`mp  
=G@`tJ,G@`tr=(.hURr>.G@`M(G@`G@`G@`+QJmG@`+Qtr?.(hURr@.G@`Z(G@` }p  
=G@`^qGzG@`^J]YG@`^trA.(hURrB.G@]zGG@`\_1RG@]QGzG@`^O(J]SoG@`^O(\trC.(hURrD.G@`QG@`\_IG@`^f  
ffffG@`\_^QJPG@`\_^QtrE.(hURrF.G@`QG@`?(G@`\_G@`.ffffJG@`.fffftrG.(hURrH.G@`VffffG@``G@`^zHG  
@\_7  
=pJ(G@\_7  
=ptrI.(hU  
RrJ.G@\_QG@`8)G@\_hQG@`,`J\$G@`,`trK.(hURrL.G@`,`G@`YG@\_  
=qG@\_p  
=JG@\_p  
=trM.(hURrN.G@\_GzG@\_QG@\_  
=pG@`\_(QJt+G@`\_(QtrO.(hURrP.G@`\_(G@`\_G@`^(\G@`^QJ#G@`^QtrQ.(hURrR.G@`^G@`\_&ffffG@`^kQG@`^zG  
Jh6G@`^zGtrS.(hURrT.G@`\_33333G@`\_zGG@`^7  
=pG@`^b(Jd;G@`^b(\trU.(hURrV.G@`^QG@`^  
=qG@`zHG@]p  
=JAG@]p  
=trW.(hURrX.G@](\G@`^  
G{G@]`=p  
G@](\J@G@](\trY.(hURrZ.G@]h)G@`^E(\G@`(\G@`]JP4G@]tr[.(hURr\G@]QG@`^33333G@](\G@`^p  
=J#G@`^p  
=tr].(hURr^.G@`\_G@`ffffG@`\_qGzG@``=QJqG@``=Qtr\_.(hURr`.G@`)GzG@`FQG@`\_333333G@`\_AGzJ.G@`\_AG  
ztra.(hURrb.G@`\_(\G@`\_~QG@]G@`^nQJ#dG@`^nQtrc.(hURrd.G@`^(\G@`\_8QG@`^G@`\_zHJG@`\_zHtre.(hURrf.G  
@`^QG@`\_H)G@`^G@`\_(\JG@`\_(\trg.(hURrh.G@`\_'  
=pG@`\_G@`^3  
=pG@`^;QJ\$#G@`^;Qtri.(hURrj.G@`^=p  
G@`^QG@]33333G@`^JG@`^trk.(hU  
Rrl.G@`^~QG@`^p  
=G@]zHG@](\J  
G@](\trm.(hURrm.G@`\_ffffG@\_QG@`^G@`\_ffffJxLzG@`\_fffftro.(hURrp.G@`\_(\G@`(\G@`\_JG{G@`\_RJsAG@`\_R  
trq.(hU  
Rrr.G@`\_ffffG@`\_0G@`^G@`\_(\JG@`\_(\trs.(hURrt.G@`<(\G@`^IG@`\_G@`\_  
=pJQvG@`\_  
=ptru.(hURrv.G@`^33333G@`^G{G@`^33333G@`\_QJHG@`\_Qtrw.(hURrx.G@`%(\G@`G@`p  
=G@`=p  
JG@`=p  
try.(hURrz.G@`  
=pG@`GzG@`5p  
=G@`5p  
=J&G@`5p  
=tr{.(hURr|.G@`c  
=pG@`GzG@`%\G@`  
=qJ?G@`

=qtr}.(hURr~.G@`QG@a33333G@`zGG@ap)JIEG@ap)tr.(hURr.G@a~QG@a33333G@a\*(G@a(J=G@a(tr.(hURr.G@a  
=pG@b7zHG@azGG@b!RJT/G@b!Rtr.(hURr.G@bffffG@b)RG@azHG@aJ(  
G@atr.(hURr.G@aG@bG@a33333G@aQJnG@aQtr.(hURr.G@a  
=qG@b6QG@a  
=qG@aJG@atr.(hURr.G@bIG@bQG@aQG@b)JX2G@b)tr.(hURr.G@b)G@bffffG@b;33333G@bozHJlv9G  
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Rr.G@c1G@cffffG@c  
=p  
G@cG{J(G@cG{tr.(hURr.G@c)RG@c<zGG@bQG@bzGJG@bzGtr.(hU  
Rr.G@b(\G@cmp  
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G@dffffJ4G@dfffftr.(hURr.G@dG@dG{G@d"(\G@d\zGJ3G@d\zGtr.(hURr.G@dG{G@e?(\G@czGG@ezGJH  
HG@ezGtr.(hURr.G@eVffffG@ec33333G@dp  
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URr.G@fzGG@fG@ep  
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=G@g)RG@fG@g(J\G@g(tr.(hURr.G@gQG@gc  
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G@gEQG@f  
=pG@g<JG@g<tr.(hURr.G@g5p  
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=qG@fzHG@f9G@fp  
=J;G@fp  
=tr.(hURr.G@fG@gM(G@fG{G@gM(JvG@gM(tr.(hURr.G@gmQG@g~ffffG@f  
=pG@g`QJoG@g`Qtr.(hURr.G@gXQG@gffffG@g0G@gVzGJH`G@gVzGtr.(hURr.G@gnffffG@gGzG@gl(\G  
@gJ,LG@gtr.(hURr.G@g33333G@gG@g)G@gC33333J{rG@gC33333tr.(hURr.G@g[QG@gffffG@fG{G@g+  
=pJLG@g+  
=ptr.(hURr.G@g  
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=qG@g4zGJxmG@g4zGtr.(hURr.G@g  
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=ptr.(hURr.G@eiG@f<G@e"=p  
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@e  
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7G@eBG{tr/(hURr/G@eFzGG@eG@d  
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/G@d(G@e  
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/(hURr/G@e2G{G@ep  
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=pJ5G@eC  
=ptr/(hURr/G@eQG@f\$(G@eJG@eQJG@eQtr/(hURr/G@eG@fvfffffG@ezHG@fqJG@fqtr/(hU  
Rr/G@fffffG@fQG@ezGG@eJ(G@etr/(hU  
Rr/G@ep  
=G@f\*G{G@ek  
=pG@f(J#G@f(tr/(hURr/G@e  
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P6

# CREATOR: The GIMP's PNM Filter Version 1.0

16 16

255

hIu=-Kx\*C[?e?e?e?e?e?e?e4So\*C[?e?e)BZ)BZ)BZ)BZ-G`\*C[?e\*C[

### MATPLOTLIBRC FORMAT

# This is a sample matplotlib configuration file - you can find a copy

# of it on your system in

# site-packages/matplotlib/mpl-data/matplotlibrc. If you edit it

# there, please note that it will be overridden in your next install.

```

# If you want to keep a permanent local copy that will not be
# over-written, place it in HOME/.matplotlib/matplotlibrc (unix/linux
# like systems) and C:\Documents and Settings\yourname\.matplotlib
# (win32 systems).
#
# This file is best viewed in a editor which supports python mode
# syntax highlighting. Blank lines, or lines starting with a comment
# symbol, are ignored, as are trailing comments. Other lines must
# have the format
# key : val # optional comment
#
# Colors: for the color values below, you can either use - a
# matplotlib color string, such as r, k, or b - an rgb tuple, such as
# (1.0, 0.5, 0.0) - a hex string, such as ff00ff or #ff00ff - a scalar
# grayscale intensity such as 0.75 - a legal html color name, eg
red,
# blue, darkslategray

#### CONFIGURATION BEGINS HERE

# the default backend; one of GTK GTKAgg GTKCairo CocoaAgg FltkAgg
# MacOSX QtAgg Qt4Agg TkAgg WX WXAgg Agg Cairo GDK PS PDF SVG Template
# You can also deploy your own backend outside of matplotlib by
# referring to the module name (which must be in the PYTHONPATH) as
# 'module://my_backend'
backend      : GTKAgg

# if you are running pyplot inside a GUI and your backend choice
# conflicts, we will automatically try and find a compatible one for
# you if backend_fallback is True
#backend_fallback: True
#interactive      : False
#toolbar         : toolbar2 # None | classic | toolbar2
#timezone        : UTC      # a pytz timezone string, eg US/Central or Europe/Paris

# Where your matplotlib data lives if you installed to a non-default
# location. This is where the matplotlib fonts, bitmaps, etc reside
#datapath : /home/jdhunter/mpldata

### LINES
# See http://matplotlib.sourceforge.net/api/artist\_api.html#module-matplotlib.lines for
more
# information on line properties.
#lines.linewidth : 1.0 # line width in points
#lines.linestyle  : -   # solid line
#lines.color      : blue
#lines.marker     : None # the default marker

```



```
#lines.markeredgewidth : 0.5 # the line width around the marker symbol
#lines.markersize : 6 # markersize, in points
#lines.dash_joinstyle : miter # miter|round|bevel
#lines.dash_capstyle : butt # butt|round|projecting
#lines.solid_joinstyle : miter # miter|round|bevel
#lines.solid_capstyle : projecting # butt|round|projecting
#lines.antialiased : True # render lines in antialiased (no jaggies)
```

### ### PATCHES

```
# Patches are graphical objects that fill 2D space, like polygons or
# circles. See
# http://matplotlib.sourceforge.net/api/artist\_api.html#module-matplotlib.patches
# information on patch properties
#patch.linewidth : 1.0 # edge width in points
#patch.facecolor : blue
#patch.edgecolor : black
#patch.antialiased : True
# render patches in antialiased (no jaggies)
```

### ### FONT

```
#
# font properties used by text.Text. See
# http://matplotlib.sourceforge.net/api/font\_manager\_api.html for more
# information on font properties. The 6 font properties used for font
# matching are given below with their default values.
#
# The font.family property has five values: 'serif' (e.g. Times),
# 'sans-serif' (e.g. Helvetica), 'cursive' (e.g. Zapf-Chancery),
# 'fantasy' (e.g. Western), and 'monospace' (e.g. Courier). Each of
# these font families has a default list of font names in decreasing
# order of priority associated with them.
#
# The font.style property has three values: normal (or roman), italic
# or oblique. The oblique style will be used for italic, if it is not
# present.
#
# The font.variant property has two values: normal or small-caps. For
# TrueType fonts, which are scalable fonts, small-caps is equivalent
# to using a font size of 'smaller', or about 83% of the current font
# size.
#
# The font.weight
# property has effectively 13 values: normal, bold,
# bolder, lighter, 100, 200, 300, ..., 900. Normal is the same as
# 400, and bold is 700. bolder and lighter are relative values with
# respect to the current weight.
#
# The font.stretch property has 11 values: ultra-condensed,
```

```

# extra-condensed, condensed, semi-condensed, normal, semi-expanded,
# expanded, extra-expanded, ultra-expanded, wider, and narrower. This
# property is not currently implemented.
#
# The font.size property is the default font size for text, given in pts.
# 12pt is the standard value.
#
#font.family      : sans-serif
#font.style       : normal
#font.variant     : normal
#font.weight      : medium
#font.stretch     : normal
# note that font.size controls default text sizes. To configure
# special text sizes tick labels, axes, labels, title, etc, see the rc
# settings for axes and ticks. Special text sizes can be defined
# relative to font.size, using the following values: xx-small, x-small,
#
# small, medium, large, x-large, xx-large, larger, or smaller
#font.size        : 12.0
#font.serif       : Bitstream Vera Serif, New Century Schoolbook, Century Schoolbook L, Utopia, ITC Bookman,
Bookman, Nimbus Roman No9 L, Times New Roman, Times, Palatino, Charter, serif
#font.sans-serif  : Bitstream Vera Sans, Lucida Grande, Verdana, Geneva, Lucid, Arial, Helvetica, Avant Garde,
sans-serif
#font.cursive     : Apple Chancery, Textile, Zapf Chancery, Sand, cursive
#font.fantasy     : Comic Sans MS, Chicago, Charcoal, Impact, Western, fantasy
#font.monospace   : Bitstream Vera Sans Mono, Andale Mono, Nimbus Mono L, Courier New, Courier, Fixed,
Terminal, monospace

### TEXT
# text properties used by text.Text. See
# http://matplotlib.sourceforge.net/api/artist\_api.html#module-matplotlib.text for more
# information on text properties

#text.color       : black

### LaTeX customizations. See http://www.scipy.org/Wiki/Cookbook/Matplotlib/UsingTex
#text.usetex
    : False # use latex for all text handling. The following fonts
           # are supported through the usual rc parameter settings:
           # new century schoolbook, bookman, times, palatino,
           # zapf chancery, charter, serif, sans-serif, helvetica,
           # avant garde, courier, monospace, computer modern roman,
           # computer modern sans serif, computer modern typewriter
           # If another font is desired which can loaded using the
           # LaTeX \usepackage command, please inquire at the
           # matplotlib mailing list
#text.latex.unicode : False # use "ucs" and "inputenc" LaTeX packages for handling
           # unicode strings.

```

```

#text.latex.preamble : # IMPROPER USE OF THIS FEATURE WILL LEAD TO LATEX FAILURES
                        # AND IS THEREFORE UNSUPPORTED. PLEASE
                        DO NOT ASK FOR HELP
                        # IF THIS FEATURE DOES NOT DO WHAT YOU EXPECT IT TO.
                        # preamble is a comma separated list of LaTeX statements
                        # that are included in the LaTeX document preamble.
                        # An example:
                        # text.latex.preamble : \usepackage{bm},\usepackage{euler}
                        # The following packages are always loaded with usetex, so
                        # beware of package collisions: color, geometry, graphicx,
                        # type1cm, textcomp. Adobe Postscript (PSSNFS) font packages
                        # may also be loaded, depending on your font settings

#text.dvipnghack : None # some versions of dvipng don't handle alpha
                    # channel properly. Use True to correct
                    # and flush ~/.matplotlib/tex.cache
                    # before testing and

False to force
                    # correction off. None will try and
                    # guess based on your dvipng version

#text.markup      : 'plain' # Affects how text, such as titles and labels, are
                        # interpreted by default.
                        # 'plain': As plain, unformatted text
                        # 'tex': As TeX-like text. Text between '$'s will be
                        # formatted as a TeX math expression.
                        # This setting has no effect when text.usetex is True.
                        # In that case, all text will be sent to TeX for
                        # processing.

# The following settings allow you to select the fonts in math mode.
# They map from a TeX font name to a fontconfig font pattern.
# These settings are only used if mathtext.fontset is 'custom'.
# Note that this "custom" mode is unsupported and may go away in the
# future.
#mathtext.cal : cursive
#mathtext.rm  : serif
#mathtext.tt  : monospace
#mathtext.it  : serif:italic
#mathtext.bf  : serif:bold
#mathtext.sf
: sans
#mathtext.fontset : cm # Should be 'cm' (Computer Modern), 'stix',
                        # 'stixsans' or 'custom'
#mathtext.fallback_to_cm : True # When True, use symbols from the Computer Modern
                                # fonts when a symbol can not be found in one of
                                # the custom math fonts.

```

```

#mathtext.default : it # The default font to use for math.
    # Can be any of the LaTeX font names, including
    # the special name "regular" for the same font
    # used in regular text.

### AXES
# default face and edge color, default tick sizes,
# default font sizes for tick labels, and so on. See
# http://matplotlib.sourceforge.net/api/axes_api.html#module-matplotlib.axes
#axes.hold      : True # whether to clear the axes by default on
#axes.facecolor : white # axes background color
#axes.edgecolor : black # axes edge color
#axes.linewidth : 1.0 # edge linewidth
#axes.grid      : False # display
grid or not
#axes.titlesize : large # fontsize of the axes title
#axes.labelsize : medium # fontsize of the x any y labels
#axes.labelcolor : black
#axes.axisbelow : False # whether axis gridlines and ticks are below
                        # the axes elements (lines, text, etc)
#axes.formatter.limits : -7, 7 # use scientific notation if log10
                        # of the axis range is smaller than the
                        # first or larger than the second
#axes.unicode_minus : True # use unicode for the minus symbol
                        # rather than hyphen. See http://en.wikipedia.org/wiki/Plus_sign#Plus_sign

#polaraxes.grid : True # display grid on polar axes
#axes3d.grid     : True # display grid on 3d axes

### TICKS
# see http://matplotlib.sourceforge.net/api/axis_api.html#matplotlib.axis.Tick
#xtick.major.size : 4 # major tick size in points
#xtick.minor.size : 2 # minor tick
size in points
#xtick.major.pad : 4 # distance to major tick label in points
#xtick.minor.pad : 4 # distance to the minor tick label in points
#xtick.color     : k # color of the tick labels
#xtick.labelsize : medium # fontsize of the tick labels
#xtick.direction : in # direction: in or out

#ytick.major.size : 4 # major tick size in points
#ytick.minor.size : 2 # minor tick size in points
#ytick.major.pad : 4 # distance to major tick label in points
#ytick.minor.pad : 4 # distance to the minor tick label in points
#ytick.color     : k # color of the tick labels
#ytick.labelsize : medium # fontsize of the tick labels
#ytick.direction : in # direction: in or out

```

### ### GRIDS

```
#grid.color      : black # grid color
#grid.linestyle  : :     # dotted
#grid.linewidth  : 0.5   # in points
```

### ### Legend

```
#legend.fancybox : False # if True, use a rounded box for the
                        # legend, else a rectangle
#legend.isaxes   : True
#legend.numpoints : 2    # the number of points in the legend line
#legend.fontsize  : large
#legend.pad       : 0.0  # deprecated; the fractional whitespace inside the legend border
#legend.borderpad : 0.5  # border whitespace in fontsize units
#legend.markerscale : 1.0 # the relative size of legend markers vs. original
# the following dimensions are in axes coords
#legend.labelsep  : 0.010 # the vertical space between the legend entries
#legend.handlelen : 0.05  # the length of the legend lines
#legend.handletextsep : 0.02 # the space between the legend line and legend text
#legend.axespad   : 0.02  # the border between the axes and legend edge
#legend.shadow    : False
```

### ### FIGURE

```
# See http://matplotlib.sourceforge.net/api/figure\_api.html#matplotlib.figure.Figure
#figure.figsize  : 8, 6  # figure size in inches
#figure.dpi      : 80    # figure dots
                    per inch
#figure.facecolor : 0.75 # figure facecolor; 0.75 is scalar gray
#figure.edgecolor : white # figure edgecolor
```

```
# The figure subplot parameters. All dimensions are fraction of the
# figure width or height
```

```
#figure.subplot.left : 0.125 # the left side of the subplots of the figure
#figure.subplot.right : 0.9  # the right side of the subplots of the figure
#figure.subplot.bottom : 0.1 # the bottom of the subplots of the figure
#figure.subplot.top : 0.9    # the top of the subplots of the figure
#figure.subplot.wspace : 0.2 # the amount of width reserved for blank space between subplots
#figure.subplot.hspace : 0.2 # the amount of height reserved for white space between subplots
```

### ### IMAGES

```
#image.aspect : equal      # equal | auto | a number
#image.interpolation : bilinear # see help(imshow) for options
#image.cmap    : jet       # gray | jet etc...
#image.lut     : 256       # the size of the colormap lookup table
#image.origin  : upper
                    # lower | upper
#image.resample : False
```

```

### CONTOUR PLOTS
#contour.negative_linestyle : dashed # dashed | solid

### Agg rendering
### Warning: experimental, 2008/10/10
#agg.path.chunksize : 0      # 0 to disable; values in the range
                             # 10000 to 100000 can improve speed slightly
                             # and prevent an Agg rendering failure
                             # when plotting very large data sets,
                             # especially if they are very gappy.
                             # It may cause minor artifacts, though.
                             # A value of 20000 is probably a good
                             # starting point.

### SAVING FIGURES
#path.simplify : False # When True, simplify paths by removing "invisible"
                    # points to reduce file size and increase rendering
                    # speed
#path.simplify_threshold : 0.1 # The threshold of similarity
                                below which
                                # vertices will be removed in the simplification
                                # process

# the default savefig params can be different from the display params
# Eg, you may want a higher resolution, or to make the figure
# background white
#savefig.dpi      : 100  # figure dots per inch
#savefig.facecolor : white # figure facecolor when saving
#savefig.edgecolor : white # figure edgecolor when saving

#cairo.format      : png  # png, ps, pdf, svg

# tk backend params
#tk.window_focus  : False # Maintain shell focus for TkAgg
#tk.pythoninspect : False # tk sets PYTHONINSEPCT

# ps backend params
#ps.papersize     : letter # auto, letter, legal, ledger, A0-A10, B0-B10
#ps.useafm        : False  # use of afm fonts, results in small files
#ps.usedistiller  : False  # can be: None, ghostscript or xpdf
                    # Experimental: may produce smaller files.

                    # xpdf intended for production of publication quality files,
                    # but requires ghostscript, xpdf and ps2eps
#ps.distiller.res : 6000   # dpi
#ps.fonttype      : 3      # Output Type 3 (Type3) or Type 42 (TrueType)

# pdf backend params

```



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enthought/pyface/action/images:

- action.png | Eclipse

P6

# CREATOR: The GIMP's PNM Filter Version 1.0

16 16

255

-=\*C[?e\*C[?e?e\*C[?e?e?e\*C[?e)BZ?e)BZ?e)BZ?e)BZ?e)BZhIuKx4So-G`

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enthought/units/plugin/images:  
error.png | GV  
quantity.png | OOo  
  
P6  
# CREATOR: The GIMP's PNM Filter Version 1.0  
16 16  
255  
sOoOoHf3HZ"0;xKjOo3HZOoOoOoOo3HZxOo3HZ3HZx  
wxWindows Library Licence, Version 3.1  
=====

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parallel.png | GV

save.png | Crystal

x-axis.png | Eclipse

y-axis.png | Eclipse

z-axis.png | Eclipse

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P6

# CREATOR: The GIMP's PNM Filter Version 1.0

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k<(v\$v"s\*{Je&u#})(&#r){9&,01.,)&#m{X@&1;AB=4-)%!zS{"}.<IQLTh+'#lz%u\*4DSac^8(\$|#us {,6GWkj8}\$

```

l|s>$+3CR^7($t9&w&*.:Uzqs$(+Pt{izr!%(s~yz!s"%7u
|wk{,w!#lged~z
t(tK 0+
{ vnK u-(
|
xsk{2x*%
{
xt
n=+z&F}z wso!m}[0]~~}
|z
xuro!{QG~yyy
x vuspo,s6yD0vrqr)z-vS^A><}P

```

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```

-----
about.png      Crystal Project
crit_error.png  Crystal Project
debug.png      Crystal Project
error.png      Crystal Project
warning.png    Crystal Project

```

P6

# CREATOR: The GIMP's PNM Filter Version 1.0

16 16

255

```

ItEo?e7Yx?e7Yx?e7Yx?e7Yx?e7Yx[?e?e?e*C[P?e?e*C[P?e?e*C[
_*C[Ly

```

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Version 2, June 1991

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preferences.png CP

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  closed\_folder.png | Nuvola

  document.png | GV

  open\_folder.png | Nuvola

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P6

# CREATOR: The GIMP's PNM Filter Version 1.0

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P6

# CREATOR: The GIMP's PNM Filter Version 1.0

16 16

255

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<XmΓr.CSZp(5ZpΓrkx!6H\_y,:ZpΓrYcmOdunnnzzz-9I>O\l@

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entthought/pyface/ui/wx/images:

application.ico | Entthought

heading\_level\_1.png | Entthought

warning.png | GV

<?xml version="1.0" standalone="no"?> <!--\*- mode: xml -\*-->

<!DOCTYPE glade-interface SYSTEM "http://glade.gnome.org/glade-2.0.dtd">

```

<glade-interface>

<widget class="GtkDialog" id="dialog_lineprops">
  <property name="visible">True</property>
  <property name="title" translatable="yes">Line Properties</property>
  <property name="type">GTK_WINDOW_TOPLEVEL</property>
  <property name="window_position">GTK_WIN_POS_NONE</property>
  <property name="modal">False</property>
  <property name="resizable">True</property>
  <property name="destroy_with_parent">False</property>
  <property name="decorated">True</property>
  <property name="skip_taskbar_hint">False</property>
  <property name="skip_pager_hint">False</property>
  <property name="type_hint">GDK_WINDOW_TYPE_HINT_DIALOG</property>
  <property name="gravity">GDK_GRAVITY_NORTH_WEST</property>
  <property name="has_separator">True</property>

  <child internal-child="vbox">
    <widget class="GtkVBox" id="dialog-vbox2">
      <property
name="visible">True</property>
      <property name="homogeneous">False</property>
      <property name="spacing">0</property>

      <child internal-child="action_area">
        <widget class="GtkHButtonBox" id="dialog-action_area2">
          <property name="visible">True</property>
          <property name="layout_style">GTK_BUTTONBOX_END</property>

          <child>
            <widget class="GtkButton" id="dialog_lineprops_cancelbutton">
              <property name="visible">True</property>
              <property name="can_default">True</property>
              <property name="can_focus">True</property>
              <property name="label">gtk-cancel</property>
              <property name="use_stock">True</property>
              <property name="relief">GTK_RELIEF_NORMAL</property>
              <property name="focus_on_click">True</property>
              <property name="response_id">-6</property>
              <signal name="clicked" handler="on_dialog_lineprops_cancelbutton_clicked" last_modification_time="Mon,
07 Nov 2005 20:52:05 GMT"/>
            </widget>
          </child>

          <child>
            <widget class="GtkButton" id="dialog_lineprops_okbutton">

```

```

    <property name="visible">True</property>
    <property name="can_default">True</property>
    <property name="can_focus">True</property>
    <property name="label">gtk-ok</property>
    <property name="use_stock">True</property>
    <property name="relief">GTK_RELIEF_NORMAL</property>
    <property name="focus_on_click">True</property>
    <property name="response_id">-5</property>
    <signal name="clicked" handler="on_dialog_lineprops_okbutton_clicked" last_modification_time="Mon, 07
Nov 2005 20:51:43 GMT"/>
  </widget>
</child>
</widget>
<packing>
  <property name="padding">0</property>
  <property name="expand">False</property>
  <property name="fill">True</property>
  <property name="pack_type">GTK_PACK_END</property>
</packing>
</child>

  <child>
<widget class="GtkVBox" id="vbox_lineprops">
  <property name="visible">True</property>
  <property name="homogeneous">False</property>

  <property name="spacing">0</property>

  <child>
    <widget class="GtkComboBox" id="combobox_lineprops">
      <property name="visible">True</property>
      <property name="items" translatable="yes"></property>
      <signal name="changed" handler="on_combobox_lineprops_changed" last_modification_time="Mon, 07 Nov
2005 20:40:07 GMT"/>
    </widget>
    <packing>
      <property name="padding">0</property>
      <property name="expand">True</property>
      <property name="fill">True</property>
    </packing>
  </child>

  <child>
    <widget class="GtkFrame" id="frame6">
      <property name="visible">True</property>
      <property name="label_xalign">0</property>
      <property name="label_yalign">0.5</property>
      <property name="shadow_type">GTK_SHADOW_NONE</property>

```



```

    <child>
<widget class="GtkAlignment" id="alignment9">
  <property name="visible">True</property>
  <property name="xalign">0.5</property>
  <property
name="yalign">0.5</property>
  <property name="xscale">1</property>
  <property name="yscale">1</property>
  <property name="top_padding">0</property>
  <property name="bottom_padding">0</property>
  <property name="left_padding">12</property>
  <property name="right_padding">0</property>

<child>
  <widget class="GtkVBox" id="vbox5">
    <property name="visible">True</property>
    <property name="homogeneous">False</property>
    <property name="spacing">0</property>

    <child>
<widget class="GtkTable" id="table_lineprops">
  <property name="visible">True</property>
  <property name="n_rows">2</property>
  <property name="n_columns">3</property>
  <property name="homogeneous">False</property>
  <property name="row_spacing">0</property>
  <property name="column_spacing">0</property>

<child>
  <widget class="GtkLabel" id="label11">
    <property name="visible">True</property>
    <property name="label"
translatable="yes">Marker</property>
    <property name="use_underline">False</property>
    <property name="use_markup">False</property>
    <property name="justify">GTK_JUSTIFY_LEFT</property>
    <property name="wrap">False</property>
    <property name="selectable">False</property>
    <property name="xalign">0.5</property>
    <property name="yalign">0.5</property>
    <property name="xpad">0</property>
    <property name="ypad">0</property>
  </widget>
<packing>
  <property name="left_attach">0</property>
  <property name="right_attach">1</property>
  <property name="top_attach">1</property>

```

```

    <property name="bottom_attach">2</property>
    <property name="x_options">fill</property>
    <property name="y_options"></property>
  </packing>
</child>

<child>
  <widget class="GtkComboBox" id="combobox_linestyles">
    <property name="visible">True</property>

    <property name="items" translatable="yes"></property>
      <signal name="changed" handler="on_combobox_linestyle_changed" last_modification_time="Mon, 07 Nov
2005 20:43:56 GMT"/>
    </widget>
  <packing>
    <property name="left_attach">1</property>
    <property name="right_attach">2</property>
    <property name="top_attach">0</property>
    <property name="bottom_attach">1</property>
    <property name="y_options">fill</property>
  </packing>
</child>

<child>
  <widget class="GtkComboBox" id="combobox_markers">
    <property name="visible">True</property>
    <property name="items" translatable="yes"></property>
    <signal name="changed" handler="on_combobox_marker_changed" last_modification_time="Mon, 07 Nov
2005 20:43:14 GMT"/>
  </widget>
  <packing>
    <property name="left_attach">1</property>
    <property name="right_attach">2</property>
    <property name="top_attach">1</property>

    <property name="bottom_attach">2</property>
    <property name="x_options">fill</property>
    <property name="y_options">fill</property>
  </packing>
</child>

<child>
  <widget class="GtkColorButton" id="colorbutton_linestyle">
    <property name="visible">True</property>
    <property name="can_focus">True</property>
    <property name="use_alpha">True</property>
    <property name="title" translatable="yes">Line color</property>
    <property name="focus_on_click">True</property>

```

```

    <signal name="color_set" handler="on_colorbutton_linestyle_color_set" last_modification_time="Mon, 07 Nov
2005 22:13:40 GMT"/>
  </widget>
  <packing>
    <property name="left_attach">2</property>
    <property name="right_attach">3</property>
    <property name="top_attach">0</property>
    <property name="bottom_attach">1</property>
    <property name="x_options">fill</property>
    <property
name="y_options"></property>
  </packing>
</child>

<child>
  <widget class="GtkColorButton" id="colorbutton_markerface">
    <property name="visible">True</property>
    <property name="can_focus">True</property>
    <property name="use_alpha">False</property>
    <property name="title" translatable="yes">Marker color</property>
    <property name="focus_on_click">True</property>
    <signal name="color_set" handler="on_colorbutton_markerface_color_set" last_modification_time="Mon, 07
Nov 2005 22:14:00 GMT"/>
  </widget>
  <packing>
    <property name="left_attach">2</property>
    <property name="right_attach">3</property>
    <property name="top_attach">1</property>
    <property name="bottom_attach">2</property>
    <property name="x_options">fill</property>
    <property name="y_options"></property>
  </packing>
</child>

<child>
  <widget class="GtkLabel" id="label_linestyle">

  <property name="visible">True</property>
  <property name="label" translatable="yes">Line style</property>
  <property name="use_underline">False</property>
  <property name="use_markup">False</property>
  <property name="justify">GTK_JUSTIFY_LEFT</property>
  <property name="wrap">False</property>
  <property name="selectable">False</property>
  <property name="xalign">0.5</property>
  <property name="yalign">0.5</property>
  <property name="xpad">0</property>
  <property name="ypad">0</property>

```

```

</widget>
<packing>
  <property name="left_attach">0</property>
  <property name="right_attach">1</property>
  <property name="top_attach">0</property>
  <property name="bottom_attach">1</property>
  <property name="x_options">fill</property>
  <property name="y_options"></property>
</packing>
</child>
</widget>
<packing>
  <property
name="padding">0</property>
  <property name="expand">True</property>
  <property name="fill">True</property>
</packing>
  </child>
</widget>
</child>
</widget>
  </child>

  <child>
<widget class="GtkLabel" id="fram_lineprops">
  <property name="visible">True</property>
  <property name="label" translatable="yes">&lt;b&gt;Line properties&lt;/b&gt;</property>
  <property name="use_underline">False</property>
  <property name="use_markup">True</property>
  <property name="justify">GTK_JUSTIFY_LEFT</property>
  <property name="wrap">False</property>
  <property name="selectable">False</property>
  <property name="xalign">0.5</property>
  <property name="yalign">0.5</property>
  <property name="xpad">0</property>
  <property name="ypad">0</property>
</widget>
<packing>
  <property name="type">label_item</property>
</packing>
  </child>
</widget>
<packing>
  <property name="padding">0</property>

  <property name="expand">True</property>
  <property name="fill">True</property>
</packing>

```

```
</child>
</widget>
<packing>
  <property name="padding">0</property>
  <property name="expand">True</property>
  <property name="fill">True</property>
</packing>
  </child>
</widget>
</child>
</widget>
```

</glade-interface>

Description:

Standard Traits UI Icons and Themes.

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Applicable Images:

- All

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| filename                   | source     |
|----------------------------|------------|
| 0_weight.gif               | D. Morrill |
| 1_weight.gif               | D. Morrill |
| 2_weight.gif               | D. Morrill |
| 3_weight.gif               | D. Morrill |
| 4_weight.gif               | D. Morrill |
| 5_weight.gif               | D. Morrill |
| 6_weight.gif               | D. Morrill |
| 7_weight.gif               | D. Morrill |
| 8_weight.gif               | D. Morrill |
| bottom_center_position.gif | D. Morrill |
| bottom_left_position.gif   | D. Morrill |
| bottom_right_position.gif  | D. Morrill |
| center_align.gif           | D. Morrill |

center\_left\_position.gif D. Morrill  
center\_position.gif D. Morrill  
center\_right\_position.gif D. Morrill  
left\_align.gif D. Morrill  
right\_align.gif D. Morrill  
top\_center\_position.gif  
D. Morrill  
top\_left\_position.gif D. Morrill  
top\_right\_position.gif D. Morrill

P6

24 24

255

```
:u*!xnY~C~C~C}A|@{ @ { ?z?y?ab[,jdwMrwzyu } }zywtrY^d&Ytz@iPvtwplhebgBu1\w&YtzH~Mwl`]|ZzWvUu`C  
uSt+jzKSyWv2az0`y^x.]w-  
]vUwBuSt+jzLUyAsSt+jzJPHMLHA|=w9s2o+i(d\zSt+jzDzILMKH~By=u6o.i(c!]|UuSt+jz>uAxCzDzBy@w<t7p0  
j*e#_}YyStSt+jz5n7p;r;s^wNdwJ`tnfyIavE]rAZnGcStSt+jz,f.i0j0k0?U*C!OkStSt+jz#_}&a~(b(a~/>T*B'RmStSt+j  
gWvYyZyQn.<S*B*SnStSt+j:vStStHfVfxScuy(QkStWw[W`Dd{D`uC`uC_tB_tB^sA^sA^sEg`n
```

P6

24 24

255

```
k<(v$v"s*{Je&u#})(&#r){9&,01..}&#m{X@&1;AB=4-)%!zS{"}.<IQLAd+'#lz%u*4DSacXX($|#us  
{,6GWkq]S$ |s>$+3CR^aVMt9&w&*.:jzqs$(+/{izr!%(+~yjs!"% '{  
|wk{,w!#_~z  
t(tK !! &  
{ vnK u  
|  
xsk{2x  
{  
xt  
n=+zD}z wso!m}[0]~~}  
|z  
xurol{QG~yyy  
x vuspo,s6yD0vrqr)z-vS^A><}P
```

P6

# CREATOR: The GIMP's PNM Filter Version 1.0

16 16

255

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- Crystal: LGPL
- OOo: LGPL
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-----  
actor.png | Nuvola  
camera.png | GV  
coordinate.png | Crystal  
filter.png | Nuvola  
lookuptable.png | OOO  
mapper.png | Crystal  
polydata.png | Nuvola  
property.png | Eclipse  
reader.png | Crystal  
render.png | Crystal  
renderwindowinteractor.png | Nuvola  
renderwindow.png | Crystal

source.png | OOO  
texture.png | Eclipse  
window.png | Eclipse  
writer.png | GV

P6

# CREATOR: The GIMP's PNM Filter Version 1.0

16 16

255

&&&FFFpppFFF%%(((FFF \$\$\$\$\$\$\$\$\$FFF %%%FFFmmmFFFFFFF hhh444yyy::hhh

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-----  
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lookuptable.png | OOO  
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```
#!/usr/bin/env bash
# MIT licenses will each have a unique MD5 sum since they contain a unique copyright.
# This script will read the license file into memory, strip out the copyright, and then
# generate an MD5 that is not unique and can be matched against a known MIT MD5 sum.
set -e

calculate_md5_sum() {
    local FILE="$1"

    # Read the contents of the file into a variable
    local raw_license_text=$(<"$FILE")

    # Convert to lowercase
    local lowercase_license_text="${raw_license_text,,}"

    # Remove anything before the beginning of the license
    local stripped_license_text=$(awk '/permission is hereby granted,/{p=1} p' <<< "$lowercase_license_text")

    # Replace line breaks (Windows or Linux) with a single space
    local replaced_linebreaks=$(tr '\r\n' ' ' <<< "$stripped_license_text")

    # Replace multiple spaces with a single space
    local normalized_license_text=$(tr -s ' ' <<< "$replaced_linebreaks")

    # Strip away any non-printable, non-whitespace characters
    local clean_unprintable_license_text=$(tr
    -cd '[:print:][:space:]' <<< "$normalized_license_text")

    # Remove punctuation and special characters
    local clean_license_text=$(echo "$clean_unprintable_license_text" | sed 's/[^a-zA-Z ]//g')

    # Calculate the MD5 sum of the cleaned license text
    local MD5SUM=$(echo -n "$clean_license_text" | md5sum | awk '{print $1}')
```

```

# Output the MD5 sum
echo "$MD5SUM"
}

# Check if the FILE argument is provided
if [ -z "$1" ]; then
echo "Please provide a FILE as an argument."
exit 1
fi

# Invoke the function with the provided FILE argument
calculate_md5_sum "$1"
These are my best guesses at where each icon came from,
this file should be updated whenever icons change.

```

```

filename          source
-----
about.png        Python Software Foundation
application.ico   Gael Varoquaux
P6
24 24
255
pVpTSXWVZc\|pgg|okcQU^nq[SPVSPdLPU[ituk\UQMIEXhOQT[`g`UZWURPuZW[UTXQPWNMUJIRiTm
Please see the LICENSE file.
$Id: LICENSE.txt 15 2009-10-30 18:26:52Z sarkiss $
Copyright (c) [2008-2010], Sargis Dallakyan, The Scripps Research Institute
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```

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traitsui/wx/images:

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cb\_hover\_on.png | Enthought  
cb\_off.png | Enthought  
cb\_on.png  
| Enthought  
file.png | Enthought  
frame.ico | Enthought  
group.png | Nuvola  
item.png | Eclipse  
list\_editor.png | Enthought  
nb\_open.png | Enthought  
object.png | Enthought  
open.png | Nuvola  
table\_add.png | OOo  
table\_colors.png | GV  
table\_delete.png | OOo  
table\_delete\_synthetic.png | OOo  
table\_display.png | OOo  
table\_move\_down.png | GV  
table\_move\_up.png | GV  
table\_no\_sort.png | OOo  
table\_prefs.png | OOo  
table\_search.png | OOo  
table\_synthetic.png | OOo  
table\_undelete.png | OOo

# 1.343 godspeed\_godlib 1.0

## 1.343.1 Available under license :

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Version 2.1, February 1999

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=====  
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=====

Current Maintainers:

Gregory R. Warnes <Gregory.R.Warnes@Pfizer.com>

Christopher Blunck <blunck2@gst.com>

Original Authors:

Cayce Ullman <c\_ullman@yahoo.com>

Brian Matthews <blm@blmatthews.com>

Contributions by:

Brad Knotwell <b.knotwell@f5.com>

Mark Bucciarelli <mark@hubcapconsulting.com> (ported WSDL  
client from ZSI)

Ivan R. Judson <judson@mcs.anl.gov> (Globus support)

Kirk Strauser <kirk@daycos.com>

Antonio Beamud Montero <antonio.beamud@linkend.com> (patches  
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\*\*\*\*\*/

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/Sane/\_sane.c

No license file was found, but licenses were detected in source scan.

/\*

\* The Python Imaging Library

\* \$Id: Fill.c 2134 2004-10-06 08:55:20Z fredrik \$

\*

\* fill image with constant pixel value

\*

\* history:

\* 95-11-26 fl moved from Imaging.c

\* 96-05-17 fl added radial fill, renamed wedge to linear

\* 98-06-23 fl changed ImageFill signature

\*

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\*

\* See the README file for information on usage and redistribution.

\*/

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/libImaging/Fill.c

No license file was found, but licenses were detected in source scan.

/\*

\* The Python Imaging Library

\* \$Id: RankFilter.c 2929 2006-11-22 21:12:55Z fredrik \$

\*

\* min, max, median filters

\*

\* history:

\* 2002-06-08 fl Created  
\*  
\* Copyright (c) Secret Labs AB 2002. All rights reserved.  
\*  
\* See the README file for information on usage and redistribution.  
\*/  
/\* Fast rank algorithm (due to Wirth), based on public domain code  
by Nicolas Devillard, available at <http://ndevilla.free.fr> \*/

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/libImaging/RankFilter.c  
No license file was found, but licenses were detected in source scan.

# image from the file; higher resolutions are encoded in a proprietary

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/PIL/PcdImagePlugin.py  
No license file was found, but licenses were detected in source scan.

# Copyright (c) Secret Labs AB 1997. All rights reserved.

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/Scripts/image2py.py  
\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/Scripts/gifmaker.py  
No license file was found, but licenses were detected in source scan.

The Python Imaging Library

\$Id: README 2939 2006-12-03 13:43:51Z fredrik \$

Release 1.1.6 (December 3, 2006)

=====  
The Python Imaging Library 1.1.6  
=====

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-----

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-----

## Introduction

---

The Python Imaging Library (PIL) adds image processing capabilities to your Python environment. This library provides extensive file format support, an efficient internal representation, and powerful image processing capabilities.

This source kit has been built and tested on Windows NT/2000/XP, Mac OS X, and most major Unix platforms (including Alpha, Solaris, and Intel systems). We believe that it should work on most modern platforms (people are using it on Crays, after all ;-)

The main distribution site for this software is:

<http://www.pythonware.com/products/pil/>

That site also contains information about free and commercial support options, PIL add-ons, answers to frequently asked questions, and more.

Development versions (alphas, betas) are available here:

<http://effbot.org/downloads/>

The PIL handbook is not included in this distribution; to get the latest version, check:

<http://www.pythonware.com/library/>  
<http://effbot.org/books/imagingbook/> (drafts)

For installation and licensing details, see below.

---

## Support Options

---

### + Commercial Support

Secret Labs (PythonWare) offers support contracts for companies using the Python Imaging Library in commercial applications, and in mission-critical environments. The support contract includes technical support, bug fixes, extensions to the PIL library, sample applications, and more.

For the full story, check:

<http://www.pythonware.com/products/pil/support.htm>

+ Free Support

For support and general questions on the Python Imaging Library, send e-mail to the Image SIG mailing list:

[image-sig@python.org](mailto:image-sig@python.org)

You can join the Image SIG by sending a mail to:

[image-sig-request@python.org](mailto:image-sig-request@python.org)

Put "subscribe" in the message body to automatically subscribe to the list, or "help" to get additional information. Alternatively, you can send your questions to the Python mailing list, [python-list@python.org](mailto:python-list@python.org), or post them to the newsgroup [comp.lang.python](http://comp.lang.python). **DO NOT SEND SUPPORT QUESTIONS TO PYTHONWARE ADDRESSES.**

-----  
Software License  
-----

The  
Python Imaging Library is

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-----  
Build instructions (all platforms)  
-----

For a list of changes in this release, see the CHANGES document.

0. If you're in a hurry, try this:

```
$ gunzip Imaging-1.1.6.tar.gz
$ tar xvf Imaging-1.1.6.tar
$ cd Imaging-1.1.6
$ python setup.py install
```

If you prefer to know what you're doing, read on.

1. Prerequisites.

If you need any of the features described below, make sure you have the necessary libraries before building PIL.

| feature | library |
|---------|---------|
|---------|---------|

-----  
JPEG support      libjpeg (6a or 6b)

```
http://www.ijg.org
http://www.ijg.org/files/jpegsrc.v6b.tar.gz
ftp://ftp.uu.net/graphics/jpeg/
```

PNG support      zlib (1.2.3 or later is recommended)

```
http://www.gzip.org/zlib/
```

OpenType/TrueType support      freetype2 (2.1.3 or later is recommended)

```
http://www.freetype.org
http://freetype.sourceforge.net
```

If you have a recent Linux version, the libraries provided with

the operating system usually work just fine. If some library is missing, installing a prebuilt version (jpeg-devel, zlib-devel, etc) is usually easier than building from source.

If you're using Mac OS X, you can use the 'fink' tool to install missing libraries (also see the Mac OS X section below).

Similar tools are available for many other platforms (such as 'apt-get' for Debian).

2. To build under Python 1.5.2, you need to install the stand-alone version of the distutils library:

<http://www.python.org/sigs/distutils-sig/download.html>

For newer releases, the distutils library is included in the Python standard library.

3. If you didn't build Python from sources, make sure you have Python's build support files on your machine. If you've downloaded a prebuilt package (e.g. a Linux RPM), you probably need additional developer packages. Look for packages named "python-devel", "py-dev", or similar.
4. When you have everything you need, unpack the PIL distribution (the file `Imaging-1.1.6.tar.gz`) in your Python extensions directory (if you have one, that is. If not, feel free to unpack it in any other suitable directory).

```
$ cd Python-2.4/Extensions # example
$ gunzip Imaging-1.1.6.tar.gz
$ tar xvf Imaging-1.1.6.tar
```

5. Build the library. We recommend that you do an in-place build, and run the self test before installing.

```
$ cd Imaging-1.1.6
$ python setup.py build_ext -i
$ python selftest.py
```

When the build finishes, a summary report is shown. Make sure that the optional components you need are included.

-----  
PIL 1.1.6 BUILD SUMMARY  
-----

\*\*\* TKINTER support not available (Tcl/Tk 8.3 libraries needed)  
--- JPEG support ok  
--- ZLIB (PNG/ZIP) support ok  
--- FREETYPE support ok  
-----

If the build script won't find a given component, you can edit the setup.py file and set the appropriate ROOT variable. For details, see instructions in the file.

6. If the setup.py and selftest.py commands finish without any errors, you're ready to install the library:

```
$ python setup.py install
```

(depending on how Python has been installed on your machine, you might have to log in as a superuser to run the 'install' command.)

-----  
Additional notes for Mac OS X  
-----

On Mac OS X you will usually install additional software such as libjpeg or freetype with the "fink" tool, and then it ends up in "/sw". If you have installed the libraries elsewhere, you may have to tweak the "setup.py" file before building.

-----  
Additional notes for Windows  
-----

On Windows, you need to tweak the ROOT settings in the "setup.py" file, to make it find the external libraries. See comments in the file for details.

Make sure to build PIL and the external libraries with the same runtime linking options as was used for the Python interpreter (usually /MD, under Visual Studio).

Note that most Python distributions include libraries compiled for Microsoft Visual Studio. To build extensions using other tool chains, see the "Using non-Microsoft compilers on Windows" section in the distutils handbook:

<http://www.python.org/doc/current/inst/non-ms-compilers.html>

For additional information on how to build extensions using the popular MinGW compiler, see:

<http://mingw.org> (compiler)

<http://sebsauvage.net/python/mingw.html> (build instructions)

<http://sourceforge.net/projects/gnuwin32> (prebuilt libraries)

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/README

No license file was found, but licenses were detected in source scan.

# version.

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/PIL/JpegImagePlugin.py

No license file was found, but licenses were detected in source scan.

/\*

\* The Python Imaging Library

\* \$Id: Palette.c 2591 2005-12-08 21:25:29Z fredrik \$

\*

\* imaging palette object

\*

\* history:

\* 1996-05-05 fl Added to library

\* 1996-05-27 fl Added colour mapping stuff

\* 1997-05-12 fl Support RGBA palettes

\* 2005-02-09 fl Removed grayscale entries from web palette

\*

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\* Copyright (c) Fredrik Lundh 1995-1997.

\*

\* See the README file for information on usage and redistribution.

\*/

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/libImaging/Palette.c

No license file was found, but licenses were detected in source scan.

```
/*
* The Python Imaging Library
* $Id: ModeFilter.c 2134 2004-10-06 08:55:20Z fredrik $
*
* mode filter
*
* history:
* 2002-06-08 fl Created (based on code from IFUNC95)
* 2004-10-05 fl Rewritten; use a simpler brute-force algorithm
*
* Copyright (c) Secret Labs AB 2002-2004. All rights reserved.
*
* See the README file for information on usage and redistribution.
*/
```

Found in path(s):

```
* /opt/cola/permits/1304943026_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/libImaging/ModeFilter.c
No license file was found, but licenses were detected in source scan.
```

# Copyright (c) 1997-2004 by Secret Labs AB. All rights reserved.

Found in path(s):

```
* /opt/cola/permits/1304943026_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/PIL/PdfImagePlugin.py
No license file was found, but licenses were detected in source scan.
```

# Copyright (c) Secret Labs AB 1997-2004. All rights reserved.

Found in path(s):

```
* /opt/cola/permits/1304943026_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/PIL/WmfImagePlugin.py
* /opt/cola/permits/1304943026_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/PIL/GimpPaletteFile.py
No license file was found, but licenses were detected in source scan.
```

```
/*
* The Python Imaging Library
* $Id: Filter.c 2134 2004-10-06 08:55:20Z fredrik $
*
* apply convolution kernel to image
*
* history:
* 1995-11-26 fl Created, supports 3x3 kernels
* 1995-11-27 fl Added 5x5 kernels, copy border
* 1999-07-26 fl Eliminated a few compiler warnings
* 2002-06-09 fl Moved kernel definitions to Python
* 2002-06-11 fl Support floating point kernels
* 2003-09-15 fl Added ImagingExpand helper
*
* Copyright (c) Secret Labs AB 1997-2002. All rights reserved.
* Copyright (c) Fredrik Lundh 1995.
```

\*  
\* See the README file for information on usage and redistribution.  
\*/

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/libImaging/Filter.c

No license file was found, but licenses were detected in source scan.

STARTFONT 2.1

COMMENT \$XConsortium: courB08.bdf,v 1.5 94/04/10 21:46:11 gildea Exp \$

COMMENT

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COMMENT

COMMENT

FONT -Adobe-Courier-Bold-R-Normal--11-80-100-100-M-60-ISO8859-1

SIZE 8 100 100

FONTBOUNDINGBOX 8 11 -1 -2

STARTPROPERTIES

22

COMMENT Begin LogicalFontDescription

FONTNAME\_REGISTRY ""

FAMILY\_NAME "Courier"  
FOUNDRY "Adobe"  
WEIGHT\_NAME "Bold"  
SLANT "R"  
SETWIDTH\_NAME "Normal"  
ADD\_STYLE\_NAME ""  
PIXEL\_SIZE 11  
POINT\_SIZE 80  
RESOLUTION\_X 100  
RESOLUTION\_Y 100  
SPACING "M"  
AVERAGE\_WIDTH 60  
CHARSET\_REGISTRY "ISO8859"  
CHARSET\_ENCODING "1"  
COMMENT END LogicalFontDescription  
CHARSET\_COLLECTIONS "ASCII ISO8859-1 ADOBE-STANDARD"  
FACE\_NAME "Courier Bold"  
COPYRIGHT "Copyright (c) 1987 Adobe Systems, Inc., Portions Copyright 1988 Digital Equipment Corp."  
COMMENT \*\*\*\*\* end of inserted font properties  
FONT\_ASCENT 8  
FONT\_DESCENT 2  
CAP\_HEIGHT 6  
X\_HEIGHT 5  
ENDPROPERTIES  
CHARS 194  
STARTCHAR space  
ENCODING 32  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 1 1 0 0  
BITMAP  
00  
ENDCHAR  
STARTCHAR exclam  
ENCODING 33  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 2 6 1 0  
BITMAP  
C0  
C0  
C0  
C0  
00  
C0  
ENDCHAR  
STARTCHAR quotedbl  
ENCODING 34

SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 3 1 3  
BITMAP  
A0  
A0  
A0  
ENDCHAR  
STARTCHAR numbersign  
ENCODING 35  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 -1  
BITMAP  
50  
50  
F8  
50  
50  
F8  
50  
50  
ENDCHAR  
STARTCHAR  
dollar  
ENCODING 36  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 9 0 -1  
BITMAP  
20  
78  
C8  
F0  
78  
18  
D8  
F0  
20  
ENDCHAR  
STARTCHAR percent  
ENCODING 37  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
E0  
A8



F0  
20  
78  
A8  
38  
ENDCHAR  
STARTCHAR ampersand  
ENCODING 38  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
38  
60  
30  
7C  
D8  
7C  
ENDCHAR  
STARTCHAR quoteright  
ENCODING 39  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 3 1 4  
BITMAP  
60  
40  
80  
ENDCHAR  
STARTCHAR parenleft  
ENCODING 40  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 8 1 -1  
BITMAP  
20  
40  
C0  
C0  
C0  
C0  
40  
20  
ENDCHAR  
STARTCHAR parenright  
ENCODING 41  
SWIDTH 600 0  
DWIDTH 6 0

BBX 3 8 1 -1  
BITMAP  
80  
40  
60  
60  
60  
40  
80  
ENDCHAR  
STARTCHAR asterisk  
ENCODING 42  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 4 0 3  
BITMAP  
20  
F0  
60  
90  
ENDCHAR  
STARTCHAR plus  
ENCODING 43  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 5 0 1  
BITMAP  
20  
20  
F8  
20  
20  
ENDCHAR  
STARTCHAR comma  
ENCODING 44  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 3 1 -2  
BITMAP  
60  
40  
80  
ENDCHAR  
STARTCHAR minus  
ENCODING 45  
SWIDTH 600 0  
DWIDTH 6 0

BBX 5 1 0 3  
BITMAP  
F8  
ENDCHAR  
STARTCHAR  
period  
ENCODING 46  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 2 1 1 0  
BITMAP  
C0  
ENDCHAR  
STARTCHAR slash  
ENCODING 47  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 8 1 -1  
BITMAP  
10  
10  
20  
20  
40  
40  
80  
80  
ENDCHAR  
STARTCHAR zero  
ENCODING 48  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
70  
D8  
D8  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR one  
ENCODING 49  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP

30  
F0  
30  
30  
30  
30  
FC  
ENDCHAR  
STARTCHAR two  
ENCODING 50  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
70  
D8  
18  
30  
60  
D8  
F8  
ENDCHAR  
STARTCHAR three  
ENCODING 51  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
70  
D8  
18  
70  
18  
D8  
70  
ENDCHAR  
STARTCHAR four  
ENCODING 52  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
18  
38  
58  
D8  
FC  
18

18  
ENDCHAR  
STARTCHAR five  
ENCODING 53  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
F8  
C0  
F0  
D8  
18  
98  
F0  
ENDCHAR  
STARTCHAR six  
ENCODING 54  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
70  
D8  
C0  
F0  
D8  
D8  
70  
ENDCHAR  
STARTCHAR seven  
ENCODING 55  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
F8  
D8  
18  
30  
30  
60  
60  
ENDCHAR  
STARTCHAR  
eight  
ENCODING 56  
SWIDTH 600 0

DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
70  
D8  
D8  
70  
D8  
D8  
70  
ENDCHAR  
STARTCHAR nine  
ENCODING 57  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
70  
D8  
D8  
78  
18  
D8  
70  
ENDCHAR  
STARTCHAR colon  
ENCODING 58  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 2 4 1 0  
BITMAP  
C0  
00  
00  
C0  
ENDCHAR  
STARTCHAR semicolon  
ENCODING 59  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 6 0 -2  
BITMAP  
60  
00  
00  
60  
40  
80

ENDCHAR  
STARTCHAR less  
ENCODING 60  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 5 0 1  
BITMAP  
30  
60  
C0  
60  
30  
ENDCHAR  
STARTCHAR equal  
ENCODING 61  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 3 0 2  
BITMAP  
F0  
00  
F0  
ENDCHAR  
STARTCHAR greater  
ENCODING 62  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 5 1 1  
BITMAP  
C0  
60  
30  
60  
C0  
ENDCHAR  
STARTCHAR question  
ENCODING 63  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 6 0 0  
BITMAP  
70  
98  
30  
60  
00  
60  
ENDCHAR

STARTCHAR at  
ENCODING 64  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 8 0 -1  
BITMAP  
70  
C8  
98  
A8  
A8  
9C  
C0  
70  
ENDCHAR  
STARTCHAR A  
ENCODING 65  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
78  
38  
28  
7C  
6C  
EE  
ENDCHAR  
STARTCHAR B  
ENCODING 66  
SWIDTH 600 0  
DWIDTH  
6 0  
BBX 6 6 -1 0  
BITMAP  
F8  
6C  
78  
6C  
6C  
F8  
ENDCHAR  
STARTCHAR C  
ENCODING 67  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 6 0 0  
BITMAP



78  
D8  
C0  
C0  
D8  
70  
ENDCHAR  
STARTCHAR D  
ENCODING 68  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
F8  
6C  
6C  
6C  
6C  
F8  
ENDCHAR  
STARTCHAR E  
ENCODING 69  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
FC  
60  
78  
60  
6C  
FC  
ENDCHAR  
STARTCHAR F  
ENCODING 70  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
FC  
60  
78  
60  
60  
F0  
ENDCHAR  
STARTCHAR G  
ENCODING 71

SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 6 0 0  
BITMAP  
70  
D8  
C0  
F8  
D8  
78  
ENDCHAR  
STARTCHAR H  
ENCODING 72  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
EE  
6C  
7C  
6C  
6C  
EE  
ENDCHAR  
STARTCHAR I  
ENCODING 73  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 6 0 0  
BITMAP  
F0  
60  
60  
60  
60  
F0  
ENDCHAR  
STARTCHAR J  
ENCODING 74  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
3C  
18  
18  
D8  
D8

70  
ENDCHAR  
STARTCHAR K  
ENCODING 75  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
EC  
68  
70  
78  
6C  
F6  
ENDCHAR  
STARTCHAR L  
ENCODING 76  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
F0  
60  
60  
60  
6C  
FC  
ENDCHAR  
STARTCHAR M  
ENCODING  
77  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
C4  
6C  
6C  
7C  
54  
D4  
ENDCHAR  
STARTCHAR N  
ENCODING 78  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP

EE  
74  
74  
6C  
6C  
E4  
ENDCHAR  
STARTCHAR O  
ENCODING 79  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 6 0 0  
BITMAP  
70  
D8  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR P  
ENCODING 80  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
F8  
6C  
6C  
78  
60  
F0  
ENDCHAR  
STARTCHAR Q  
ENCODING 81  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 -1  
BITMAP  
70  
D8  
D8  
D8  
D8  
70  
18  
ENDCHAR  
STARTCHAR R

ENCODING 82  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
F8  
6C  
6C  
78  
6C  
F6  
ENDCHAR  
STARTCHAR S  
ENCODING 83  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 6 0 0  
BITMAP  
78  
C8  
F0  
38  
98  
F0  
ENDCHAR  
STARTCHAR T  
ENCODING 84  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
FC  
B4  
30  
30  
30  
78  
ENDCHAR  
STARTCHAR U  
ENCODING 85  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
EE  
6C  
6C  
6C

6C  
38  
ENDCHAR  
STARTCHAR V  
ENCODING 86  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
EE  
6C  
28  
38  
38  
10  
ENDCHAR  
STARTCHAR W  
ENCODING 87  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
D6  
54  
54  
7C  
38  
28  
ENDCHAR  
STARTCHAR  
X  
ENCODING 88  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 0 0  
BITMAP  
CC  
78  
30  
30  
78  
CC  
ENDCHAR  
STARTCHAR Y  
ENCODING 89  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0

BITMAP  
E6  
66  
3C  
18  
18  
3C  
ENDCHAR  
STARTCHAR Z  
ENCODING 90  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 6 0 0  
BITMAP  
F8  
D8  
30  
60  
D8  
F8  
ENDCHAR  
STARTCHAR bracketleft  
ENCODING 91  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 8 1 -1  
BITMAP  
E0  
C0  
C0  
C0  
C0  
C0  
C0  
E0  
ENDCHAR  
STARTCHAR backslash  
ENCODING 92  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 8 0 -1  
BITMAP  
80  
80  
40  
40  
20  
20

10  
10  
ENDCHAR  
STARTCHAR bracketright  
ENCODING 93  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 8 1 -1  
BITMAP  
E0  
60  
60  
60  
60  
60  
60  
E0  
ENDCHAR  
STARTCHAR asciicircum  
ENCODING 94  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 3 0 4  
BITMAP  
20  
70  
D8  
ENDCHAR  
STARTCHAR underscore  
ENCODING 95  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 1 0 -2  
BITMAP  
FC  
ENDCHAR  
STARTCHAR quoteleft  
ENCODING 96  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 3 1 4  
BITMAP  
C0  
40  
20  
ENDCHAR  
STARTCHAR a  
ENCODING 97



SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 0 0  
BITMAP  
70  
D8  
78  
D8  
FC  
ENDCHAR  
STARTCHAR  
b  
ENCODING 98  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 -1 0  
BITMAP  
E0  
60  
78  
6C  
6C  
6C  
F8  
ENDCHAR  
STARTCHAR c  
ENCODING 99  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 5 0 0  
BITMAP  
70  
D8  
C0  
D8  
70  
ENDCHAR  
STARTCHAR d  
ENCODING 100  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
38  
18  
78  
D8  
D8

D8  
7C  
ENDCHAR  
STARTCHAR e  
ENCODING 101  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 5 0 0  
BITMAP  
70  
D8  
F8  
C0  
78  
ENDCHAR  
STARTCHAR f  
ENCODING 102  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
38  
60  
F8  
60  
60  
60  
F8  
ENDCHAR  
STARTCHAR g  
ENCODING 103  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 -2  
BITMAP  
6C  
D8  
D8  
D8  
78  
18  
F0  
ENDCHAR  
STARTCHAR h  
ENCODING 104  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 -1 0

BITMAP  
E0  
60  
78  
6C  
6C  
6C  
6C  
ENDCHAR  
STARTCHAR i  
ENCODING 105  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
30  
00  
F0  
30  
30  
30  
FC  
ENDCHAR  
STARTCHAR j  
ENCODING 106  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 9 0 -2  
BITMAP  
30  
00  
F0  
30  
30  
30  
30  
30  
E0  
ENDCHAR  
STARTCHAR k  
ENCODING 107  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 0  
BITMAP  
E0  
60  
6C

78  
70  
78  
6E  
ENDCHAR  
STARTCHAR 1  
ENCODING 108  
SWIDTH 600  
0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
F0  
30  
30  
30  
30  
30  
FC  
ENDCHAR  
STARTCHAR m  
ENCODING 109  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 -1 0  
BITMAP  
F8  
7C  
54  
54  
54  
ENDCHAR  
STARTCHAR n  
ENCODING 110  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 -1 0  
BITMAP  
D8  
6C  
6C  
6C  
6C  
ENDCHAR  
STARTCHAR o  
ENCODING 111  
SWIDTH 600 0  
DWIDTH 6 0

BBX 5 5 0 0  
BITMAP  
70  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR p  
ENCODING 112  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 -1 -2  
BITMAP  
F8  
6C  
6C  
6C  
78  
60  
F0  
ENDCHAR  
STARTCHAR q  
ENCODING 113  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 -2  
BITMAP  
6C  
D8  
D8  
D8  
78  
18  
3C  
ENDCHAR  
STARTCHAR r  
ENCODING 114  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 0 0  
BITMAP  
DC  
74  
60  
60  
F0  
ENDCHAR

STARTCHAR s  
ENCODING 115  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 0 0  
BITMAP  
78  
E0  
78  
1C  
F8  
ENDCHAR  
STARTCHAR t  
ENCODING 116  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
60  
60  
F8  
60  
60  
6C  
38  
ENDCHAR  
STARTCHAR u  
ENCODING 117  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 5 -1 0  
BITMAP  
EC  
6C  
6C  
6C  
3E  
ENDCHAR  
STARTCHAR v  
ENCODING 118  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 -1 0  
BITMAP  
EC  
6C  
38  
38

10  
ENDCHAR  
STARTCHAR  
w  
ENCODING 119  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 5 -1 0  
BITMAP  
D6  
54  
7C  
3C  
28  
ENDCHAR  
STARTCHAR x  
ENCODING 120  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 0 0  
BITMAP  
EC  
78  
30  
78  
DC  
ENDCHAR  
STARTCHAR y  
ENCODING 121  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 -2  
BITMAP  
EE  
6C  
6C  
28  
38  
30  
E0  
ENDCHAR  
STARTCHAR z  
ENCODING 122  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 5 0 0  
BITMAP  
F8

B0  
60  
D8  
F8  
ENDCHAR  
STARTCHAR braceleft  
ENCODING 123  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 8 1 -1  
BITMAP  
30  
60  
60  
C0  
60  
60  
60  
30  
ENDCHAR  
STARTCHAR bar  
ENCODING 124  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 1 7 2 -1  
BITMAP  
80  
80  
80  
80  
80  
80  
80  
ENDCHAR  
STARTCHAR braceright  
ENCODING 125  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 8 0 -1  
BITMAP  
C0  
60  
60  
30  
60  
60  
60  
C0



ENDCHAR  
STARTCHAR asciitilde  
ENCODING 126  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 2 0 3  
BITMAP  
68  
B0  
ENDCHAR  
STARTCHAR exclamdown  
ENCODING 161  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 2 7 1 -2  
BITMAP  
C0  
00  
C0  
C0  
C0  
C0  
C0  
C0  
ENDCHAR  
STARTCHAR cent  
ENCODING 162  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 -1  
BITMAP  
20  
20  
78  
C8  
C0  
78  
20  
20  
ENDCHAR  
STARTCHAR  
sterling  
ENCODING 163  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
38  
68

20  
F8  
20  
64  
F8  
ENDCHAR  
STARTCHAR currency  
ENCODING 164  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 5 0 1  
BITMAP  
88  
70  
50  
70  
88  
ENDCHAR  
STARTCHAR yen  
ENCODING 165  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
CC  
48  
FC  
30  
FC  
30  
78  
ENDCHAR  
STARTCHAR brokenbar  
ENCODING 166  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 1 9 2 -2  
BITMAP  
80  
80  
80  
80  
00  
80  
80  
80  
80  
80  
ENDCHAR

STARTCHAR section

ENCODING 167

SWIDTH 600 0

DWIDTH 6 0

BBX 5 8 0 -1

BITMAP

78

48

60

90

48

30

90

F0

ENDCHAR

STARTCHAR dieresis

ENCODING 168

SWIDTH 600 0

DWIDTH 6 0

BBX 3 1 1 5

BITMAP

A0

ENDCHAR

STARTCHAR copyright

ENCODING 169

SWIDTH 600 0

DWIDTH 6 0

BBX 6 7 0 0

BITMAP

30

48

B4

A4

B4

48

30

ENDCHAR

STARTCHAR ordfeminine

ENCODING 170

SWIDTH 600 0

DWIDTH 6 0

BBX 4 5 1 2

BITMAP

E0

10

D0

00

F0

ENDCHAR  
STARTCHAR guillemotleft  
ENCODING 171  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 5 -1 0  
BITMAP  
36  
6C  
D8  
6C  
36  
ENDCHAR  
STARTCHAR logicalnot  
ENCODING 172  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 3 0 2  
BITMAP  
F8  
08  
08  
ENDCHAR  
STARTCHAR  
hyphen  
ENCODING 173  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 1 0 3  
BITMAP  
F8  
ENDCHAR  
STARTCHAR registered  
ENCODING 174  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 0 0  
BITMAP  
38  
44  
BA  
B2  
AA  
44  
38  
ENDCHAR  
STARTCHAR macron  
ENCODING 175

SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 1 0 5  
BITMAP  
F0  
ENDCHAR  
STARTCHAR degree  
ENCODING 176  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 3 0 4  
BITMAP  
60  
90  
60  
ENDCHAR  
STARTCHAR plusminus  
ENCODING 177  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 6 0 0  
BITMAP  
20  
20  
F8  
20  
00  
F8  
ENDCHAR  
STARTCHAR twosuperior  
ENCODING 178  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 4 1 3  
BITMAP  
60  
A0  
40  
E0  
ENDCHAR  
STARTCHAR threesuperior  
ENCODING 179  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 4 1 3  
BITMAP  
E0  
40

20  
C0  
ENDCHAR  
STARTCHAR acute  
ENCODING 180  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 2 2 2 5  
BITMAP  
40  
80  
ENDCHAR  
STARTCHAR mu  
ENCODING 181  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 -2  
BITMAP  
EC  
6C  
6C  
6C  
7E  
40  
40  
ENDCHAR  
STARTCHAR paragraph  
ENCODING 182  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 8 0 -1  
BITMAP  
7C  
A8  
A8  
68  
28  
28  
28  
6C  
ENDCHAR  
STARTCHAR periodcentered  
ENCODING 183  
SWIDTH  
600 0  
DWIDTH 6 0  
BBX 2 1 1 3  
BITMAP

C0  
ENDCHAR  
STARTCHAR cedilla  
ENCODING 184  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 3 1 -2  
BITMAP  
40  
20  
C0  
ENDCHAR  
STARTCHAR onesuperior  
ENCODING 185  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 3 4 1 3  
BITMAP  
C0  
40  
40  
E0  
ENDCHAR  
STARTCHAR ordmasculine  
ENCODING 186  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 5 1 2  
BITMAP  
60  
90  
60  
00  
F0  
ENDCHAR  
STARTCHAR guillemotright  
ENCODING 187  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 5 -1 0  
BITMAP  
D8  
6C  
36  
6C  
D8  
ENDCHAR  
STARTCHAR onequarter

ENCODING 188  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 0  
BITMAP  
C0  
44  
48  
F4  
2C  
5E  
04  
ENDCHAR  
STARTCHAR onehalf  
ENCODING 189  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 0  
BITMAP  
C0  
44  
48  
F6  
2A  
44  
0E  
ENDCHAR  
STARTCHAR threequarters  
ENCODING 190  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 0  
BITMAP  
E0  
44  
28  
D4  
2C  
5E  
04  
ENDCHAR  
STARTCHAR questiondown  
ENCODING 191  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 -2  
BITMAP  
30



00  
30  
30  
60  
C8  
70  
ENDCHAR  
STARTCHAR Agrave  
ENCODING 192  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
20  
10  
00  
78  
38  
28  
7C  
6C  
EE  
ENDCHAR  
STARTCHAR Aacute  
ENCODING  
193  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
10  
20  
00  
78  
38  
28  
7C  
6C  
EE  
ENDCHAR  
STARTCHAR Acircumflex  
ENCODING 194  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
10  
28

00  
78  
38  
28  
7C  
6C  
EE  
ENDCHAR  
STARTCHAR Atilde  
ENCODING 195  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
34  
48  
00  
78  
38  
28  
7C  
6C  
EE  
ENDCHAR  
STARTCHAR Adieresis  
ENCODING 196  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 8 -1 0  
BITMAP  
28  
00  
78  
38  
28  
7C  
6C  
EE  
ENDCHAR  
STARTCHAR Aring  
ENCODING 197  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
30  
48  
30

78  
38  
28  
7C  
6C  
EE  
ENDCHAR  
STARTCHAR AE  
ENCODING 198  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
7E  
3A  
6C  
78  
DA  
DE  
ENDCHAR  
STARTCHAR Ccedilla  
ENCODING 199  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 -2  
BITMAP  
78  
D8  
C0  
C0  
D8  
70  
10  
60  
ENDCHAR  
STARTCHAR Egrave  
ENCODING 200  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 9 -1 0  
BITMAP  
20  
10  
00  
FC  
64  
78  
60

6C  
FC  
ENDCHAR  
STARTCHAR Eacute  
ENCODING 201  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 9 -1 0  
BITMAP  
10  
20  
00  
FC  
64  
78  
60  
6C  
FC  
ENDCHAR  
STARTCHAR Ecircumflex  
ENCODING 202  
SWIDTH  
600 0  
DWIDTH 6 0  
BBX 6 9 -1 0  
BITMAP  
20  
50  
00  
FC  
64  
78  
60  
6C  
FC  
ENDCHAR  
STARTCHAR Edieresis  
ENCODING 203  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 8 -1 0  
BITMAP  
50  
00  
FC  
64  
78  
60

6C  
FC  
ENDCHAR  
STARTCHAR Igrave  
ENCODING 204  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 9 0 0  
BITMAP  
40  
20  
00  
F0  
60  
60  
60  
60  
F0  
ENDCHAR  
STARTCHAR Iacute  
ENCODING 205  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 9 0 0  
BITMAP  
20  
40  
00  
F0  
60  
60  
60  
60  
F0  
ENDCHAR  
STARTCHAR Icircumflex  
ENCODING 206  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 9 0 0  
BITMAP  
40  
A0  
00  
F0  
60  
60  
60

60  
F0  
ENDCHAR  
STARTCHAR Idieresis  
ENCODING 207  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 4 8 0 0  
BITMAP  
A0  
00  
F0  
60  
60  
60  
60  
F0  
ENDCHAR  
STARTCHAR Eth  
ENCODING 208  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
F8  
6C  
F4  
64  
6C  
F8  
ENDCHAR  
STARTCHAR Ntilde  
ENCODING 209  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
34  
48  
00  
EE  
64  
74  
7C  
6C  
EC  
ENDCHAR  
STARTCHAR Ograve

ENCODING 210  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 9 0 0  
BITMAP  
40  
20  
00  
70  
D8  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR Oacute  
ENCODING 211  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 9 0 0  
BITMAP  
20  
40  
00  
70  
D8  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR  
Ocircumflex  
ENCODING 212  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 9 0 0  
BITMAP  
20  
50  
00  
70  
D8  
D8  
D8  
D8  
70  
ENDCHAR

STARTCHAR Otilde  
ENCODING 213  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 9 0 0  
BITMAP  
68  
90  
00  
70  
D8  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR Odieresis  
ENCODING 214  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 0  
BITMAP  
50  
00  
70  
D8  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR multiply  
ENCODING 215  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 5 0 1  
BITMAP  
88  
50  
20  
50  
88  
ENDCHAR  
STARTCHAR Oslash  
ENCODING 216  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0



BITMAP  
3A  
6C  
7C  
6C  
6C  
B8  
ENDCHAR  
STARTCHAR Ugrave  
ENCODING 217  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
20  
10  
00  
EE  
6C  
6C  
6C  
6C  
38  
ENDCHAR  
STARTCHAR Uacute  
ENCODING 218  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
08  
10  
00  
EE  
6C  
6C  
6C  
6C  
38  
ENDCHAR  
STARTCHAR Ucircumflex  
ENCODING 219  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
10  
28

00  
EE  
6C  
6C  
6C  
6C  
38  
ENDCHAR  
STARTCHAR Udieresis  
ENCODING 220  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 8 -1 0  
BITMAP  
28  
00  
EE  
6C  
6C  
6C  
6C  
38  
ENDCHAR  
STARTCHAR  
Yacute  
ENCODING 221  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 9 -1 0  
BITMAP  
04  
08  
00  
E6  
66  
3C  
18  
18  
3C  
ENDCHAR  
STARTCHAR Thorn  
ENCODING 222  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 6 -1 0  
BITMAP  
E0  
78

6C  
6C  
78  
E0  
ENDCHAR  
STARTCHAR germandbls  
ENCODING 223  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 6 -1 0  
BITMAP  
38  
68  
7C  
66  
66  
EC  
ENDCHAR  
STARTCHAR agrave  
ENCODING 224  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 8 0 0  
BITMAP  
20  
10  
00  
70  
98  
78  
D8  
FC  
ENDCHAR  
STARTCHAR aacute  
ENCODING 225  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 8 0 0  
BITMAP  
10  
20  
00  
70  
98  
78  
D8  
FC  
ENDCHAR

STARTCHAR acircumflex

ENCODING 226

SWIDTH 600 0

DWIDTH 6 0

BBX 6 8 0 0

BITMAP

20

50

00

70

98

78

D8

FC

ENDCHAR

STARTCHAR atilde

ENCODING 227

SWIDTH 600 0

DWIDTH 6 0

BBX 6 8 0 0

BITMAP

68

90

00

70

98

78

D8

FC

ENDCHAR

STARTCHAR adieresis

ENCODING 228

SWIDTH 600 0

DWIDTH 6 0

BBX 6 7 0 0

BITMAP

50

00

70

98

78

D8

FC

ENDCHAR

STARTCHAR aring

ENCODING 229

SWIDTH 600 0

DWIDTH 6 0

BBX 6 9 0 0  
BITMAP  
30  
48  
30  
00  
70  
98  
78  
D8  
FC  
ENDCHAR  
STARTCHAR ae  
ENCODING 230  
SWIDTH 600 0  
DWIDTH  
6 0  
BBX 6 5 -1 0  
BITMAP  
6C  
B4  
7C  
B0  
DC  
ENDCHAR  
STARTCHAR ccedilla  
ENCODING 231  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 -2  
BITMAP  
70  
D8  
C0  
D8  
70  
10  
60  
ENDCHAR  
STARTCHAR egrave  
ENCODING 232  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 0  
BITMAP  
40  
20  
00

70  
D8  
F8  
C0  
78  
ENDCHAR  
STARTCHAR eacute  
ENCODING 233  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 0  
BITMAP  
20  
40  
00  
70  
D8  
F8  
C0  
78  
ENDCHAR  
STARTCHAR ecircumflex  
ENCODING 234  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 0  
BITMAP  
20  
50  
00  
70  
D8  
F8  
C0  
78  
ENDCHAR  
STARTCHAR edieresis  
ENCODING 235  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
50  
00  
70  
D8  
F8  
C0

78

ENDCHAR

STARTCHAR igrave

ENCODING 236

SWIDTH 600 0

DWIDTH 6 0

BBX 6 8 0 0

BITMAP

20

10

00

70

30

30

30

FC

ENDCHAR

STARTCHAR iacute

ENCODING 237

SWIDTH 600 0

DWIDTH 6 0

BBX 6 8 0 0

BITMAP

10

20

00

70

30

30

30

FC

ENDCHAR

STARTCHAR icircumflex

ENCODING 238

SWIDTH 600 0

DWIDTH 6 0

BBX 6 8 0 0

BITMAP

20

50

00

70

30

30

30

FC

ENDCHAR

STARTCHAR idieresis

ENCODING 239

SWIDTH 600 0

DWIDTH 6 0

BBX 6 7 0 0

BITMAP

50

00

70

30

30

30

FC

ENDCHAR

STARTCHAR

eth

ENCODING 240

SWIDTH 600 0

DWIDTH 6 0

BBX 5 8 0 0

BITMAP

D0

60

B0

78

D8

D8

D8

70

ENDCHAR

STARTCHAR ntilde

ENCODING 241

SWIDTH 600 0

DWIDTH 6 0

BBX 7 8 -1 0

BITMAP

34

48

00

D8

6C

6C

6C

6E

ENDCHAR

STARTCHAR ograve

ENCODING 242

SWIDTH 600 0

DWIDTH 6 0



BBX 5 8 0 0  
BITMAP  
40  
20  
00  
70  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR oacute  
ENCODING 243  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 0  
BITMAP  
20  
40  
00  
70  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR ocircumflex  
ENCODING 244  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 0  
BITMAP  
20  
50  
00  
70  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR otilde  
ENCODING 245  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 8 0 0  
BITMAP  
68

90  
00  
70  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR odieresis  
ENCODING 246  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 0  
BITMAP  
50  
00  
70  
D8  
D8  
D8  
70  
ENDCHAR  
STARTCHAR divide  
ENCODING 247  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 5 0 1  
BITMAP  
20  
00  
F8  
00  
20  
ENDCHAR  
STARTCHAR oslash  
ENCODING 248  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 5 7 0 -1  
BITMAP  
08  
70  
D8  
F8  
D8  
70  
80  
ENDCHAR

STARTCHAR ugrave

ENCODING 249

SWIDTH 600 0

DWIDTH 6 0

BBX

7 8 -1 0

BITMAP

20

10

00

EC

6C

6C

6C

3E

ENDCHAR

STARTCHAR uacute

ENCODING 250

SWIDTH 600 0

DWIDTH 6 0

BBX 7 8 -1 0

BITMAP

10

20

00

EC

6C

6C

6C

3E

ENDCHAR

STARTCHAR ucircumflex

ENCODING 251

SWIDTH 600 0

DWIDTH 6 0

BBX 7 8 -1 0

BITMAP

10

28

00

EC

6C

6C

6C

3E

ENDCHAR

STARTCHAR udieresis

ENCODING 252

SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 0  
BITMAP  
28  
00  
EC  
6C  
6C  
6C  
3E  
ENDCHAR  
STARTCHAR yacute  
ENCODING 253  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 10 -1 -2  
BITMAP  
08  
10  
00  
EE  
6C  
6C  
28  
38  
30  
F0  
ENDCHAR  
STARTCHAR thorn  
ENCODING 254  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 9 -1 -2  
BITMAP  
E0  
60  
78  
6C  
6C  
6C  
78  
60  
F0  
ENDCHAR  
STARTCHAR ydieresis  
ENCODING 255  
SWIDTH 600 0

DWIDTH 6 0  
BBX 7 9 -1 -2  
BITMAP  
28  
00  
EE  
6C  
6C  
28  
38  
30  
F0  
ENDCHAR  
STARTCHAR trademark  
ENCODING 8482  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 4 -1 3  
BITMAP  
F6  
5E  
5A  
5A  
ENDCHAR  
STARTCHAR perthousand  
ENCODING 8240  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 7 0 0  
BITMAP  
60  
A8  
D0  
60  
6C  
B4  
D8  
ENDCHAR  
STARTCHAR oe  
ENCODING 339  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 6 5 0 0  
BITMAP  
6C  
B4  
BC  
B0

6C  
ENDCHAR  
STARTCHAR  
fl  
ENCODING 64258  
SWIDTH 600 0  
DWIDTH 6 0  
BBX 7 7 -1 0  
BITMAP  
3C  
6C  
FC  
6C  
6C  
6C  
FE  
ENDCHAR  
ENDFONT

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/Images/courB08.bdf  
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\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/doctest.py  
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\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/PIL/Image.py  
\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/PIL/TiffImagePlugin.py  
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/\*

\* The Python Imaging Library  
\* \$Id: Quant.c 2284 2005-02-07 20:58:58Z fredrik \$\br/>\*  
\* image quantizer  
\*

\* history:  
\* 1998-09-10 tjs Contributed  
\* 1998-12-29 fl Added to PIL 1.0b1  
\* 2004-02-21 fl Fixed bogus free() on quantization error  
\* 2005-02-07 fl Limit number of colors to 256  
\*  
\* Written by Toby J Sargeant <tjs@longford.cs.monash.edu.au>.  
\*  
\* Copyright (c) 1998 by Toby J Sargeant  
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\*  
\* See the README file for information on usage and redistribution.  
\*/

Found in path(s):

\* /opt/cola/permits/1304943026\_1649692840.42/0/imaging-1-1-6-2-tar-gz/Imaging-1.1.6/libImaging/Quant.c

## 1.349 python-setuptools 44.0.0

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# 1.352 afero 1.5.1

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## 1.353 pcre 10.40

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pycparser -- A C parser in Python

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Dustin Sallings <dustin@spy.net>

Jason Mooberry <jasonmoo@me.com>

Sergey Shepelev <temotor@gmail.com>

Alex Edwards <ajmedwards@gmail.com>

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Any

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Version 3, 29 June 2007

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Version 2, June 1991

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this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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##### Appendix: How to Apply These Terms to Your New Libraries

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<signature of Ty Coon>, 1 April 1990  
Ty Coon, President of Vice

That's all there is to it!

@ignore

@c Set file name and title for man page.

@setfilename gpl

@settitle GNU General Public License

@c man begin SEEALSO

gfdl(7), fsf-funding(7).

@c man end

@c man begin COPYRIGHT

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@c man end

@end ignore

@node Copying

@c man begin DESCRIPTION

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@c The GNU General Public License.

@center Version 3, 29 June 2007

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The precise terms and conditions for copying, distribution and modification follow.

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@enumerate 0

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## 1.366 zlib 1.2.11

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/\* zlib.h -- interface of the 'zlib' general purpose compression library  
version 1.2.11, January 15th, 2017

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Jean-loup Gailly      Mark Adler  
jloup@gzip.org      madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950> (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

\*/

# 1.367 bison 3.6.4

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immerrr again

Alexander Artemenko

Aymeric Augustin

Lee Ball

Ben Bariteau

Ned Batchelder

Wouter Bolsterlee

Brett Cannon

Jason R. Coombs

Julien Danjou

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Tim Graham

Thomas Grainger

Max Grender-Jones

Pierre Grimaud

Joshua Harlow  
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Alexander Lukanin  
James Mills  
Jordan Moldow  
Berker Peksag  
Sridhar Ratnakumar  
Erik Rose  
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Peter Ruibal  
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## 1.371 pycsocks 1.7.1

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## 1.372 pycparser 2.21

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# 1.375 packaging 16.8

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Metadata-Version: 1.0

Name: wsgiref

Version: 0.1.2

Summary: WSGI (PEP 333) Reference Library

Home-page: <http://cheeseshop.python.org/pypi/wsgiref>

Author: Phillip J. Eby

Author-email: [web-sig@python.org](mailto:web-sig@python.org)

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Description: This is a standalone release of the ``wsgiref`` library to be included in

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## 1.377 mako 1.0.14

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## 1.379 meson 0.56.0

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```
project('bar', 'c', license: 'Apache')
```

```
executable('bar', 'bar.c')
```

```
license = meson.project_license()[0]  
if license != 'Apache'  
    error("The license should be Apache, but it is: " + license)  
endif
```

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# 1.380 x-time-rate 0.0.0-20220922220347-f3bd1da661af

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Found in path(s):

```
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/RpcUtil.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/RpcCallback.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/AbstractMessage.java  
*  
/opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/ProtocolMessageEnum.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/BlockingRpcChannel.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/CodedOutputStream.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/DynamicMessage.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/Descriptors.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/AbstractMessageLite.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/GeneratedMessageLite.java  
*  
/opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/RpcChannel.java  
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-  
jar/com/google/protobuf/CodedInputStream.java
```

```

* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/TextFormat.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ByteString.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ExtensionRegistry.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ExtensionRegistryLite.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/GeneratedMessage.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/UninitializedMessageException.java
*
/opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/RpcController.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/FieldSet.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/BlockingService.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/Internal.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/ServiceException.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/InvalidProtocolBufferException.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/MessageLite.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/Message.java
*
/opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/WireFormat.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/Service.java
* /opt/cola/permits/1444266101_1666009119.5882916/0/protobuf-java-2-2-0-sources-1-
jar/com/google/protobuf/UnknownFieldSet.java

```

## 1.382 klauspost-compress 1.15.11

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Wilk, Jakub  
Winiger, Gary  
Wood, David  
Zacarias, Gustavo  
Zolnowsky, John

The following people have worked to translate sudo into other languages as part of the Translation Project, see <https://translationproject.org> for more details.

Albuquerque, Pedro  
Blttermann, Mario  
Bogusz, Jakub  
Buo-ren, Lin  
Casagrande, Milo  
Castro, Felipe  
Cho, Seong-ho  
Chornoivan, Yuri  
Diguez,  
Francisco  
Fontenelle, Rafael  
Garca-Fontes, Walter  
Gezer, Volkan  
Hamasaki, Takeshi  
Hamming, Peter  
Hansen, Joe  
Hantrais, Frdric  
Hein, Jochen  
Hufthammer, Karl Ove  
Jerovek, Damir  
Karvonen, Jorma  
Kazik, Duan  
Kelemen, Gbor  
Keeci, Mehmet

Koir, Klemen  
Kozlov, Yuri  
Kramer, Jakob  
Krznar, Tomislav  
Marchal, Frdric  
Margeviius, Algimantas  
Maryanov, Pavel  
Nikoli, Miroslav  
Nylander, Daniel  
Psa, Petr  
Puente, Enol  
Putanec, Boidar  
Qun, Trn Ngc  
Rasmussen, Sebastian  
Regueiro, Leandro  
Sarar, zgr  
Sendn, Abel  
Sikrom, ka  
Spingos, Dimitris  
Taniguchi, Yasuaki  
Tomat, Fbio  
r, Balzs  
Uranga, Mikel Olasagasti  
Vorotnikov, Artem  
Wang, Wylmer

# 1.384 go-stomp-stomp 2.1.4+incompatible

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Raul Silvera <rsilvera@google.com>

Tipp Moseley <tipp@google.com>

Hyoun Kyu Cho <netforce@google.com>

Martin Spier <spiermar@gmail.com>

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## 1.386 x-crypto 1.17.9

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Tipp Moseley <tipp@google.com>

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# 1.388 aws-sdk-go 1.44.126

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## 1.389 libunwind 20200331

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## 1.390 jwt 2.3.0



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Marc Stevens

Cryptology Group

Centrum Wiskunde & Informatica

P.O. Box 94079, 1090 GB Amsterdam, Netherlands

marc@marc-stevens.nl

Dan Shumow

Microsoft Research

danshu@microsoft.com

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GIMP Palette

Name: Test

Columns: 0

#

GIMP Palette

Name: custompalette

Columns: 4

#

0 0 0 Index 3  
65 38 30 Index 4  
103 62 49 Index 6  
79 73 72 Index 7  
114 101 97 Index 8  
208 127 100 Index 9  
151 144 142 Index 10  
221 207 199 Index 11

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GIMP Palette

Name: badpalettefile

Columns: 4

#

0 0 0 Index 3  
012345678901234567890123456789012345678901234567890123456789012345678901234567890123456789012  
34567890123456789  
103 62 49 Index 6  
79 73 72 Index 7  
114 101 97 Index 8  
208 127 100 Index 9  
151 144 142 Index 10  
221 207 199 Index 11

GIMP Palette

Name: badpaletteentry

Columns: 4

#

0 0 0 Index 3



65 38  
103 62 49 Index 6  
79 73 72 Index 7  
114 101 97 Index 8  
208 127 100 Index 9  
151 144 142 Index 10  
221 207 199 Index 11

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<center>

<h1>Challenges in Embedded Database System Administration</h1>

<h3>Margo Seltzer, Harvard University</h3>

<h3>Michael Olson, Sleepycat Software, Inc.</h3>

<em>{margo,mao}@sleepycat.com</em>

</center>

<p>

Database configuration and maintenance have historically been complex tasks, often

requiring expert knowledge of database design and application behavior.

In an embedded environment, it is not feasible to require such expertise and ongoing database maintenance.

This paper discusses the database administration challenges posed by embedded systems and describes how the Berkeley DB architecture addresses these challenges.

<h2>1. Introduction</h2>

Embedded systems provide a combination of opportunities and challenges in application and system configuration and management.

As an embedded system is most

often dedicated to a single application or small set of tasks, the operating conditions of the system are typically better understood than those of general purpose computing environments.

Similarly, as embedded systems are dedicated to a small set of tasks, one would expect that the software to manage them should be small and simple.

On the other hand, once an embedded system is deployed, it must continue to function without interruption and without administrator intervention.

<p>

Database administration consists of two components, initial configuration and ongoing maintenance.

Initial configuration consists of database design, manifestation, and tuning.

The instantiation of the design includes decomposing the design

into tables, relations, or objects and designating proper indices and their implementations (e.g., Btrees, hash tables, etc.).

Tuning a design requires selecting a location for the log and data files, selecting appropriate database page sizes, specifying the size of in-memory

caches, and specifying the limits of multi-threading and concurrency.

As embedded systems define a specific environment and set of tasks, requiring expertise during the initial system configuration process is acceptable, and we focus our efforts on the ongoing maintenance of the system.

In this way, our emphasis differs from other projects such as Microsoft's AutoAdmin project [\[3\]](#), and the "no-knobs" administration that is identified as an area of important future research by the Asilomar authors [\[1\]](#).

<p>

In this paper, we focus on what the authors of the Asilomar report call "gizmo" databases [\[1\]](#), databases

that reside in devices such as smart cards, toasters, or telephones.

The key characteristics of such databases are that their functionality is completely transparent to users, no one ever performs explicit database operations or database maintenance, the database may crash at any time and must recover instantly,

the device may undergo a hard reset at any time, requiring that the database return to its initial state, and the semantic integrity of the database must be maintained at all times.

In Section 2, we provide more detail on the sorts of tasks typically performed by database administrators (DBAs) that must be automated in an embedded system.

<p>

The rest of this paper is structured as follows.

In Section 2, we outline the requirements for embedded database support.

In Section 3, we discuss how Berkeley DB is conducive to the hands-off management required in embedded systems.

In Section 4, we discuss novel features that enhance Berkeley

DB's suitability for the embedded applications.

In Section 5, we discuss issues of footprint size.

In Section 6 we discuss related work, and we conclude in Section 7.

## <h2>2. Embedded Database Requirements</h2>

Historically, much of the commercial database industry has been driven by the requirements of high performance online transaction

processing

(OLTP), complex query processing, and the industry standard benchmarks that have emerged (e.g., TPC-C [\[9\]](#TPCC), TPC-D [\[10\]](#TPCD)) to allow for system comparisons.

As embedded systems typically perform fairly simple queries, such metrics are not nearly as relevant for embedded database systems as are ease of maintenance, robustness, and small footprint.

Of these three requirements, robustness and ease of maintenance are the key issues.

Users must trust the data stored in their devices and must not need to manually perform anything resembling system administration in order to get their unit to work properly.

Fortunately, ease of use and robustness are important side effects of simplicity and good design.

These, in turn, lead to a small size, providing the third requirement of an embedded system.

### 2.1 The User Perspective

In the embedded database arena, it is the ongoing maintenance tasks that must be automated, not necessarily the initial system configuration.

There

are five tasks

that are traditionally performed by DBAs, but must be performed automatically in embedded database systems.

These tasks are

log archival and reclamation,  
backup,  
data compaction/reorganization,  
automatic and rapid recovery, and  
reinitialization from scratch.

Log archival and backup are tightly coupled.

Database backups are part of any large database installation, and log archival is analogous to incremental backup.

It is not clear what the implications of backup and archival are in an embedded system.

Consumers do not back up their VCRs or refrigerators, yet they do (or should) back up their personal computers or personal digital assistants.

For the remainder of this paper, we assume that backups, in some form, are required for gizmo databases (imagine having to reprogram, manually, the television viewing access pattern learned by some set-top television systems today).

Furthermore, we require that those backups are nearly instantaneous or completely

transparent,  
as users should not be aware that their gizmos are being backed up  
and should not have to explicitly initiate such backups.

<p>

Data compaction or reorganization has traditionally required periodic  
dumping and restoration of  
database tables and the recreation of indices.

In an embedded system, such reorganization must happen automatically.

<p>

Recovery issues are similar in embedded and traditional environments  
with a few exceptions.

While a few seconds or even a minute recovery is acceptable  
for a large server installation, no one is willing to wait  
for their telephone or television to reboot.

As with archival, recovery must be nearly instantaneous in an embedded product.

Secondly, it is often the case that a system will be completely  
reinitialized, rather than simply rebooted.

In this case, the embedded database must be restored to its initial  
state, freeing all its resources.

This is not typically a requirement of large server systems.

### <h3>2.2 The Developer Perspective</h3>

<p>

In

addition to the maintenance-free operation required of the  
embedded systems, there are a number of requirements that fall  
out of the constrained resources typically found in the "gizmos"  
using gizmo databases. These requirements are:

small footprint,

short code-path,

programmatic interface for tight application coupling and

to avoid the overhead (in both time and size) of

interfaces such as SQL and ODBC,

application configurability and flexibility,

support for complete memory-resident operation (e.g., these systems  
must run on gizmos without file systems), and

support for multi-threading.

<p>

A small footprint and short code-path are self-explanatory, however  
what is not as obvious is that the programmatic interface requirement  
is the logical result of them.

Traditional interfaces such as ODBC and SQL add significant  
size overhead and frequently add multiple context/thread switches  
per operation, not to mention several IPC calls.

An embedded product is less likely to require  
the complex

query processing that SQL enables.

Instead, in the embedded space, the ability for an application  
to configure the database for the specific tasks in question

is more important than a general query interface.

<p>

As some systems do not provide storage other than RAM and ROM, it is essential that an embedded database work seamlessly in memory-only environments.

Similarly, many of today's embedded operating systems provide a single address space architecture, so a simple, multi-threaded capability is essential for application requiring any concurrency.

<p>

In general, embedded applications run on gizmos whose native operating system support varies tremendously.

For example, the embedded OS may or may not support user-level processing or multi-threading.

Even if it does, a particular embedded application may or may not need it.

Not all applications need more than one thread of control.

An embedded database must provide mechanisms to developers without deciding policy.

For

example, the threading model in an application is a matter of policy, and depends

not on the database software, but on the hardware, operating system, and the application's feature set.

Therefore, the data manager must provide for the use of multi-threading, but not require it.

### <h2>3. Berkeley DB: A Database for Embedded Systems</h2>

Berkeley DB is the result of implementing database functionality using the UNIX tool-based philosophy.

The current Berkeley DB package, as distributed by Sleepycat Software, is a descendant of the hash and btree access methods distributed with 4.4BSD and its descendents.

The original package (referred to as DB-1.85), while intended as a public domain replacement for dbm and its followers (e.g., ndbm, gdbm, etc), rapidly became widely used as an efficient, easy-to-use data store.

It was incorporated into a number of Open Source packages including Perl, Sendmail, Kerberos, and the GNU C-library.

<p>

Versions 2.X and higher are distributed by Sleepycat Software and

add functionality for concurrency, logging, transactions, and recovery.

Each piece of additional functionality is implemented as an independent module, which means that the subsystems can be used outside the context of Berkeley DB. For example, the locking subsystem can easily be used to implement locking for a non-DB application and the shared memory buffer pool can be used for any application



caching data in main memory.

This subsystem design allows a designer to pick and choose the functionality necessary for the application, minimizing memory footprint and maximizing performance.

This addresses the small footprint and short code-path criteria mentioned in the previous section.

<p>

As Berkeley DB grew out of a replacement for dbm, its primary implementation language has always been C and its interface has been programmatic. The C interface is the native interface, unlike many database systems where the programmatic API is simply a layer on top of an already-costly query interface (e.g. embedded SQL).

Berkeley DB's heritage is also apparent in its data model; it has none.

The database stores unstructured key/data pairs, specified as variable length byte strings.

This leaves schema design and representation issues the responsibility of the application, which is ideal for an embedded environment.

Applications retain full control over specification of their data types, representation, index values, and index relationships.

In other words, Berkeley DB provides a robust, high-performance, keyed storage system, not a particular database management system.

We have designed for simplicity and performance, trading off complex, general purpose support that is better encapsulated in applications.

<p>

Another element of Berkeley DB's programmatic interface is its customizability; applications can specify Btree comparison and prefix compression functions, hash functions, error routines, and recovery models.

This means that embedded applications can tailor the underlying database to best suit their data demands.

Similarly, the utilities traditionally bundled with a database manager (e.g., recovery, dump/restore, archive) are implemented as tiny wrapper programs around library routines. This means that it is not necessary to run separate applications for the utilities. Instead, independent threads can act as utility daemons, or regular query threads can perform utility functions. Many of the current products built on Berkeley DB are bundled as a single large server with independent threads that perform functions such as checkpoint, deadlock detection, and performance monitoring.

<p>

As mentioned earlier, living in an embedded environment requires flexible management of storage.

Berkeley DB does not require any preallocation of disk space

for log or data files.

While many commercial database systems take complete control of a raw device, Berkeley DB uses a normal file system, and can therefore, safely and easily share a data space with other programs.

All

databases and log files are native files of the host environment, so whatever utilities are provided by the environment can be used to manage database files as well.

<p>

Berkeley DB provides three different memory models for its management of shared information.

Applications can use the IEEE Std 1003.1b-1993 (POSIX) <tt>mmap</tt> interface to share

data, they can use system shared memory, as frequently provided by the shmget family of interfaces, or they can use per-process heap memory (e.g., malloc).

Applications that require no permanent storage and do not provide shared memory facilities can still use Berkeley DB by requesting strictly private memory and specifying that all databases be memory-resident.

This provides pure-memory operation.

<p>

Lastly, Berkeley DB is designed for rapid startup -- recovery can happen automatically as part of system initialization.

This means that Berkeley DB works correctly in environments where gizmos are suddenly shut down and restarted.

<h2>4.

Extensions for Embedded Environments </h2>

While the Berkeley DB library has been designed for use in embedded systems, all the features described above are useful in more conventional systems as well.

In this section, we discuss a number of features and "automatic knobs" that are specifically geared toward the more constrained environments found in gizmo databases.

<h3>4.1 Automatic compression</h3>

Following the programmatic interface design philosophy, we support application-specific (or default) compression routines. These can be geared toward the particular data types present in the application's dataset, thus providing better compression than a general purpose routine.

Note that the application could instead specify an encryption function and create encrypted databases instead of compressed ones. Alternately, the application might specify a function that performs both compression and encryption.

<p>

As applications are also permitted to specify comparison and hash functions, the application can choose to organize its data based either on uncompressed and clear-text data or compressed and encrypted data.

If the application indicates that data should be compared in its processed form (i.e., compressed and encrypted), then the compression and encryption are performed on individual data items and the in-memory representation retains these characteristics.

However, if the application indicates that data should be compared in its original form, then entire pages are transformed upon being read into or written out of the main memory buffer cache.

These two alternatives provide the flexibility to trade space and security for performance.

### <h3>4.2 In-memory logging & transactions</h3>

One of the four key properties of transaction systems is durability. This means that transaction systems are designed for permanent storage (most commonly disk). However, as mentioned above, embedded systems do not necessarily contain any such storage.

Nevertheless, transactions can be useful in this environment to preserve the semantic integrity of the underlying storage. Berkeley DB optionally provides logging functionality and transaction support regardless of whether the database and logs are on disk or in memory.

### <h3>4.3 Remote Logs</h3>

While we do not expect users to backup their television sets and toasters, it is conceivable that a set-top box provided by a cable carrier should, in fact, be backed up by that cable carrier. The ability to store logs remotely can provide "information appliance" functionality, and can also be used in conjunction with local logs to enhance reliability.

Furthermore, remote logs provide for catastrophic recovery, e.g., loss of the gizmo, destruction of the gizmo, etc.

### <h3>4.4 Application References to Database Buffers</h3>

Typically, when data is returned to the user, it must be copied from the data manager's buffer cache (or data page) into the application's memory.

However, in an embedded environment, the robustness of the total software package is of paramount importance, not the isolation between the application and the data manager. As a result, it is possible for the data manager to avoid copies by giving applications direct references to data items

in a shared memory cache.

This is a significant performance optimization that can be allowed when the application and data manager are tightly integrated.

### 4.5 Recoverable database creation/deletion

In a conventional database management system, the creation of database tables (relations) and indices are heavyweight operations that are not recoverable.

This is not acceptable in a complex embedded environment where instantaneous recovery and robust operation in the face of all types of database operations is essential.

While Berkeley DB files can be removed using normal file system utilities, we provide transaction protected utilities that allow us to recover both database creation and deletion.

### 4.6 Adaptive concurrency control

The

Berkeley DB package uses page-level locking by default.

This trades off fine grain concurrency control for simplicity during recovery. (Finer grain concurrency control can be obtained by reducing the page size in the database.)

However, when multiple threads/processes perform page-locking in the presence of writing operations, there is the potential for deadlock.

As some environments do not need or desire the overhead of logging and transactions, it is important to provide the ability for concurrent access without the potential for deadlock.

<p>

Berkeley DB provides an option to perform coarser grain, deadlock-free locking.

Rather than locking on pages, locking is performed at the interface to the database.

Multiple readers or a single writer are allowed to be active in the database at any instant in time, with conflicting requests queued automatically.

The presence of cursors, through which applications can both read and write data, complicates this design.

If a cursor is currently

being used for reading, but will later

be used to write, the system will be deadlock prone if no special precautions are taken.

To handle this situation, we require that, when a cursor is created, the application specify any future intention to write.

If there is an intention to write, the cursor is granted an intention-to-write lock which does not conflict with readers,

but does conflict with other intention-to-write locks and write locks.

The end result is that the application is limited to a single potentially writing cursor accessing the database at any point in time.

<p>

Under periods of low contention (but potentially high throughput), the normal page-level locking provides the best overall throughput.

However, as contention rises, so does the potential for deadlock.

As some cross-over point, switching to the less concurrent, but deadlock-free locking protocol will result in higher throughput as operations must never be retried.

Given the operating conditions of an embedded database manager,

it is useful to make this change automatically as the system itself detects high contention.

### <h3>4.7 Adaptive synchronization</h3>

In addition to the logical locks that protect the integrity of the database pages, Berkeley DB must synchronize access to shared memory data structures, such as the lock table, in-memory buffer pool, and in-memory log buffer.

Each independent module uses a single mutex to protect its shared data structures, under the assumption that operations that require the mutex are very short and the potential for conflict is low.

Unfortunately, in highly concurrent environments with multiple processors present, this assumption is not always true.

When this assumption becomes invalid (that is, we observe significant contention for the subsystem mutexes), we can switch over to a finer-grained concurrency model for the mutexes.

Once again, there is a performance trade-off. Fine-grain mutexes impose a penalty of approximately 25% (due to the increased number of

mutexes required for each operation), but allow for higher throughput.

Using fine-grain mutexes under low contention would cause a decrease in performance, so it is important to monitor the system carefully, so that the change can be executed only when it will increase system throughput without jeopardizing latency.

## <h2>5. Footprint of an Embedded System</h2>

While traditional systems compete on price-performance, the embedded players will compete on price, features, and footprint.

The earlier sections have focused on features; in this section we focus on footprint.

<p>

Oracle reports that Oracle Lite 3.0 requires 350 KB to 750 KB

of memory and approximately 2.5 MB of hard disk space [\[7\]](#Oracle).

This includes drivers for interfaces such as ODBC and JDBC.

In contrast, Berkeley DB ranges in size from 75 KB to under 200 KB, foregoing heavyweight interfaces such as ODBC and JDBC and providing a variety of deployed sizes that can be used depending on application needs.

At the low end, applications requiring a simple single-user access method can choose from either extended linear hashing, B+ trees, or record-number based retrieval and pay only the 75 KB space requirement.

Applications requiring all three access methods will observe the 110 KB footprint.

At the high end, a fully recoverable, high-performance system occupies less than a quarter megabyte of memory.

This is a system you can easily incorporate in your toaster oven.

Table 1 shows the per-module break down of the entire Berkeley DB library. Note that this does not include memory used to cache database pages.

| Object sizes in bytes                    |        |      |     |
|------------------------------------------|--------|------|-----|
| Subsystem                                | Text   | Data | Bss |
| Btree-specific routines                  | 28812  | 0    | 0   |
| Recno-specific routines                  | 7211   | 0    | 0   |
| Hash-specific routines                   | 23742  | 0    | 0   |
| Memory Pool                              |        |      |     |
| Memory Pool                              | 14535  | 0    | 0   |
| Access method common code                | 23252  | 0    | 0   |
| OS compatibility library                 | 4980   | 52   | 0   |
| Support utilities                        | 6165   | 0    | 0   |
| All modules for Btree access method only |        |      |     |
| All modules for Btree access method only | 77744  | 52   | 0   |
| All modules for Recno access method only |        |      |     |
| All modules for Recno access method only | 84955  | 52   | 0   |
| All modules for Hash access method only  |        |      |     |
| All modules for Hash access method only  | 72674  | 52   | 0   |
| All Access Methods                       |        |      |     |
| All Access Methods                       | 108697 | 52   | 0   |
| Locking                                  |        |      |     |
| Locking                                  | 12533  | 0    | 0   |

```
<tr><td colspan=4></td></tr>
<tr><td>Recovery</td><td align=right>26948</td><td align=right>8</td><td align=right>4</td></tr>
<tr><td>Logging</td><td align=right>37367</td><td align=right>0</td><td align=right>0</td></tr>
<tr><td colspan=4></td></tr>
<tr><th align=left>Full Package</th><td align=right>185545</td><td align=right>60</td><td align=right>4</td></tr>
<tr><br></tr>
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```

## <h2>6. Related Work</h2>

Every three to five years, leading researchers in the database community convene to identify future directions in database research.

They produce a report of this meeting, named for the year and location of the meeting.

The most recent of these reports, the 1998 Asilomar report, identifies the embedded

database market as one of the

high growth areas in database research <a href="#Bern98">[1]</a>.

Not surprisingly, market analysts identify the embedded database

market as a high-growth area in the commercial sector as well <a href="#Host98">[5]</a>.

<p>

The Asilomar report identifies a new class of database applications, which they term "gizmo" databases, small databases embedded in tiny mobile appliances, e.g., smart-cards, telephones, personal digital assistants.

Such databases must be self-managing, secure and reliable.

Thus, the idea is that gizmo databases require plug and play data management with no database administrator (DBA), no human settable parameters, and the ability to adapt to changing conditions.

More specifically, the Asilomar authors claim that the goal is self-tuning, including defining the physical DB design, the

logical DB design, and automatic reports and utilities <a href="#Bern98">[1]</a>

To date,

few researchers have accepted this challenge, and there

is a dearth

of research literature on the subject.

<p>

Our approach to embedded database administration is fundamentally different than that described by the Asilomar authors.

We adopt their terminology, but view the challenge in supporting gizmo databases to be that of self-sustenance <em>after</em> initial deployment. Therefore, we find it, not only acceptable, but desirable to assume that application developers control initial database design and configuration. To the best of our knowledge, none of the published work in this area addresses this approach.

<p>

As the research community has not provided guidance in this arena, most work in embedded database administration has fallen to the commercial vendors.

These vendors fall into two camps, companies selling databases specifically designed for embedding or programmatic access and the major database vendors (e.g., Oracle, Informix, Sybase).

<p>

The embedded vendors all acknowledge the need for automatic administration, but fail to identify precisely how their products actually accomplish this.

A notable exception is Interbase whose white paper comparison with Sybase and Microsoft's SQL servers explicitly address features of maintenance ease.

Interbase claims that as they use no log files, there is no need for log reclamation, checkpoint tuning, or other tasks associated with log management. However, Interbase uses Transaction Information Pages, and it is unclear how these are reused or reclaimed [\[6\]](#Interbase).

Additionally, with a log-free system, they must use a FORCE policy (write all pages to disk at commit), as defined by Haerder and Reuter [\[4\]](#Haerder). This has serious performance consequences for disk-based systems.

The approach described in this paper does use logs and therefore requires log reclamation, but provides hooks so the application may reclaim logs safely and programmatically.

While Berkeley DB does require checkpoints, the goal of tuning the checkpoint interval is to bound recovery time.

Since the checkpoint interval in Berkeley DB can be expressed by the amount of log data written, it requires no tuning.

The application designer sets a target recovery time, and selects the amount of log data that can be read in that interval and specifies the checkpoint interval appropriately. Even as load changes, the time to recover does not.

<p>

The backup approaches taken by Interbase and Berkeley DB are similar in that they both allow online backup, but rather different in their affect on transactions running during backup. As Interbase performs backups as transactions [\[6\]](#Interbase), concurrent queries can suffer potentially long delays. Berkeley DB uses native operating system system utilities and recovery for backups, so there is no interference with concurrent activity, other than potential contention on disk arms.

<p>

There are a number of database vendors selling in



the embedded market (e.g., Raima, Centura, Pervasive, Faircom), but none highlight the special requirements of embedded database applications.

On the other end of the spectrum, the major vendors, Oracle, Sybase, Microsoft, are all becoming convinced of the importance of the embedded market.

As mentioned earlier, Oracle has announced its Oracle Lite server for embedded use.

Sybase has announced its UltraLite platform for "application-optimized, high-performance, SQL database engine for professional application developers building solutions for mobile and embedded platforms." [\[8\]](#).

We believe that SQL is incompatible with the gizmo database environment or truly embedded systems for which Berkeley DB is most suitable.

Microsoft research is taking a different approach, developing technology to assist in automating initial database design and index specification [\[2\]](#) [\[3\]](#).

As mentioned earlier, we believe that such configuration is, not only acceptable in the embedded market, but desirable so that applications can tune their database management for the target environment.

## 7. Conclusions

The coming wave of embedded systems poses a new set of challenges for data management.

The traditional server-based, big footprint systems designed for high performance on big iron are not the right approach in this environment.

Instead, application developers need small, fast, versatile systems that can be tailored to a specific environment.

In this paper, we have identified several of the key issues in providing these systems and shown how Berkeley DB provides many of the characteristics necessary for such applications.

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</body>  
</html>

Found in path(s):

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/refs/embedded.html

No license file was found, but licenses were detected in source scan.

Metadata-Version: 1.0

Name: bsddb3

Version: 4.4.2

Summary: Python interface for BerkeleyDB 3.2 thru 4.4

Home-page: <http://pybsddb.sf.net/>

Author: Robin Dunn, Gregory P. Smith, Andrew Kuchling, Barry Warsaw

Author-email: [pybsddb-users@lists.sourceforge.net](mailto:pybsddb-users@lists.sourceforge.net)

License: UNKNOWN

Description: This module provides a nearly complete wrapping of the Sleepycat C API for the Database Environment, Database, Cursor, and Transaction objects, and each of these is exposed as a Python type in the bsddb3.db module. The database objects can use various access methods: btree, hash, recno, and queue. For the first time all of these are fully supported in the Python wrappers. Please see the documents in the docs directory of the source distribution or at the website for more details on the types and methods provided.

Platform: UNKNOWN

Found in path(s):

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/PKG-INFO

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\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_lk\_max.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_map.html

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\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_truncate.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/dumpload/utility.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_printlog.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/txn\_commit.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_open.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/need.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/disk.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/xa/xa\_intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_verify.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-

4.4.2/docs/ref/upgrade.3.1/config.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbc\_put.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/txn/other.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_del.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_data\_dir.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/truncate.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/appsignals.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_re\_source.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/region.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/value\_set.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/scope.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/throughput.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/page.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/rpc/server.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_trickle.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/term.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_key\_range.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/lock\_put.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/re\_source.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/rmw.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/java.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/berkeley\_db\_svc.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/bt\_minkey.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/faq.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/arch/progmodel.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/read.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/verify.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/incomplete.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/rpc.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_flags.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_tx\_max.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/lock\_detect.html

\*  
 /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/terrain.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_rename.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_stat.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/close.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/stdmode.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/conflict.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/intro.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/perm.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/distrib.html  
 \*  
 /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_sleep.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/faq.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/tcl/using.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_remove.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/memp\_fget.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/error.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/reclimit.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/set\_feedback.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_unlink.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/archival.html  
 \*  
 /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/bigfile.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/log/intro.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/twoopl.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_compare.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/db.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_vxworks/notes.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_sync.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/open.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/intro.html  
 \*  
 /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_lk\_detect.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_unregister.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/ultrix.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_associate.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_errpfx.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/curclose.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/txn/nested.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/xa/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/config.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/inc.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/bt\_compare.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/app.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/disk.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/hsearch.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.2.0/system.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_close.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/lock\_vec.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_open.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_get.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/close.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbt.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/txn/limits.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/opensub.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/copy.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/xa.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/tcl/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/version.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_feedback.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/checkpoint.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/txn\_checkpoint.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/handle.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/tmp.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_errcall.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/set\_paniccall.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/tcl/error.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/tcl/faq.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/toc.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_unmap.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_rec\_init.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_put.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_lg\_bsize.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/create.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_paniccall.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/where.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/toc.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/dumpload/format.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/stat.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/notfound.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_register.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/txn\_begin.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/join.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/c\_index.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/env\_open.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/extentsize.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_stat.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_upgrade.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.2.0/toc.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/logfile.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_paniccall.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_win/notes.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-



4.4.2/docs/ref/transapp/routine.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/api\_c/env\_set\_errcall.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/why.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/distrib/layout.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/api\_c/set\_func\_read.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/txn\_prepare.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_stat.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/toc.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/ref/upgrade.3.2/set\_flags.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/cursor.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/ref/upgrade.3.0/eaccess.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_remove.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/api\_c/db\_set\_append\_recno.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/ref/upgrade.3.3/disk.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/osf1.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_fsync.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/ref/program/runtime.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_err.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/api\_c/env\_set\_lk\_conflicts.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/api\_c/env\_set\_tas\_spins.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/install/multiple.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/rpc/client.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_lsn.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/ref/upgrade.3.0/envopen.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/ref/upgrade.3.0/txn\_commit.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/utility/db\_deadlock.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/api\_c/set\_func\_write.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/api\_c/db\_set\_re\_len.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-  
4.4.2/docs/ref/upgrade.3.3/alloc.html  
\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_feedback.html

- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/rpc/intro.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/solaris.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/tx\_recover.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/logrec.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/callback.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_vxworks/intro.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/txn/config.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/arch/utilities.html
- \*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_bt\_minkey.html

- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_tx\_timestamp.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_malloc.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/memp\_register.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_open.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_win/intro.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_close.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/btstat.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/curput.html
- \*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/set\_tx\_recover.html

- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_free.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/txn\_begin.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/java/faq.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_win/faq.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/lock\_stat.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/install/rpm.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/partial.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_win/test.html
- \*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_fset.html

- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/memp\_stat.html
- \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_alloc.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/txn\_recover.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/errors.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/xa/config.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/log\_register.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/java/compat.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_upgrade.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/dbsizes.html  
 \*  
 /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/align.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/java/conf.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/refs/refs.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade/version.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/intro.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/delete.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_create.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/byteorder.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/mp/intro.html  
 \*  
 /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/put.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_put.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade/process.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_vxworks/faq.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_get.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/do.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/byteorder.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/dbinfo.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/pagesize.html  
 \*  
 /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/java/program.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_h\_ffactor.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/products.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/dead.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_bt\_prefix.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/tcl.html  
 \* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_iinfo.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/handles.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/func.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_rpc\_server.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_bt\_compare.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_verbose.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_strerror.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/test/run.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_create.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/h\_nelem.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_fsync.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/what.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbm.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_dump.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_checkpoint.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/txn/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/cxx.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/recovery.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/sync.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/stat.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_errpfx.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/freebsd.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_seek.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/renumber.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_rename.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/get.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_realloc.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/log\_register.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/upgrade.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_verify.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/xa/build.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/nested.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/xa/xa\_config.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbc\_dup.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_tmp\_dir.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/compatible.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.2.0/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_join.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_tx\_recover.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/dumpload/text.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_close.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_mutexlocks.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/qnx.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/partial.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/log/limits.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_dirfree.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/put.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/malloc.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/extending.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbc\_close.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/intro.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_file.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_fput.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/close.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/dbenv\_cxx.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/db\_dump.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/gettype.html

\*

/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/renumber.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-

4.4.2/docs/ref/upgrade.3.1/dup.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/txn\_abort.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/count.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_archive.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_get\_type.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/test.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_recover.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/bt\_prefix.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/diskspace.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/log/config.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/txn/stability.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/conf.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/logalloc.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/notxn.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_lg\_dir.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/del.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/open.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/install.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_get\_byteswapped.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/open.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/cursor.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/jump\_set.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/open.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/env.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_register.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/select.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_err.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/dbsizes.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_q\_extentsize.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbc\_count.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/notes.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbc\_get.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-

4.4.2/docs/ref/am\_conf/bt\_recnum.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.2.0/disk.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/sendmail/intro.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/dbis.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/lock\_put.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/security.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_pageyield.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/simple\_tut/keydata.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/put.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/lock\_detect.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_cachesize.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/curdel.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/dbt\_bulk.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/irix.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/tcl/program.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_stat.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_panicstate.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_flush.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/naming.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.1/systemem.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_region\_init.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_fopen.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/log\_stat.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_flags.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/getswap.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/recimp.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/cachesize.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/hpux.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/log\_archive.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-

4.4.2/docs/api\_c/env\_set\_lg\_regionmax.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_errfile.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/lock\_get.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/transapp/data\_open.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_errfile.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/dup.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/mutexlock.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_mp\_mmapsize.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/envron.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_re\_pad.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/db\_load.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/mp/config.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/arch/apis.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_cachesize.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/aix.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/second.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_set\_alloc.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/refs/witold.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/intro/dbisnot.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/db\_set\_h\_hash.html  
\*  
/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/env/faq.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am/curget.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/diskspace.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/txn\_prepare.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/intro.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/max.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/h\_hash.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/get\_bulk.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.2/toc.html  
\*



/opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/txn\_stat.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_conf/h\_ffactor.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.0/dbenv.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/memp\_fclos.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/build\_unix/shlib.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/set\_func\_exists.html  
\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/intro.html  
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\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/program/mt.html  
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\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/upgrade.3.3/shared.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/stability.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/env\_version.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/lock/cam\_conv.html

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\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/api\_c/lock\_stat.html

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\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/utility/index.html

\* /opt/cola/permits/1738309235\_1696575413.21528/0/bsddb3-4-4-2-zip/bsddb3-4.4.2/docs/ref/am\_misc/tune.html

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## 1.423 servlet-api 8.5.32

### 1.423.1 Available under license :

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- javaee\_5.xsd
- javaee\_web\_services\_1\_2.xsd
- javaee\_web\_services\_client\_1\_2.xsd
- javaee\_6.xsd
- javaee\_web\_services\_1\_3.xsd
- javaee\_web\_services\_client\_1\_3.xsd
- jsp\_2\_2.xsd

- web-app\_3\_0.xsd
- web-common\_3\_0.xsd
- web-fragment\_3\_0.xsd
- javaee\_7.xsd
- javaee\_web\_services\_1\_4.xsd
- javaee\_web\_services\_client\_1\_4.xsd
- jsp\_2\_3.xsd
- web-app\_3\_1.xsd
- web-common\_3\_1.xsd
- web-fragment\_3\_1.xsd

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- javaee\_web\_services\_1\_2.xsd
- javaee\_web\_services\_client\_1\_2.xsd
- javaee\_6.xsd
- javaee\_web\_services\_1\_3.xsd
- javaee\_web\_services\_client\_1\_3.xsd



- jsp\_2\_2.xsd
- web-app\_3\_0.xsd
- web-common\_3\_0.xsd
- web-fragment\_3\_0.xsd
- javaee\_7.xsd
- javaee\_web\_services\_1\_4.xsd
- javaee\_web\_services\_client\_1\_4.xsd
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\* Inki Dae <inki.dae@samsung.com>

\* Joonyoung Shim <jy0922.shim@samsung.com>

\* Seung-Woo Kim <sw0312.kim@samsung.com>

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-

2.4.81/vc4/vc4\_qpu\_defines.h

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmHash.h

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2.4.81/freedreno/freedreno\_ringbuffer.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/freedreno\_bo.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/freedreno\_pipe.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/freedreno\_drmif.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/freedreno\_priv.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/freedreno\_device.c  
\*  
/opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
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\* Created: Sun Apr 18 09:35:45 1999 by faith@precisioninsight.com

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\*

\* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

\*

\* DESCRIPTION

\*

\* This file contains a straightforward implementation of a fixed-sized

\* hash table using self-organizing linked lists [Knuth73, pp. 398-399] for

\* collision resolution. There are two potentially interesting things

\* about this implementation:

\*

\* 1) The table is power-of-two sized. Prime sized tables are more

\* traditional, but do not have a significant advantage over power-of-two

\* sized table, especially when double hashing is not used for collision

\* resolution.



\*

\* 2) The hash computation  
uses a table of random integers [Hanson97,  
\* pp. 39-41].

\*

#### \* FUTURE ENHANCEMENTS

\*

\* With a table size of 512, the current implementation is sufficient for a  
\* few hundred keys. Since this is well above the expected size of the  
\* tables for which this implementation was designed, the implementation of  
\* dynamic hash tables was postponed until the need arises. A common (and  
\* naive) approach to dynamic hash table implementation simply creates a  
\* new hash table when necessary, rehashes all the data into the new table,  
\* and destroys the old table. The approach in [Larson88] is superior in  
\* two ways: 1) only a portion of the table is expanded when needed,  
\* distributing the expansion cost over several insertions, and 2) portions  
\* of the table can be locked, enabling a scalable thread-safe  
\* implementation.

\*

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\*

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\* 1988, pp. 446-457.

\*

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Found in path(s):

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/hash.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmHash.c

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\* Authors:

\* Gareth Hughes <gareth@valinux.com>

\* Kevin E. Martin <martin@valinux.com>

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\* Authors:

\* Rob Clark <rob@ti.com>

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\* Paulo Zandoni <paulo.r.zandoni@intel.com>

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-

2.4.81/radeon/radeon\_cs\_gem.c

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2.4.81/intel/intel_bufmgr_fake.c

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\* Christian Gmeiner <christian.gmeiner@gmail.com>

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2.4.81/tests/etnaviv/etnaviv\_bo\_cache\_test.c

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/etnaviv/etnaviv\_bo\_cache.c

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2.4.81/etnaviv/etnaviv\_gpu.c

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/etnaviv/etnaviv\_cmd\_stream\_test.c

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2.4.81/tests/modetest/modetest.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/modetest/buffers.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/modetest/buffers.h

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- \* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/exynos/fimg2d\_reg.h
- \* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/exynos/exynos\_fimg2d\_test.c
- \* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/exynos/exynos\_fimg2d.c

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/\* radeon\_drm.h -- Public header for the radeon driver -\*- linux-c -\*-

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/include/drm/radeon\_drm.h

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2.4.81/etnaviv/etnaviv\_drmif.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/etnaviv/etnaviv\_priv.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/etnaviv/etnaviv\_2d\_test.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/etnaviv/etnaviv\_pipe.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/etnaviv/etnaviv\_cmd\_stream.c

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\* Gareth Hughes <gareth@valinux.com>  
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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kms/libkms-test-crtc.c

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/intel/mm.h

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/radeon/radeon\_surface.c

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/radeon/radeon\_surface.h

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/\* savage\_drm.h -- Public header for the savage driver

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\*/opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/include/drm/savage\_drm.h

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/etnaviv/state.xml.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/etnaviv/state\_2d.xml.h

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/include/drm/drm\_mode.h

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/\* amdgpu\_drm.h -- Public header for the amdgpu driver \*- linux-c \*-

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\*  
\* Authors:  
\* Kevin E. Martin <martin@valinux.com>  
\* Gareth Hughes <gareth@valinux.com>  
\* Keith Whitwell <keith@tungstengraphics.com>  
\*/

Found in path(s):

\*/opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/include/drm/amdgpu\_drm.h

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/\* xf86drmRandom.c -- "Minimal Standard" PRNG Implementation  
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\*

\* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

\*

\* DESCRIPTION

\*

\* This file contains a simple, straightforward implementation of the Park  
\* & Miller "Minimal Standard" PRNG [PM88, PMS93], which is a Lehmer  
\* multiplicative linear congruential generator (MLCG) with a period of  
\*  $2^{31}-1$ .

\*

\* This implementation is intended to provide a reliable, portable PRNG  
\* that is suitable for testing a hash table implementation and for  
\* implementing skip lists.

\*

\* FUTURE ENHANCEMENTS

\*

\* If initial seeds are not selected randomly, two instances of the PRNG  
\* can be correlated.

[Knuth81, pp. 32-33] describes a shuffling technique

\* that can eliminate this problem.

\*

\* If PRNGs are used for simulation, the period of the current  
\* implementation may be too short. [LE88] discusses methods of combining  
\* MLCGs to produce much longer periods, and suggests some alternative  
\* values for A and M. [LE90 and Sch92] also provide information on  
\* long-period PRNGs.

\*

\* REFERENCES

\*

\* [Knuth81] Donald E. Knuth. The Art of Computer Programming. Volume 2:  
\* Seminumerical Algorithms. Reading, Massachusetts: Addison-Wesley, 1981.

\*

\* [LE88] Pierre L'Ecuyer. "Efficient and Portable Combined Random Number  
\* Generators". CACM 31(6), June 1988, pp. 742-774.

\*

\* [LE90] Pierre L'Ecuyer. "Random Numbers for Simulation". CACM 33(10),  
\* October 1990, pp. 85-97.

\*

\* [PM88] Stephen K. Park and Keith W. Miller. "Random Number Generators:  
\* Good Ones are Hard to Find". CACM 31(10), October 1988, pp. 1192-1201.

\*

\* [Sch92] Bruce Schneier. "Pseudo-Ransom Sequence  
\* Generator for 32-Bit

\* CPUs". Dr. Dobbs's Journal 17(2), February 1992, pp. 34, 37-38, 40.

\*

\* [PMS93] Stephen K. Park, Keith W. Miller, and Paul K. Stockmeyer. In  
\* "Technical Correspondence: Remarks on Choosing and Implementing Random  
\* Number Generators". CACM 36(7), July 1993, pp. 105-110.  
\*  
\*/

Found in path(s):

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/random.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmRandom.c  
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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/amdgpu/amdgpu\_vamgr.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/amdgpu/uvd\_messages.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/amdgpu.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/amdgpu\_cs.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/amdgpu/amdgpu\_device.c

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libdrm\_macros.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/tegra/openclose.c

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/exynos/exynos\_fimg2d\_perf.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/exynos/exynos\_fimg2d\_event.c

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```

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```
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/etnaviv/write_bmp.h
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/tests/etnaviv/write_bmp.c
```

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```
<!--
```

```
Written 2012 by David Herrmann <dh.herrmann@googlemail.com>
```

```
Dedicated to the Public Domain
```

```
-->
```

Found in path(s):

```
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/man/drmModeGetResources.xml
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/man/drm.xml
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/man/drm-
memory.xml
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
2.4.81/man/drmAvailable.xml
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
```



2.4.81/man/drmHandleEvent.xml

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/man/drm-kms.xml

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/\* xf86drmSL.c -- Skip list support

\* Created: Mon May 10 09:28:13 1999 by faith@precisioninsight.com

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\* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

\*

\* DESCRIPTION

\*

\* This file contains a straightforward skip list implementation.n

\*

\* FUTURE ENHANCEMENTS

\*

\* REFERENCES

\*

\* [Pugh90] William Pugh. Skip Lists: A Probabilistic Alternative to

\* Balanced Trees. CACM 33(6), June 1990, pp. 668-676.

\*

\*/

Found in path(s):

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmSL.c

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```
*/opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/libkms.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/dumb.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/intel.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/kmstest/main.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/internal.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/radeon.c

\*

/opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/vmwgfx.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/nouveau.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/api.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/libkms/linux.c

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/include/drm/via\_drm.h

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\* Authors:  
\* Jerome Glisse <j.glisse@gmail.com>  
\*/

Found in path(s):

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/radeon/rbo.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/tests/radeon/radeon\_ttm.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/radeon/rbo.h  
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\* DRM based mode setting test program  
\* Copyright (C) 2014 Red Hat  
\* Author: Rob Clark <robdclark@gmail.com>  
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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-

2.4.81/tests/modetest/cursor.h

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```

```
AC_PREREQ([2.63])
AC_INIT([libdrm],
        [2.4.81],
        [https://bugs.freedesktop.org/enter_bug.cgi?product=DRI],
        [libdrm])
```

```
AC_CONFIG_HEADERS([config.h])
AC_CONFIG_SRCDIR([Makefile.am])
AC_CONFIG_MACRO_DIR([m4])
AC_CONFIG_AUX_DIR([build-aux])
PKG_PROG_PKG_CONFIG
```

```
# Require xorg-macros minimum of 1.12 for XORG_WITH_XSLTPROC
m4_ifndef([XORG_MACROS_VERSION],
          [m4_fatal([must install xorg-macros 1.12 or later before running autoconf/autogen])])
XORG_MACROS_VERSION(1.12)
XORG_WITH_XSLTPROC
XORG_MANPAGE_SECTIONS
```

```
AM_INIT_AUTOMAKE([1.10 foreign dist-bzip2])
```

```
# Enable quiet compiles on automake 1.11.
m4_ifdef([AM_SILENT_RULES], [AM_SILENT_RULES([yes])])
```

```
# Check for programs
AC_PROG_CC
AC_PROG_CC_C99
```

```
if test "x$ac_cv_prog_cc_c99" = xno; then
  AC_MSG_ERROR([Building libdrm requires
  C99 enabled compiler])
fi
```

```
AC_USE_SYSTEM_EXTENSIONS
AC_SYS_LARGEFILE
AC_FUNC_ALLOCA
```

```
save_CFLAGS="$CFLAGS"
export CFLAGS="$CFLAGS -Werror"
AC_HEADER_MAJOR
CFLAGS="$save_CFLAGS"
```

```
AC_CHECK_HEADERS([sys/sysctl.h sys/select.h])
```

```
# Initialize libtool
```

```
LT_PREREQ([2.2])
```

```
LT_INIT([disable-static])
```

dnf pthread-stubs is mandatory on BSD platforms, due to the nature of the dnf project. Even then there's a notable issue as described in the project README

case "\$host\_os" in

```
linux* | cygwin* | darwin* | solaris* | *-gnu* | gnu*)
```

```
    pthread_stubs_possible="no"
```

```
    ;;
```

```
*)
```

```
    pthread_stubs_possible="yes"
```

```
    ;;
```

```
esac
```

```
if test "x$pthread_stubs_possible" = xyes; then
```

```
    PKG_CHECK_MODULES(PTHREADSTUBS, pthread-stubs >= 0.4)
```

```
    AC_SUBST(PTHREADSTUBS_CFLAGS)
```

```
    AC_SUBST(PTHREADSTUBS_LIBS)
```

```
fi
```

```
pkgconfigdir=${libdir}/pkgconfig
```

```
AC_SUBST(pkgconfigdir)
```

```
AC_ARG_ENABLE([udev],
```

```
    [AS_HELP_STRING([--enable-udev],
```

```
        [Enable support for using udev instead of mknod (default: disabled)]),
```

```
    [UDEV=$enableval], [UDEV=no])
```

```
AC_ARG_ENABLE([libkms],
```

```
    [AS_HELP_STRING([--disable-libkms],
```

```
        [Disable KMS mm abstraction library (default: auto, enabled on supported platforms)]),
```

```
    [LIBKMS=$enableval], [LIBKMS=auto])
```

```
AC_ARG_ENABLE([intel],
```

```
    [AS_HELP_STRING([--disable-intel],
```

```
        [Enable support for intel's KMS API (default: auto, enabled on x86)]),
```

```
    [INTEL=$enableval], [INTEL=auto])
```

```
AC_ARG_ENABLE([radeon],
```

```
    [AS_HELP_STRING([--disable-radeon],
```

```
        [Enable support for radeon's KMS API (default: auto)]),
```

```
    [RADEON=$enableval], [RADEON=auto])
```

```
AC_ARG_ENABLE([amdgpu],
```

```
    [AS_HELP_STRING([--disable-amdgpu],
```

```
        [Enable support for amdgpu's KMS API (default: auto)]),
```



[AMDGPU=\$enableval], [AMDGPU=auto])

AC\_ARG\_ENABLE(nouveau,  
AS\_HELP\_STRING([--disable-nouveau],  
[Enable support for nouveau's KMS API (default: auto)]),  
[NOUVEAU=\$enableval], [NOUVEAU=auto])

AC\_ARG\_ENABLE(vmwgfx,  
AS\_HELP\_STRING([--disable-vmwgfx],  
  
[Enable support for vmwgfx's KMS API (default: yes)]),  
[VMWGFYX=\$enableval], [VMWGFYX=yes])

AC\_ARG\_ENABLE(omap-experimental-api,  
AS\_HELP\_STRING([--enable-omap-experimental-api],  
[Enable support for OMAP's experimental API (default: disabled)]),  
[OMAP=\$enableval], [OMAP=no])

AC\_ARG\_ENABLE(exynos-experimental-api,  
AS\_HELP\_STRING([--enable-exynos-experimental-api],  
[Enable support for EXYNOS's experimental API (default: disabled)]),  
[EXYNOS=\$enableval], [EXYNOS=no])

AC\_ARG\_ENABLE(freedreno,  
AS\_HELP\_STRING([--disable-freedreno],  
[Enable support for freedreno's KMS API (default: auto, enabled on arm)]),  
[FREEDRENO=\$enableval], [FREEDRENO=auto])

AC\_ARG\_ENABLE(freedreno-kgsl,  
AS\_HELP\_STRING([--enable-freedreno-kgsl],  
[Enable support for freedreno's to use downstream android kernel API (default: disabled)]),  
[FREEDRENO\_KGSL=\$enableval], [FREEDRENO\_KGSL=no])

AC\_ARG\_ENABLE(tegra-experimental-api,  
  
AS\_HELP\_STRING([--enable-tegra-experimental-api],  
[Enable support for Tegra's experimental API (default: disabled)]),  
[TEGRA=\$enableval], [TEGRA=no])

AC\_ARG\_ENABLE(vc4,  
AS\_HELP\_STRING([--disable-vc4],  
[Enable support for vc4's API (default: auto, enabled on arm)]),  
[VC4=\$enableval], [VC4=auto])

AC\_ARG\_ENABLE(etnaviv-experimental-api,  
AS\_HELP\_STRING([--enable-etnaviv-experimental-api],  
[Enable support for etnaviv's experimental API (default: disabled)]),  
[ETNAVIV=\$enableval], [ETNAVIV=no])

```
AC_ARG_ENABLE(install-test-programs,
  AS_HELP_STRING(--enable-install-test-programs,
  [Install test programs (default: no)]),
  [INSTALL_TESTS=$enableval], [INSTALL_TESTS=no])
```

```
dnl =====
```

```
dnl check compiler flags
```

```
AC_DEFUN([LIBDRM_CC_TRY_FLAG], [
  AC_MSG_CHECKING([whether $CC supports $1])
```

```
  libdrm_save_CFLAGS="$CFLAGS"
  CFLAGS="$CFLAGS
  $1"
```

```
  AC_COMPILE_IFELSE([AC_LANG_SOURCE([ ])], [libdrm_cc_flag=yes], [libdrm_cc_flag=no])
  CFLAGS="$libdrm_save_CFLAGS"
```

```
  if test "x$libdrm_cc_flag" = "xyes"; then
    ifelse([$2], , :, [$2])
  else
    ifelse([$3], , :, [$3])
  fi
  AC_MSG_RESULT([$libdrm_cc_flag])
])
```

```
dnl We use clock_gettime to check for timeouts in drmWaitVBlank
```

```
AC_CHECK_FUNCS([clock_gettime], [CLOCK_LIB=],
  [AC_CHECK_LIB([rt], [clock_gettime], [CLOCK_LIB=-lrt],
  [AC_MSG_ERROR([Couldn't find clock_gettime]])])])
AC_SUBST([CLOCK_LIB])
```

```
AC_CHECK_FUNCS([open_memstream], [HAVE_OPEN_MEMSTREAM=yes])
```

```
dnl Use lots of warning flags with gcc and compatible compilers
```

```
dnl Note: if you change the following variable, the cache is automatically
dnl skipped and all flags rechecked. So there's no need to do anything
dnl else. If for any reason you need to force a recheck, just change
dnl MAYBE_WARN in an ignorable way (like adding whitespace)
```

```
MAYBE_WARN="-Wall -Wextra \
-Wsign-compare
-Werror-implicit-function-declaration \
-Wpointer-arith -Wwrite-strings -Wstrict-prototypes \
-Wmissing-prototypes -Wmissing-declarations -Wnested-externs \
-Wpacked -Wswitch-enum -Wmissing-format-attribute \
```

```

-Wstrict-aliasing=2 -Winit-self \
-Wdeclaration-after-statement -Wold-style-definition \
-Wno-unused-parameter \
-Wno-attributes -Wno-long-long -Winline -Wshadow \
-Wno-missing-field-initializers"

# invalidate cached value if MAYBE_WARN has changed
if test "x$libdrm_cv_warn_maybe" != "x$MAYBE_WARN"; then
unset libdrm_cv_warn_cflags
fi
AC_CACHE_CHECK([for supported warning flags], libdrm_cv_warn_cflags, [
echo
WARN_CFLAGS=""

# Some warning options are not supported by all versions of
# gcc, so test all desired options against the current
# compiler.
#
# Note that there are some order dependencies
# here. Specifically, an option that disables a warning will
# have no net effect if a later option then enables that
# warnings, (perhaps implicitly). So we put
some grouped
# options (-Wall and -Wextra) up front and the -Wno options
# last.

for W in $MAYBE_WARN; do
LIBDRM_CC_TRY_FLAG([$W], [WARN_CFLAGS="$WARN_CFLAGS $W"])
done

libdrm_cv_warn_cflags=$WARN_CFLAGS
libdrm_cv_warn_maybe=$MAYBE_WARN

AC_MSG_CHECKING([which warning flags were supported])
WARN_CFLAGS="$libdrm_cv_warn_cflags"

# Check for atomic intrinsics
AC_CACHE_CHECK([for native atomic primitives], drm_cv_atomic_primitives, [
drm_cv_atomic_primitives="none"

AC_LINK_IFELSE([AC_LANG_PROGRAM([
int atomic_add(int *i) { return __sync_add_and_fetch (i, 1); }
int atomic_cmpxchg(int *i, int j, int k) { return __sync_val_compare_and_swap (i, j, k); }
],[[[]]]), [drm_cv_atomic_primitives="Intel"],[])

if test "x$drm_cv_atomic_primitives" = "xnone"; then
AC_CHECK_HEADER([atomic_ops.h], drm_cv_atomic_primitives="libatomic-ops")
fi

```

```

# atomic functions defined in <atomic.h> & libc on Solaris
if test "$drm_cv_atomic_primitives" = "xnone"; then
    AC_CHECK_FUNC([atomic_cas_uint],
drm_cv_atomic_primitives="Solaris")
fi
)

if test "$drm_cv_atomic_primitives" = xIntel; then
    AC_DEFINE(HAVE_LIBDRM_ATOMICS, 1,
    [Enable if your compiler supports the Intel __sync_* atomic primitives])
fi
if test "$drm_cv_atomic_primitives" = "xlibatomic-ops"; then
    AC_DEFINE(HAVE_LIB_ATOMICS_OPS, 1, [Enable if you have libatomic-ops-dev installed])
fi

dnl Print out the appropriate message considering the value set be the
dnl respective in $1.
dnl $1 - value to be evaluated. Eg. $INTEL, $NOUVEAU, ...
dnl $2 - libdrm shortname. Eg. intel, freedreno, ...
dnl $3 - GPU name/brand. Eg. Intel, NVIDIA Tegra, ...
dnl $4 - Configure switch. Eg. intel, omap-experimental-api, ...
AC_DEFUN([LIBDRM_ATOMICS_NOT_FOUND_MSG], [
    case "$1" in
        xyes) AC_MSG_ERROR([libdrm_$2 depends upon atomic operations, which were not found for your compiler/cpu.
Try compiling with -march=native, or install the libatomics-op-dev package, or, failing both of those, disable
support for $3
GPUs by passing --disable-$4 to ./configure]) ;;
        xauto) AC_MSG_WARN([Disabling $2. It depends on atomic operations, which were not found for your
compiler/cpu. Try compiling with -march=native, or install the libatomics-op-dev package.]) ;;
        *) ;;
    esac
])

if test "$drm_cv_atomic_primitives" = "xnone"; then
    LIBDRM_ATOMICS_NOT_FOUND_MSG($INTEL, intel, Intel, intel)
    INTEL=no

    LIBDRM_ATOMICS_NOT_FOUND_MSG($RADEON, radeon, Radeon, radeon)
    RADEON=no

    LIBDRM_ATOMICS_NOT_FOUND_MSG($AMDGPU, amdgpu, AMD, amdgpu)
    AMDGPU=no

    LIBDRM_ATOMICS_NOT_FOUND_MSG($NOUVEAU, nouveau, NVIDIA, nouveau)
    NOUVEAU=no

    LIBDRM_ATOMICS_NOT_FOUND_MSG($OMAP, omap, OMAP, omap-experimental-api)

```

OMAP=no

LIBDRM\_ATOMICS\_NOT\_FOUND\_MSG(\$FREEDRENO, freedreno, Qualcomm Adreno, freedreno)  
FREEDRENO=no

LIBDRM\_ATOMICS\_NOT\_FOUND\_MSG(\$TEGRA, tegra, NVIDIA Tegra, tegra-experimental-api)  
TEGRA=no

LIBDRM\_ATOMICS\_NOT\_FOUND\_MSG(\$ETNAVIV, etnaviv, Vivante, etnaviv-experimental-api)  
ETNAVIV=no

else

if test "x\$INTEL"

= xauto; then

case \$host\_cpu in

i?86|x86\_64) INTEL=yes ;;

\*) INTEL=no ;;

esac

fi

if test "x\$RADEON" = xauto; then

RADEON=yes

fi

if test "x\$AMDGPU" = xauto; then

AMDGPU=yes

fi

if test "x\$NOUVEAU" = xauto; then

NOUVEAU=yes

fi

if test "x\$FREEDRENO" = xauto; then

case \$host\_cpu in

arm\*|aarch64) FREEDRENO=yes ;;

\*) FREEDRENO=no ;;

esac

fi

if test "x\$VC4" = xauto; then

case \$host\_cpu in

arm\*|aarch64) VC4=yes ;;

\*) VC4=no ;;

esac

fi

fi

if test "x\$INTEL" != "xno"; then

PKG\_CHECK\_MODULES(PCIACCESS, [pciaccess >= 0.10])

fi

AC\_SUBST(PCIACCESS\_CFLAGS)

AC\_SUBST(PCIACCESS\_LIBS)

if test "x\$UDEV" = xyes; then

```
AC_DEFINE(UDEV, 1, [Have UDEV support])
```

```
fi
```

```
AC_CANONICAL_HOST
```

```
if test "x$LIBKMS" = xauto ; then
```

```
case $host_os in
```

```
linux*) LIBKMS="yes" ;;
```

```
freebsd* | kfreebsd*-gnu)
```

```
LIBKMS="yes" ;;
```

```
dragonfly*) LIBKMS="yes" ;;
```

```
*) LIBKMS="no" ;;
```

```
esac
```

```
fi
```

```
AM_CONDITIONAL(HAVE_LIBKMS, [test "x$LIBKMS" = xyes])
```

```
AM_CONDITIONAL(HAVE_INTEL, [test "x$INTEL"  
= xyes])
```

```
if test "x$INTEL" = xyes; then
```

```
AC_DEFINE(HAVE_INTEL, 1, [Have intel support])
```

```
fi
```

```
AM_CONDITIONAL(HAVE_VMWGFX, [test "x$VMWGFX" = xyes])
```

```
if test "x$VMWGFX" = xyes; then
```

```
AC_DEFINE(HAVE_VMWGFX, 1, [Have vmwgfx kernel headers])
```

```
fi
```

```
AM_CONDITIONAL(HAVE_NOUVEAU, [test "x$NOUVEAU" = xyes])
```

```
if test "x$NOUVEAU" = xyes; then
```

```
AC_DEFINE(HAVE_NOUVEAU, 1, [Have nouveau (nvidia) support])
```

```
fi
```

```
AM_CONDITIONAL(HAVE_OMAP, [test "x$OMAP" = xyes])
```

```
if test "x$OMAP" = xyes; then
```

```
AC_DEFINE(HAVE_OMAP, 1, [Have OMAP support])
```

```
fi
```

```
AM_CONDITIONAL(HAVE_EXYNOS, [test "x$EXYNOS" = xyes])
```

```
if test "x$EXYNOS" = xyes; then
```

```
AC_DEFINE(HAVE_EXYNOS, 1, [Have EXYNOS support])
```

```
fi
```

```
AM_CONDITIONAL(HAVE_FREEDRENO, [test "x$FREEDRENO" = xyes])
```

```
if test "x$FREEDRENO" = xyes; then
```

```
AC_DEFINE(HAVE_FREEDRENO, 1, [Have freedreno support])
```

```
fi
```

```
if test "x$FREEDRENO_KGSL" = xyes; then
```

```

if test "x$FREEDRENO" != xyes; then
  AC_MSG_ERROR([Cannot enable freedreno KGSL interface if freedreno is disabled])
fi
fi
AM_CONDITIONAL(HAVE_FREEDRENO_KGSL,
  [test "x$FREEDRENO_KGSL" = xyes])
if test "x$FREEDRENO_KGSL" = xyes; then
  AC_DEFINE(HAVE_FREEDRENO_KGSL, 1, [Have freedreno support for KGSL kernel interface])
fi

AM_CONDITIONAL(HAVE_RADEON, [test "x$RADEON" = xyes])
if test "x$RADEON" = xyes; then
  AC_DEFINE(HAVE_RADEON, 1, [Have radeon support])
fi

if test "x$AMDGPU" != xno; then
  # Detect cunit library
  PKG_CHECK_MODULES([CUNIT], [cunit >= 2.1], [have_cunit=yes], [have_cunit=no])
  # If pkg-config does not find cunit, check it using AC_CHECK_LIB. We
  # do this because Debian (Ubuntu) lacks pkg-config file for cunit.
  # fixed in 2.1-2.dfsg-3: http://anonscm.debian.org/cgit/collab-maint/cunit.git/commit/?h=debian
  if test "x${have_cunit}" = "xno"; then
    AC_CHECK_LIB([cunit], [CU_initialize_registry], [have_cunit=yes], [have_cunit=no])
    if test "x${have_cunit}" = "xyes"; then
      CUNIT_LIBS="-lcunit"
      CUNIT_CFLAGS=""
      AC_SUBST([CUNIT_LIBS])
      AC_SUBST([CUNIT_CFLAGS])
    fi
  fi
  else
    have_cunit=no
  fi
  AM_CONDITIONAL(HAVE_CUNIT,
    [test "x$have_cunit" != "xno"])

  AM_CONDITIONAL(HAVE_AMDGPU, [test "x$AMDGPU" = xyes])
  if test "x$AMDGPU" = xyes; then
    AC_DEFINE(HAVE_AMDGPU, 1, [Have amdgpu support])

    AC_DEFINE(HAVE_CUNIT, [test "x$have_cunit" != "xno"], [Enable CUNIT Have amdgpu support])

    if test "x$have_cunit" = "xno"; then
      AC_MSG_WARN([Could not find cunit library. Disabling amdgpu tests])
    fi
  fi

  AM_CONDITIONAL(HAVE_TEGRA, [test "x$TEGRA" = xyes])

```

```

if test "x$TEGRA" = xyes; then
  AC_DEFINE(HAVE_TEGRA, 1, [Have Tegra support])
fi

AM_CONDITIONAL(HAVE_VC4, [test "x$VC4" = xyes])
if test "x$VC4" = xyes; then
  AC_DEFINE(HAVE_VC4, 1, [Have VC4 support])
fi

AM_CONDITIONAL(HAVE_ETNAVIV, [test "x$ETNAVIV" = xyes])
if test "x$ETNAVIV" = xyes; then
  AC_DEFINE(HAVE_ETNAVIV, 1, [Have etnaviv support])
fi

AM_CONDITIONAL(HAVE_INSTALL_TESTS, [test "x$INSTALL_TESTS" = xyes])
if test "x$INSTALL_TESTS" = xyes; then
  AC_DEFINE(HAVE_INSTALL_TESTS, 1, [Install test programs])
fi

AC_ARG_ENABLE([cairo-tests],
  [AS_HELP_STRING([--enable-cairo-tests],
    [Enable support for Cairo rendering in tests (default: auto)]),
  [CAIRO=$enableval], [CAIRO=auto])
if test "x$CAIRO" != xno; then
  PKG_CHECK_MODULES(CAIRO, cairo, [HAVE_CAIRO=yes], [HAVE_CAIRO=no])
fi
AC_MSG_CHECKING([whether to enable Cairo tests])
if test "x$CAIRO" = xauto; then
  CAIRO="$HAVE_CAIRO"
fi
if test "x$CAIRO" = xyes; then
  if ! test "x$HAVE_CAIRO" = xyes; then
    AC_MSG_ERROR([Cairo support required but not present])
  fi
  AC_DEFINE(HAVE_CAIRO, 1, [Have Cairo support])
fi
AC_MSG_RESULT([$CAIRO])
AM_CONDITIONAL(HAVE_CAIRO, [test "x$CAIRO" = xyes])

# xsltproc for docbook manpages
AC_ARG_ENABLE([manpages],
  AS_HELP_STRING([--enable-manpages], [enable manpages @<:@default=auto@:>@]),
  [MANS=$enableval], [MANS=auto])
AM_CONDITIONAL([BUILD_MANPAGES], [test "x$XSLTPROC" != "x" -a "x$MANS" != "xno"])

# check for offline man-pages stylesheet
AC_MSG_CHECKING([for

```



```

docbook manpages stylesheet])
MANPAGES_STYLESHEET="http://docbook.sourceforge.net/release/xsl/current/manpages/docbook.xsl"
AC_PATH_PROGS_FEATURE_CHECK([XSLTPROC_TMP], [xsltproc],
    AS_IF([ "$ac_path_XSLTPROC_TMP" --nonet "$MANPAGES_STYLESHEET" > /dev/null
2>&1`],
        [HAVE_MANPAGES_STYLESHEET=yes]))
if test "x$HAVE_MANPAGES_STYLESHEET" = "xyes"; then
    AC_SUBST(MANPAGES_STYLESHEET)
    AC_MSG_RESULT([yes])
else
    AC_MSG_RESULT([no])
fi
AM_CONDITIONAL([HAVE_MANPAGES_STYLESHEET], [test "x$HAVE_MANPAGES_STYLESHEET" =
"xyes"])

AC_ARG_ENABLE(valgrind,
    [AS_HELP_STRING([--enable-valgrind],
        [Build libdrm with valgrind support (default: auto)]),
    [VALGRIND=$enableval], [VALGRIND=auto])
if test "x$VALGRIND" != xno; then
    PKG_CHECK_MODULES(VALGRIND, [valgrind], [have_valgrind=yes], [have_valgrind=no])
fi
AC_MSG_CHECKING([whether to enable Valgrind
support])
if test "x$VALGRIND" = xauto; then
    VALGRIND="$have_valgrind"
fi

if test "x$VALGRIND" = "xyes"; then
    if ! test "x$have_valgrind" = xyes; then
        AC_MSG_ERROR([Valgrind support required but not present])
    fi
    AC_DEFINE([HAVE_VALGRIND], 1, [Use valgrind intrinsics to suppress false warnings])
fi

AC_MSG_RESULT([$VALGRIND])

AC_ARG_WITH([kernel-source],
    [AS_HELP_STRING([--with-kernel-source],
        [specify path to linux kernel source]),
    [kernel_source="$with_kernel_source"])
AC_SUBST(kernel_source)

AC_MSG_CHECKING([whether $CC supports __attribute__(("hidden"))])
AC_LINK_IFELSE([AC_LANG_PROGRAM([
    int foo_hidden( void ) __attribute__((visibility("hidden")));
]), HAVE_ATTRIBUTE_VISIBILITY="yes"; AC_MSG_RESULT([yes]), AC_MSG_RESULT([no]));

```

```
if test "x$HAVE_ATTRIBUTE_VISIBILITY" = xyes; then
  AC_DEFINE(HAVE_VISIBILITY, 1, [Compiler supports __attribute__((hidden))])
fi
```

```
AC_SUBST(WARN_CFLAGS)
```

```
AC_CONFIG_FILES([
```

```
  Makefile
```

```
  libkms/Makefile
```

```
  libkms/libkms.pc
```

```
  intel/Makefile
```

```
  intel/libdrm_intel.pc
```

```
  radeon/Makefile
```

```
  radeon/libdrm_radeon.pc
```

```
  amdgpu/Makefile
```

```
  amdgpu/libdrm_amdgpu.pc
```

```
  nouveau/Makefile
```

```
  nouveau/libdrm_nouveau.pc
```

```
  omap/Makefile
```

```
  omap/libdrm_omap.pc
```

```
  exynos/Makefile
```

```
  exynos/libdrm_exynos.pc
```

```
  freedreno/Makefile
```

```
  freedreno/libdrm_freedreno.pc
```

```
  tegra/Makefile
```

```
  tegra/libdrm_tegra.pc
```

```
  vc4/Makefile
```

```
  vc4/libdrm_vc4.pc
```

```
  etnaviv/Makefile
```

```
  etnaviv/libdrm_etnaviv.pc
```

```
  tests/Makefile
```

```
  tests/modeprint/Makefile
```

```
  tests/modetest/Makefile
```

```
  tests/kms/Makefile
```

```
  tests/kmstest/Makefile
```

```
  tests/proptest/Makefile
```

```
  tests/radeon/Makefile
```

```
  tests/amdgpu/Makefile
```

```
  tests/vbltest/Makefile
```

```
  tests/exynos/Makefile
```

```
  tests/tegra/Makefile
```

```
  tests/nouveau/Makefile
```

```
  tests/etnaviv/Makefile
```

```
  tests/util/Makefile
```

```
  man/Makefile
```

```
  libdrm.pc])
```

```
AC_OUTPUT
```

```
echo
```

```
""
echo "$PACKAGE_STRING will be compiled with:"
echo ""
echo " libkms      $LIBKMS"
echo " Intel API   $INTEL"
echo " vmwgfx API  $VMWGFYX"
echo " Radeon API   $RADEON"
echo " AMDGPU API
  $AMDGPU"
echo " Nouveau API $NOUVEAU"
echo " OMAP API     $OMAP"
echo " EXYNOS API   $EXYNOS"
echo " Freedreno API $FREEDRENO (kgs!:$FREEDRENO_KGSL)"
echo " Tegra API    $TEGRA"
echo " VC4 API      $VC4"
echo " Etnaviv API  $ETNAVIV"
echo ""
```

Found in path(s):

```
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/configure.ac
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```

```
/*
```

```
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```
*
```

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```

```
*
```

```
*/
```

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```
* /opt/cola/permits/1742982781_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-
```

2.4.81/tests/amdgpu/amdgpu\_test.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/util\_math.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/amdgpu/basic\_tests.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/amdgpu/amdgpu\_test.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/amdgpu/cs\_tests.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/amdgpu/bo\_tests.c  
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```

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/util\_hash\_table.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/util\_hash\_table.h  
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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/include/drm/drm.h

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\* Authors:

\* Eric Anholt <eric@anholt.net>

\*

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/etnaviv/etnaviv\_device.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/etnaviv/etnaviv\_bo.c  
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2.4.81/amdgpu/amdgpu\_gpu\_info.c

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/amdgpu/amdgpu\_internal.h

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmMode.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86drmMode.h

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/radeon/radeon\_bo\_gem.c

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/msm/msm\_priv.h

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/msm/msm\_device.c

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/freedreno/msm/msm\_ringbuffer.c

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-

2.4.81/freedreno/kgsl/kgsl\_device.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/kgsl/kgsl\_bo.c  
\*  
/opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/kgsl/kgsl\_priv.h  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/msm/msm\_bo.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
2.4.81/freedreno/kgsl/kgsl\_pipe.c  
\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/amdgpu/util\_hash.c

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/exynos/exynos\_drm.c  
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/\* drmsl.c -- Skip list test

\* Created: Mon May 10 09:28:13 1999 by faith@precisioninsight.com

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\* Authors: Rickard E. (Rik) Faith <faith@valinux.com>

\*

\* DESCRIPTION

\*

\* This file contains a straightforward skip list implementation.n

\*

\* FUTURE ENHANCEMENTS

\*

\* REFERENCES

\*  
\* [Pugh90] William Pugh. Skip Lists: A Probabilistic Alternative to  
\* Balanced Trees. CACM 33(6), June 1990, pp. 668-676.  
\*  
\*/

Found in path(s):

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/tests/drmsl.c  
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/\* mga\_drm.h -- Public header for the Matrox g200/g400 driver -\*- linux-c -\*-

\* Created: Tue Jan 25 01:50:01 1999 by jhartmann@precisioninsight.com

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\*

\* Authors:

\* Jeff Hartmann <jhartmann@valinux.com>

\* Keith Whitwell <keith@tungstengraphics.com>

\*

\* Rewritten by:

\* Gareth Hughes <gareth@valinux.com>

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\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-  
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\* Chris Wilson <chris@chris-wilson.co.uk>  
\*  
\*/

Found in path(s):

\* /opt/cola/permits/1742982781\_1690267050.5573418/0/libdrm-2-4-81-tar-bz2/libdrm-2.4.81/xf86atomic.h

# 1.426 libmspack 1.10.1

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Version 2.1, February 1999

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Mesa 3-D graphics library

Version: 7.0

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## 1.430 libnss-nis 3.2

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## 1.435 python-setuptools 44.1.1

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```
@c Local Variables:
```

```
@c ispell-local-pdict: "ispell-dict"
```

```
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```

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# 1.439 log4cplus 2.1.0

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## 1.440 tiny-xml 2.8.2

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/\*

[www.sourceforge.net/projects/tinyxml](http://www.sourceforge.net/projects/tinyxml)

Original code by Lee Thomason ([www.grinninglizard.com](http://www.grinninglizard.com))

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/\*\* @mainpage

<h1> TinyXML </h1>

TinyXML is a simple, small, C++ XML parser that can be easily integrated into other programs.

<h2> What it does. </h2>

In brief, TinyXML parses an XML document, and builds from that a Document Object Model (DOM) that can be read, modified, and saved.

XML stands for "eXtensible Markup Language." It allows you to create your own document markups. Where HTML does a very good job of marking documents for browsers, XML allows you to define any kind of document markup, for example a document that describes a "to do" list for an organizer application. XML is a very structured and convenient format. All those random file formats created to store application data can all be replaced with XML. One parser for everything.

The best place for the complete, correct, and quite frankly hard to read spec is at <a href="http://www.w3.org/TR/2004/REC-xml-20040204/">http://www.w3.org/TR/2004/REC-xml-20040204/</a>.

An intro to XML

(that I really like) can be found at

<a href="http://skew.org/xml/tutorial/">http://skew.org/xml/tutorial</a>.

There are different ways to access and interact with XML data.

TinyXML uses a Document Object Model (DOM), meaning the XML data is parsed into a C++ objects that can be browsed and manipulated, and then written to disk or another output stream. You can also construct an XML document from scratch with C++ objects and write this to disk or another output stream.

TinyXML is designed to be easy and fast to learn. It is two headers and four cpp files. Simply add these to your project and off you go. There is an example file - xmltest.cpp - to get you started.

TinyXML is released under the ZLib license, so you can use it in open source or commercial code. The details of the license are at the top of every source file.

TinyXML attempts to be a flexible parser, but with truly correct and compliant XML output. TinyXML should compile on

any reasonably C++ compliant system. It does not rely on exceptions or RTTI. It can be compiled with or without STL support. TinyXML fully supports the UTF-8 encoding, and the first 64k character entities.

## <h2> What it doesn't do. </h2>

TinyXML doesn't parse or use DTDs (Document Type Definitions) or XSLs (eXtensible Stylesheet Language.) There are other parsers out there (check out [www.sourceforge.org](http://www.sourceforge.org), search for XML) that are much more fully featured. But they are also much bigger, take longer to set up in your project, have a higher learning curve, and often have a more restrictive license. If you are working with browsers or have more complete XML needs, TinyXML is not the parser for you.

The following DTD syntax will not parse at this time in TinyXML:

```
@verbatim
<!DOCTYPE Archiv [
  <!ELEMENT Comment (#PCDATA)>
]>
@endverbatim
```

because TinyXML sees this as a !DOCTYPE node with an illegally embedded !ELEMENT node. This may be addressed in the future.

## <h2> Tutorials. </h2>

For the impatient, here is a tutorial to get you going. A great way to get started, but it is worth your time to read this (very short) manual completely.

- @subpage tutorial0

## <h2> Code Status. </h2>

TinyXML is mature, tested code. It is very stable. If you find bugs, please file a bug report on the sourceforge web site ([www.sourceforge.net/projects/tinyxml](http://www.sourceforge.net/projects/tinyxml)). We'll get them straightened out as soon as possible.

There are some areas of improvement; please check sourceforge if you are interested in working on TinyXML.

## <h2> Related Projects </h2>

TinyXML projects you may find useful! (Descriptions provided by the projects.)



<ul>

<li> <b>TinyXPath</b> (<http://tinypath.sourceforge.net>). TinyXPath is a small footprint XPath syntax decoder, written in C++.</li>

<li> <b>TinyXML++</b> (<http://code.google.com/p/ticpp/>). TinyXML++ is a completely new interface to TinyXML that uses MANY of the C++ strengths. Templates, exceptions, and much better error handling.</li>

</ul>

<h2>

Features </h2>

<h3> Using STL </h3>

TinyXML can be compiled to use or not use STL. When using STL, TinyXML uses the std::string class, and fully supports std::istream, std::ostream, operator<<, and operator>>. Many API methods have both 'const char\*' and 'const std::string&' forms.

When STL support is compiled out, no STL files are included whatsoever. All the string classes are implemented by TinyXML itself. API methods all use the 'const char\*' form for input.

Use the compile time #define:

TIXML\_USE\_STL

to compile one version or the other. This can be passed by the compiler, or set as the first line of "tinyxml.h".

Note: If compiling the test code in Linux, setting the environment variable TINYXML\_USE\_STL=YES/NO will control STL compilation. In the Windows project file, STL and non STL targets are provided. In your project, It's probably easiest to add the line "#define TIXML\_USE\_STL" as the first line of tinyxml.h.

<h3> UTF-8 </h3>

TinyXML supports UTF-8 allowing to manipulate XML files in any language. TinyXML also supports "legacy mode" - the encoding used before UTF-8 support and probably best described as "extended ascii".

Normally, TinyXML will try to detect the correct encoding and use it. However, by setting the value of TIXML\_DEFAULT\_ENCODING in the header file, TinyXML can be forced to always use one encoding.

TinyXML will assume Legacy Mode until one of the following occurs:

```
<ol>
  <li> If the non-standard but common "UTF-8 lead bytes" (0xef 0xbb 0xbf)
    begin the file or data stream, TinyXML will read it as UTF-8. </li>
  <li> If the declaration tag is read, and it has an encoding="UTF-8", then
    TinyXML will read it as UTF-8. </li>
  <li> If the declaration tag is read, and it has no encoding specified, then TinyXML will
    read it as UTF-8. </li>
  <li> If the declaration tag is read, and it has an encoding="something else", then TinyXML
    will read it as Legacy Mode. In legacy mode, TinyXML will work as it did before. It's
    not clear what that mode does exactly, but old content should keep working.</li>
  <li> Until one of the above criteria is met, TinyXML runs in Legacy Mode.</li>
</ol>
```

What happens if the encoding is incorrectly set or detected? TinyXML will try to read and pass through text seen as improperly encoded. You may get some strange results or mangled characters. You may want to force TinyXML to the correct mode.

You may force TinyXML to Legacy Mode by using `LoadFile( TIXML_ENCODING_LEGACY )` or `LoadFile( filename, TIXML_ENCODING_LEGACY )`. You may force it to use legacy mode all the time by setting `TIXML_DEFAULT_ENCODING = TIXML_ENCODING_LEGACY`. Likewise, you may force it to `TIXML_ENCODING_UTF8` with the same technique.

For English users, using English XML, UTF-8 is the same as low-ASCII. You don't need to be aware of UTF-8 or change your code in any way. You can think of UTF-8 as a "superset" of ASCII.

UTF-8 is not a double byte format - but it is a standard encoding of Unicode!

TinyXML

does not use or directly support `wchar`, `TCHAR`, or Microsoft's `_UNICODE` at this time. It is common to see the term "Unicode" improperly refer to UTF-16, a wide byte encoding of unicode. This is a source of confusion.

For "high-ascii" languages - everything not English, pretty much - TinyXML can handle all languages, at the same time, as long as the XML is encoded in UTF-8. That can be a little tricky, older programs and operating systems tend to use the "default" or "traditional" code page. Many apps (and almost all modern ones) can output UTF-8, but older or stubborn (or just broken) ones still output text in the default code page.

For example, Japanese systems traditionally use SHIFT-JIS encoding.

Text encoded as SHIFT-JIS can not be read by TinyXML.

A good text editor can import SHIFT-JIS and then save as UTF-8.

The [Skew.org link](http://skew.org/xml/tutorial/) does a great job covering the encoding issue.

The test file "utf8test.xml" is an XML containing English,

Spanish, Russian, and Simplified Chinese. (Hopefully they are translated correctly). The file "utf8test.gif" is a screen capture of the XML file, rendered in IE. Note that if you don't have the correct fonts (Simplified Chinese or Russian) on your system, you won't see output that matches the GIF file even if you can parse it correctly. Also note that (at least on my Windows machine) console output is in a Western code page, so that Print() or printf() cannot correctly display the file. This is not a bug in TinyXML - just an OS issue. No data is lost or destroyed by TinyXML. The console just doesn't render UTF-8.

### <h3> Entities </h3>

TinyXML recognizes the pre-defined "character entities", meaning special characters. Namely:

```
@verbatim
&amp; &
&lt; <
&gt; >
&quot; "
&apos; '
@endverbatim
```

These are recognized when the XML document is read, and translated to there UTF-8 equivalents. For instance, text with the XML of:

```
@verbatim
Far &amp; Away
@endverbatim
```

will have the Value() of "Far & Away" when queried from the TiXmlText object, and will be written back to the XML stream/file as an ampersand. Older versions of TinyXML "preserved" character entities, but the newer versions will translate them into characters.

Additionally, any character can be specified by its Unicode code point:

The syntax "&#xA0;" or "&#160;" are both to the non-breaking space character.

### <h3> Printing </h3>

TinyXML can print output in several different ways that all have strengths and limitations.

- Print( FILE\* ). Output to a std-C stream, which includes all C files as well as stdout.
- "Pretty prints", but you don't have control over printing options.
- The output is streamed directly to the FILE object, so there is no memory overhead in the TinyXML code.
- used by Print() and SaveFile()

- operator<<. Output to a c++ stream.
- Integrates with standart C++ iostreams.
- Outputs in "network printing" mode without line breaks. Good for network transmission and moving XML between C++ objects, but hard for a human to read.
- TiXmlPrinter. Output to a std::string or memory buffer.
- API is less concise
- Future printing options will be put here.
- Printing may change slightly in future versions as it is refined and expanded.

### <h3> Streams </h3>

With TIXML\_USE\_STL on TinyXML supports C++ streams (operator <<,>>) streams as well as C (FILE\*) streams. There are some differences that you may need to be aware of.

C style output:

- based on FILE\*
- the Print() and SaveFile() methods

Generates formatted output, with plenty of white space, intended to be as human-readable as possible. They are very fast, and tolerant of ill formed XML documents. For example, an XML document that contains 2 root elements and 2 declarations, will still print.

C style input:

- based on FILE\*
- the Parse() and LoadFile() methods

A fast, tolerant read. Use whenever you don't need the C++ streams.

C++ style output:

- based on std::ostream
- operator<<

Generates

condensed output, intended for network transmission rather than readability. Depending on your system's implementation of the ostream class, these may be somewhat slower. (Or may not.) Not tolerant of ill formed XML: a document should contain the correct one root element. Additional root level elements will not be streamed out.

C++ style input:

- based on std::istream
- operator>>

Reads XML from a stream, making it useful for network transmission. The tricky part is knowing when the XML document is complete, since there will almost certainly be other data in the stream. TinyXML will assume the XML data is

complete after it reads the root element. Put another way, documents that are ill-constructed with more than one root element will not read correctly. Also note that operator>> is somewhat slower than Parse, due to both implementation of the STL and limitations of TinyXML.

### <h3> White space </h3>

The world simply does not agree on whether white space should be kept, or condensed.

For example, pretend the '\_' is a space, and look at "Hello\_\_\_\_world". HTML, and at least some XML parsers, will interpret this as "Hello\_world". They condense white space. Some XML parsers do not, and will leave it as "Hello\_\_\_\_world". (Remember to keep pretending the \_ is a space.) Others suggest that \_\_Hello\_\_world\_\_ should become Hello\_\_world.

It's an issue that hasn't been resolved to my satisfaction. TinyXML supports the first 2 approaches. Call TiXmlBase::SetCondenseWhiteSpace( bool ) to set the desired behavior. The default is to condense white space.

If you change the default, you should call TiXmlBase::SetCondenseWhiteSpace( bool ) before making any calls to Parse XML data, and I don't recommend changing it after it has been set.

### <h3> Handles </h3>

Where browsing an XML document in a robust way, it is important to check for null returns from method calls. An error safe implementation can generate a lot of code like:

```
@verbatim
TiXmlElement* root = document.FirstChildElement(
    "Document" );
if ( root )
{
    TiXmlElement* element = root->FirstChildElement( "Element" );
    if ( element )
    {
        TiXmlElement* child = element->FirstChildElement( "Child" );
        if ( child )
        {
            TiXmlElement* child2 = child->NextSiblingElement( "Child" );
            if ( child2 )
            {
                // Finally do something useful.
            }
        }
    }
}
@endverbatim
```

Handles have been introduced to clean this up. Using the TiXmlHandle class, the previous code reduces to:

```

@verbatim
TiXmlHandle docHandle( &document );
TiXmlElement* child2 = docHandle.FirstChild( "Document" ).FirstChild( "Element" ).Child( "Child", 1
).ToElement();
if ( child2 )
{
    // do something useful
}
@endverbatim

```

Which is much easier to deal with. See TiXmlHandle for more information.

### <h3> Row and Column tracking </h3>

Being able to track nodes and attributes back to their origin location in source files can be very important for some applications. Additionally, knowing where parsing errors occurred in the original source can be very time saving.

TinyXML can track the row and column origin of all nodes and attributes in a text file. The TiXmlBase::Row() and TiXmlBase::Column() methods return the origin of the node in the source text. The correct tabs can be configured in TiXmlDocument::SetTabSize().

## <h2> Using and Installing </h2>

To Compile and Run xmltest:

A Linux Makefile and a Windows Visual C++ .dsw file is provided. Simply compile and run. It will write the file demotest.xml to your disk and generate output on the screen. It also tests walking the DOM by printing out the number of nodes found using different techniques.

The Linux makefile is very generic and runs on many systems - it is currently tested on mingw and MacOSX. You do not need to run 'make depend'. The dependencies have been hard coded.

### <h3>Windows project file for VC6</h3>

- <li>tinyxml: tinyxml library, non-STL </li>
- <li>tinyxmlSTL: tinyxml library, STL </li>
- <li>tinyXmlTest: test app, non-STL </li>
- <li>tinyXmlTestSTL: test app,

STL </li>

</ul>

### <h3>Makefile</h3>

At the top of the makefile you can set:

PROFILE, DEBUG, and TINYXML\_USE\_STL. Details (such that they are) are in the makefile.

In the tinyxml directory, type "make clean" then "make". The executable file 'xmltest' will be created.

### <h3>To Use in an Application:</h3>

Add tinyxml.cpp, tinyxml.h, tinyxmlerror.cpp, tinyxmlparser.cpp, tinystr.cpp, and tinystr.h to your project or make file. That's it! It should compile on any reasonably compliant C++ system. You do not need to enable exceptions or RTTI for TinyXML.

## <h2> How TinyXML works. </h2>

An example is probably the best way to go. Take:

@verbatim

```
<?xml version="1.0" standalone=no>
```

```
<!-- Our to do list data -->
```

```
<ToDo>
```

```
  <Item priority="1"> Go to the <bold>Toy store!</bold></Item>
```

```
  <Item priority="2"> Do bills</Item>
```

```
</ToDo>
```

@endverbatim

Its not much of a To Do list, but it will do. To read this file (say "demo.xml") you would create a document, and parse it in:

@verbatim

```
TiXmlDocument
```

```
doc( "demo.xml" );
```

```
doc.LoadFile();
```

@endverbatim

And its ready to go. Now lets look at some lines and how they relate to the DOM.

@verbatim

```
<?xml version="1.0" standalone=no>
```

@endverbatim

The first line is a declaration, and gets turned into the TiXmlDeclaration class. It will be the first child of the document node.

This is the only directive/special tag parsed by TinyXML. Generally directive tags are stored in TiXmlUnknown so the commands wont be lost when it is saved back to disk.

```
@verbatim
<!-- Our to do list data -->
@endverbatim
```

A comment. Will become a TiXmlComment object.

```
@verbatim
<ToDo>
@endverbatim
```

The "ToDo" tag defines a TiXmlElement object. This one does not have any attributes, but does contain 2 other elements.

```
@verbatim
<Item priority="1">
@endverbatim
```

Creates another TiXmlElement which is a child of the "ToDo" element. This element has 1 attribute, with the name "priority" and the value "1".

```
@verbatim
Go to the
@endverbatim
```

A TiXmlText.  
This is a leaf node and cannot contain other nodes.  
It is a child of the "Item" TiXmlElement.

```
@verbatim
<bold>
@endverbatim
```

Another TiXmlElement, this one a child of the "Item" element.

Etc.

Looking at the entire object tree, you end up with:



```
@verbatim
TiXmlDocument  "demo.xml"
TiXmlDeclaration "version='1.0' "standalone=no"
TiXmlComment  " Our to do list data"
TiXmlElement  "ToDo"
TiXmlElement  "Item" Attributes: priority = 1
  TiXmlText  "Go to the "
  TiXmlElement  "bold"
  TiXmlText  "Toy store!"
TiXmlElement  "Item" Attributes: priority=2
  TiXmlText  "Do bills"
@endverbatim
```

<h2> Documentation </h2>

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The World Wide Web Consortium is the definitive standard body for XML, and their web pages contain huge amounts of information.

The definitive spec: <a href="http://www.w3.org/TR/2004/REC-xml-20040204/">

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I also recommend "XML Pocket Reference" by Robert Eckstein and published by OReilly...the book that got the whole thing started.

## Contributors, Contacts, and a Brief History

Thanks very much to everyone who sends suggestions, bugs, ideas, and encouragement. It all helps, and makes this project fun. A special thanks to the contributors on the web pages that keep it lively.

So many people have sent in bugs and ideas, that rather than list here we try to give credit due in the "changes.txt" file.

TinyXML was originally written by Lee Thomason. (Often the "I" still in the documentation.) Lee reviews changes and releases new versions, with the help of Yves Berquin, Andrew Ellerton, and the tinyXml community.

We appreciate your suggestions, and would love to know if you use TinyXML. Hopefully you will enjoy it and find it useful. Please post questions, comments, file bugs, or contact us at:

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          | 2.1.2        | 2002      | PSF        | yes                 |
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Schemas for Java EE Deployment Descriptors:

- javaee\_5.xsd
- javaee\_web\_services\_1\_2.xsd
- javaee\_web\_services\_client\_1\_2.xsd
- javaee\_6.xsd
- javaee\_web\_services\_1\_3.xsd
- javaee\_web\_services\_client\_1\_3.xsd
- jsp\_2\_2.xsd
- web-app\_3\_0.xsd
- web-common\_3\_0.xsd
- web-fragment\_3\_0.xsd
- javaee\_7.xsd
- javaee\_web\_services\_1\_4.xsd
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- \* javax.annotation:javax.annotation-api
- \* javax.transaction:javax.transaction-api
- \* javax.websocket:javax.websocket-api

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- \* java.sun.security.ssl

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<https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html>

org.eclipse.jetty.toolchain:jetty-schemas

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Assorted

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## 1.457 perl 5.32.1

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```
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### xwd utility
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# This file contains a list of people who've made non-trivial
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# who commit code to the project are encouraged to add their names  
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Neal Norwitz <nnorwitz@gmail.com>  
Nermin Ozkiranartli <nermin@google.com>  
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Piotr Kaminski <piotrk@google.com>  
Russ Rufer <russ@pentad.com>  
Sverre Sundsdal <sundsda@gmail.com>  
Takeshi Yoshino <tyoshino@google.com>  
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## The Unicode Standard, Unicode Character Database, Version 12.1.0

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@vskip 20pt plus 1fil  
@end macro
```

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@macro copyrightstart{ }  
@end macro
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@macro copyrightend{ }  
@end macro
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@node Copyrights and Licenses, , Acknowledgments, Top  
@comment node-name, next, previous, up  
@appendix Copyrights and Licenses
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@version 3.0 (December 2000)

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@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>  
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## 1.465 samba 4.11.15

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```
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@vskip 20pt plus 1fil  
@end macro
```

```
@macro copyrightstart{ }  
@end macro
```

```
@macro copyrightend{ }  
@end macro
```

```
@node Copyrights and Licenses, , Acknowledgments, Top  
@comment node-name, next, previous, up  
@appendix Copyrights and Licenses
```

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```

```
@copyrightstart  
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@heading Vincent Rijmen, Antoon Bosselaers, Paulo Barreto

AES in libhcrypto

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rijndael-alg-fst.c

@version 3.0 (December 2000)

Optimised ANSI C code for the Rijndael cipher (now AES)

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

@author Paulo Barreto <paulo.barreto@terra.com.br>

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DES core in libcrypto

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D3DES (V5.09) -

A portable, public domain, version of the Data Encryption Standard.

Written with Symantec's THINK (Lightspeed) C by Richard Outerbridge.

Thanks to: Dan Hoey for his excellent Initial and Inverse permutation code; Jim Gillogly & Phil Karn for the DES key schedule code; Dennis Ferguson, Eric Young and Dana How for comparing notes; and Ray Lau, for humouring me on.

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Windows support

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|----|-------|-------|---------|------|------|-------------|
| 1  | 32    | 7679  | 7648    | 3.7M |      | 8f8378c0-01 |
| 2  | 7680  | 16383 | 8704    | 4.3M |      | 8f8378c0-02 |
| 5  | 7936  | 12799 | 4864    | 2.4M |      |             |
| 6  | 12544 | 16127 | 3584    | 1.8M |      |             |

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```
#!/usr/bin/env bash
# MIT licenses will each have a unique MD5 sum since they contain a unique copyright.
# This script will read the license file into memory, strip out the copyright, and then
# generate an MD5 that is not unique and can be matched against a known MIT MD5 sum.
set -e

calculate_md5_sum() {
    local FILE="$1"

    # Read the contents of the file into a variable
    local raw_license_text=$(<"$FILE")

    # Convert to lowercase
    local lowercase_license_text="{raw_license_text,}"

    # Remove anything before the beginning of the license
    local stripped_license_text=$(awk '/permission is hereby granted,/{p=1} p' <<< "$lowercase_license_text")

    # Replace line breaks (Windows or Linux) with a single space
    local replaced_linebreaks=$(tr "\r\n" ' ' <<< "$stripped_license_text")

    # Replace multiple spaces with a single space
```



```

local normalized_license_text=$(tr -s ' ' <<< "$replaced_linebreaks")

# Strip away any non-printable, non-whitespace characters
local clean_unprintable_license_text=$(tr
-cd '[:print:][:space:]' <<< "$normalized_license_text")

# Remove punctuation and special characters
local clean_license_text=$(echo "$clean_unprintable_license_text" | sed 's/[^a-zA-Z ]//g')

# Calculate the MD5 sum of the cleaned license text
local MD5SUM=$(echo -n "$clean_license_text" | md5sum | awk '{print $1}')

# Output the MD5 sum
echo "$MD5SUM"
}

# Check if the FILE argument is provided
if [ -z "$1" ]; then
echo "Please provide a FILE as an argument."
exit 1
fi

# Invoke the function with the provided FILE argument
calculate_md5_sum "$1"
WEV @@ WEV[B "1
x ?"U@,5 @mISmIN<GimCN7g1u|E
43mI,5WEV @@ WEV @mImImIAmImImI0mImImI*mImI
A0mImImI...
lost+found...

;9GimCN7g

!"#$%&'()*+,-
./0123456789;:<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`abcdefghijklmnopqrstuvwxyz{|}~

!"#$%&'()*+,-
./0123456789;:<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`abcdefghijklmnopqrstuvwxyz{|}~

!"#$%&'()*+,-
./0123456789;:<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`abcdefghijklmnopqrstuvwxyz{|}~

!"#$%&'()*+,-
./0123456789;:<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`abcdefghijklmnopqrstuvwxyz{|}~WEV @@
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# 1.469 openjdk 1.8.0u131

## 1.469.1 Available under license :

```
## zlib v1.2.11
```

```
### zlib License
```

```
<pre>
```

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- the files in src/spy/
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</pre>
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### xwd utility
<pre>
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```
Benoit Sigoure <tsuna@google.com>
Bogdan Piloca <boo@google.com>
Chandler Carruth <chandlerc@google.com>
Dave MacLachlan <dmaclach@gmail.com>
David Anderson <danderson@google.com>
Dean Sturtevant
Gene Volovich <gv@cite.com>
Hal Burch <gmock@hburch.com>
Jeffrey Yasskin <jyasskin@google.com>
Jim Keller <jimkeller@google.com>
Joe Walnes <joe@truemesh.com>
Jon Wray <jwray@google.com>
Keir Mierle <mierle@gmail.com>
Keith Ray <keith.ray@gmail.com>
Kostya Serebryany <kcc@google.com>
Lev Makhlis
Manuel Klimek <klimek@google.com>
Mario Tanev <radix@google.com>
Mark Paskin
Markus Heule <markus.heule@gmail.com>
Matthew Simmons <simmonmt@acm.org>
Mike Bland <mbland@google.com>
Neal Norwitz <nnorwitz@gmail.com>
Nermin Ozkiranartli <nermin@google.com>
Owen
```

Carlsen <ocarlsen@google.com>  
Paneendra Ba <paneendra@google.com>  
Paul Menage <menage@google.com>  
Piotr Kaminski <piotrk@google.com>  
Russ Rufer <russ@pentad.com>  
Sverre Sundsdal <sundsda@gmail.com>  
Takeshi Yoshino <tyoshino@google.com>  
Vadim Berman <vadimb@google.com>  
Vlad Losev <vladl@google.com>  
Wolfgang Klier <wklier@google.com>  
Zhanyong Wan <wan@google.com>  
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*  
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*  
*/
```

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\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/dev/sep/ivytown\_r3qpi.c

\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/dev/sep/ivytown\_ubox.c

\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-

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bz2/sepdk/inc/ivytown_r2pcie.h
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bz2/sepdk/inc/jaketown_ubox.h
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bz2/sepdk/dev/sep/ivytown_r2pcie.c
* /opt/cola/permits/1822866827_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-
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/opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/inc/montecito.h

\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/inc/corei7\_unc.h

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\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/include/pax\_shared.h

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\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/include/tbrw\_types.h

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/opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/bin/sfdump5: binary file matches

Found in path(s):

\* /bin/grep

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bz2/sepdk/dev/sep/sys64.S
* /opt/cola/permits/1822866827_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-
bz2/sepdk/dev/sep/sys32.S
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* /opt/cola/permits/1822866827_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-
bz2/sepdk/inc/ivytown_ha.h
* /opt/cola/permits/1822866827_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-
bz2/sepdk/inc/valleyview_sochap.h
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\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/dev/sep/snbunc\_gt.c

\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/dev/sep/wsmexunc\_imc.c

\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/dev/sep/wsmexunc\_qpi.c

\* /opt/cola/permits/1822866827\_1697521086.3236086/0/20130305-sep3-freebsd-em64t-tar-bz2/sepdk/dev/sep/snbunc\_cbo.c

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Sampling Enabling Product (SEP) Version 3.9 Preview U6

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SEP is a command line tool for doing hardware-based sampling using event-based sampling (EBS). SEP is developed by the PMU Based Collectors (PBC) team, Technical Computing, Analyzers, & Runtimes (TCAR), Software and Services Group (SSG) in Santa Clara, CA.

For more information, please visit

<http://sep.intel.com>

If your questions are not answered on the website, please contact

[sep-support@intel.com](mailto:sep-support@intel.com)

#### What's New in Version 3.9 Preview U6

-----

- 1) Support for Silvermont core
- 2) Support for KNF and KNC core
- 3) Support for KNC uncore
- 4) Support for Valleyview VISA/SoCHAP uncore
- 5) Revised Haswell A0 System Agent VISA/CHAP event configuration files
- 6) Fixed an issue with incorrect LBR data on IA32 platforms
- 7) Fixed sfdump5 dump annotated samples output format
- 8) TB6 files are importable VTune Amplifier XE 2013

#### What's not supported in this version

-----

- 1) TB6 files are no longer importable into VTune Amplifier XE 2011
- 2) Time-based sampling (TBS) is not supported.
- 3) XML files for event listings are not available. The \*\_db.txt files can be used instead.
- 4) Calibration mode is currently not supported. Users should take care not to specify too low of a sample after value for frequently occurring events.
- 5) Pentium 4 and earlier processor families are not supported.
- 6) The "sepreport" viewer tool and "pdiff" utility are no longer supported.
- 7) The Itanium Processor preview release does not include the following features:
  - Memory event scheduling
  - Uncore support
  - Latency events
  - Event multiplexing

#### Supported Hardware

-----

The following Intel processors are supported in this release:

Intel Pentium M processor,



Intel Core processor,  
Intel Core 2 processor,  
Intel Core i3 / i5 / i7 family of processors,  
Intel Xeon processors based on Core microarchitecture family,  
Intel Atom processor,  
Intel Westmere-EX processor  
Intel SandyBridge processor  
Intel Jaketown (Sandybridge-EP) processor  
Intel Xeon Phi / MIC  
Intel Haswell processor  
Intel Silvermont processor

Itanium Processor support:

Poulson  
Montvale  
Montecito  
Tukwila

Chipset support:

Ironlake, Ibexpeak, Lincroft GMCH, Pineview

Supported OS Distributions

-----

1) Linux kernels 2.6.9 through 3.5.x are supported. These include:

- SLES 10 through SP3
- SLES 11 through SP1
- Redhat EL 5 through U6
- Redhat EL 6
- Fedora 9 through 17
- Ubuntu 8.04 through 12.04

NOTE: Itanium2 support has been validated on RH EL 4, RH EL 5, and  
SLES 10 kernels, which use Perfmon2. SEP3 will not work on  
SLES 11 kernels, which use  
Perfmon3.

2) Windows operating systems: Microsoft\* Windows XP Professional Edition,  
Microsoft\* Windows Server 2003, Windows Vista, Windows 2008 Server,  
Windows 7 Enterprise, Windows Server 2008 R2, Windows 8

Known Issues

-----

1) The SEP3 driver is not backward compatible with earlier SEP2-based

products such as VTune 9.x and PTU 3.x.

- 2) Due to compatibility problems, SEP 3.0 Preview 7 and earlier should be completely removed from Windows systems prior to installing this software; this includes removing sepdrv3.sys and sepdal.sys drivers found in the C:\Windows\system32\drivers directory, and using regedit to manually remove "sepdal.sys" entries from the Windows registry.
- 3) On Windows, the VC redistribution package should be installed. The package is available for download from the Microsoft website and also from the sep.intel.com website.

#### Other Instructions

-----

#### \*\*\* Installation and Run-time \*\*\*

For installation and run-time instructions, see the document "SEP\_Install\_Instructions\_Windows.txt" or "SEP\_Install\_Instructions\_Linux.txt" "SEP\_Install\_Instructions\_MIC.txt" (for use on MIC system) located in the "docs" folder.

#### \*\*\* Usage \*\*\*

For SEP usage instructions, see the document "SEP\_Users\_Guide.pdf" For sfdump5 usage instructions, sep\_spec\_addendum.htm located in the "docs" folder.

#### \*\*\* Customer Feedback \*\*\*

To report bugs, usability issues, comments, etc., please send email to sep\_support@intel.com

#### \*\*\* Directory Structure \*\*\*

"bin" - contains SEP binaries  
"docs" - contains the documents  
"include" - contains the header files of SEP  
"sepdk" - contains the driver sources (Linux)

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# 1.474 python 2.7.15

## 1.474.1 Available under license :

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All

Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
| 2.1.3          | 2.1.2        | 2002      | PSF        | yes                 |
| 2.2 and above  | 2.1.1        | 2001-now  | PSF        | yes                 |

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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#### A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <https://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <https://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All

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| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |
| 2.1.2          | 2.1.1        | 2002      | PSF        | yes                 |
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# 1.478 make 2.3

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The

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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

| Release        | Derived from | Year      | Owner      | GPL-compatible? (1) |
|----------------|--------------|-----------|------------|---------------------|
| 0.9.0 thru 1.2 |              | 1991-1995 | CWI        | yes                 |
| 1.3 thru 1.5.2 | 1.2          | 1995-1999 | CNRI       | yes                 |
| 1.6            | 1.5.2        | 2000      | CNRI       | no                  |
| 2.0            | 1.6          | 2000      | BeOpen.com | no                  |
| 1.6.1          | 1.6          | 2001      | CNRI       | yes (2)             |
| 2.1            | 2.0+1.6.1    | 2001      | PSF        | no                  |
| 2.0.1          | 2.0+1.6.1    | 2001      | PSF        | yes                 |
| 2.1.1          | 2.1+2.0.1    | 2001      | PSF        | yes                 |

|       |       |           |     |     |
|-------|-------|-----------|-----|-----|
| 2.2   | 2.1.1 | 2001      | PSF | yes |
| 2.1.2 | 2.1.1 | 2002      | PSF | yes |
| 2.1.3 |       |           |     |     |
| 2.1.2 | 2002  | PSF       | yes |     |
| 2.2.1 | 2.2   | 2002      | PSF | yes |
| 2.2.2 | 2.2.1 | 2002      | PSF | yes |
| 2.2.3 | 2.2.2 | 2003      | PSF | yes |
| 2.3   | 2.2.2 | 2002-2003 | PSF | yes |
| 2.3.1 | 2.3   | 2002-2003 | PSF | yes |
| 2.3.2 | 2.3.1 | 2002-2003 | PSF | yes |
| 2.3.3 | 2.3.2 | 2002-2003 | PSF | yes |
| 2.3.4 | 2.3.3 | 2004      | PSF | yes |
| 2.3.5 | 2.3.4 | 2005      | PSF | yes |
| 2.4   | 2.3   | 2004      | PSF | yes |
| 2.4.1 | 2.4   | 2005      | PSF | yes |
| 2.4.2 | 2.4.1 | 2005      | PSF | yes |
| 2.4.3 | 2.4.2 | 2006      | PSF | yes |
| 2.4.4 | 2.4.3 | 2006      | PSF | yes |
| 2.5   | 2.4   | 2006      | PSF | yes |
| 2.5.1 | 2.5   | 2007      | PSF | yes |
| 2.5.2 | 2.5.1 | 2008      | PSF | yes |
| 2.5.3 | 2.5.2 | 2008      | PSF | yes |
| 2.6   | 2.5   | 2008      | PSF | yes |
| 2.6.1 | 2.6   | 2008      | PSF | yes |

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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1.482 nghttp2 1.8.7

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1.483 xmlsec 1.2.29

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1.484 libsoup 2.55.90

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1.486 python 3.7.9

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for

National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
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1.6.1	1.6	2001	CNRI	yes (2)
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2.1.2	2.1.1	2002	PSF	yes
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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2 and above				
2.1.1	2001-now	PSF	yes	

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#

Names should be added to this file as:

Name <email address>

Raul Silvera <rsilvera@google.com>

Tipp Moseley <tipp@google.com>

Hyoun Kyu Cho <netforce@google.com>

Martin Spier <spiermar@gmail.com>

Taco de Wolff <tacodewolff@gmail.com>

Andrew Hunter <andrewhhunter@gmail.com>

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1.496 cryptography 3.3.2

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1.497 enum34 1.1.10

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1.498 python-certifi 2021.10.8

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ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

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<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

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1.501 python-certifi 2020.12.05

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1.502 jaxb-api 2.2.11

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jar/javax/xml/bind/annotation/XmlSeeAlso.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/DataBindingException.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/JAXB.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/GetPropertyAction.java

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jar/javax/xml/bind/helpers/Messages.properties
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- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/NotIdentifiableEventImpl.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/AbstractUnmarshallerImpl.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/JAXBContext.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/ValidationEventHandler.java
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- /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/DefaultValidationEventHandler.java
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- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/ValidationEvent.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/Unmarshaller.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/ValidationEventLocator.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/ValidationEventLocatorImpl.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/AbstractUnmarshallerImpl.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/DatatypeConverterInterface.java
- *
- /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/Unmarshaller.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/ValidationException.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/util/JAXBResult.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/ContextFinder.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/Validator.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/util/ValidationEventCollector.java
- * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/ParseConversionEventImpl.java

* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/NotIdentifiableEvent.java
*
/opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/JAXBException.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/ValidationEventImpl.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/UnmarshallerHandler.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/Messages.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/util/JAXBSource.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/util/Messages.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/TypeConstraintException.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/PrintConversionEventImpl.java

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 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/annotation/XmlMimeType.java
 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/annotation/XmlList.java
 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/annotation/XmlAccessType.java
 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/annotation/XmlAccessorOrder.java
 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/annotation/XmlAccessorType.java
 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/annotation/XmlAnyElement.java
 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/attachment/AttachmentMarshaller.java
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 /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/attachment/AttachmentUnmarshaller.java
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 * /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/annotation/W3CDomHandler.java
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jar/javax/xml/bind/annotation/XmlInlineBinaryData.java
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jar/javax/xml/bind/annotation/XmlSchemaType.java
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* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-

jar/javax/xml/bind/PrintConversionEvent.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlEnum.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlElement.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlSchema.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlTransient.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlAttribute.java
*
/opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlEnumValue.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/adapters/HexBinaryAdapter.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlRegistry.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlNsForm.java
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-
jar/javax/xml/bind/annotation/XmlValue.java
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* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/helpers/package.html
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/package.html
* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/util/package.html

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* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/DatatypeConverterImpl.java

* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/JAXBPermission.java

* /opt/cola/permits/1833197682_1698356515.304526/0/jaxb-api-2-2-11-sources-5-jar/javax/xml/bind/WhiteSpaceProcessor.java

1.503 libjitterentropy 3.4.0

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```

Found in path(s):

```
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/CommandInfo.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/SecuritySupport.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/DataContentHandlerFactory.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
```

jar/com/sun/activation/registries/MimeTypeFile.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/MimeTypeParameterList.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/MimeTypeParseException.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/ActivationDataFlavor.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/MimeType.java
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jar/com/sun/activation/viewers/ImageViewerCanvas.java
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jar/javax/activation/MimetypesFileTypeMap.java
*
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jar/com/sun/activation/registries/MailcapTokenizer.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/com/sun/activation/registries/MailcapParseException.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/UnsupportedDataTypeException.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/com/sun/activation/registries/MimeTypeEntry.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/com/sun/activation/registries/MailcapFile.java
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/URLDataSource.java
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* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/CommandMap.java
*
/opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-
jar/javax/activation/FileTypeMap.java
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@(#)package.html 1.2 07/05/14

-->

Found

in path(s):
* /opt/cola/permits/1932116864_1710257017.0478957/0/activation-1-1-1-sources-10-jar/javax/activation/package.html

1.510 istack-commons 3.0.5

1.510.1 Available under license :

No license file was found, but licenses were detected in source scan.

Manifest-Version: 1.0
Bundle-Description: istack common utility code
Implementation-Title: istack common utility code runtime
Bundle-License: https://glassfish.java.net/public/CDDL+GPL_1_1.html, https://glassfish.java.net/public/CDDL+GPL_1_1.html
Bundle-SymbolicName: com.sun.istack.commons-runtime
Implementation-Version: 3.0.5
Built-By: build
Bnd-LastModified: 1486772850426
Bundle-ManifestVersion: 2
Implementation-Vendor-Id: com.sun.istack
Bundle-DocURL: <http://www.oracle.com/>
Bundle-Vendor: Oracle Corporation
Import-Package: javax.activation,javax.xml.namespace,javax.xml.stream,org.xml.sax,org.xml.sax.helpers
Require-Capability: osgi.ee;filter="(&(osgi.ee=JavaSE)(version=1.7))"
Tool: Bnd-3.2.0.201605172007
Implementation-Vendor: Oracle Corporation
Export-Package: com.sun.istack;uses="javax.activation,javax.xml.stream,org.xml.sax,org.xml.sax.helpers";version="3.0.5",com.sun.istack.logging;version="3.0.5",com.sun.istack.localization;version="3.0.5"
Bundle-Name:
istack common utility code runtime
Bundle-Version: 3.0.5
Implementation-Build-Id: tags/istack-commons-3.0.5-321, 2017-02-11T01:27:24+0100
Created-By: Apache Maven Bundle Plugin
Build-Jdk: 1.8.0_112

Found in path(s):
* /opt/cola/permits/1932130854_1710257404.4748392/0/istack-commons-runtime-3-0-5-jar/META-INF/MANIFEST.MF

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Found in path(s):

* `/opt/cola/permits/1932130854_1710257404.4748392/0/istack-commons-runtime-3-0-5-jar/META-INF/maven/com.sun.istack/istack-commons-runtime/pom.xml`

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